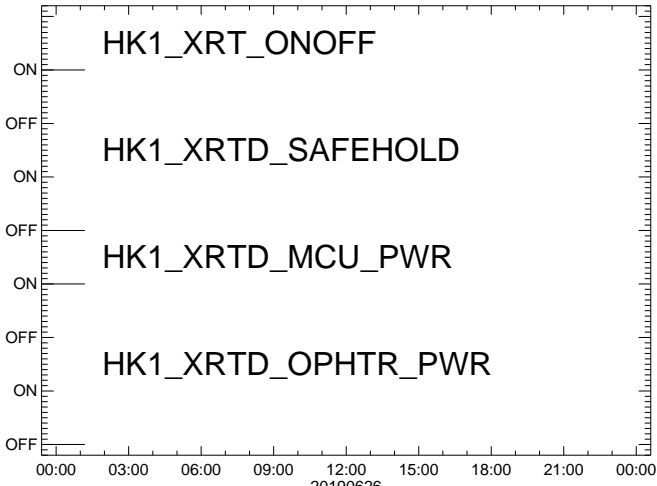
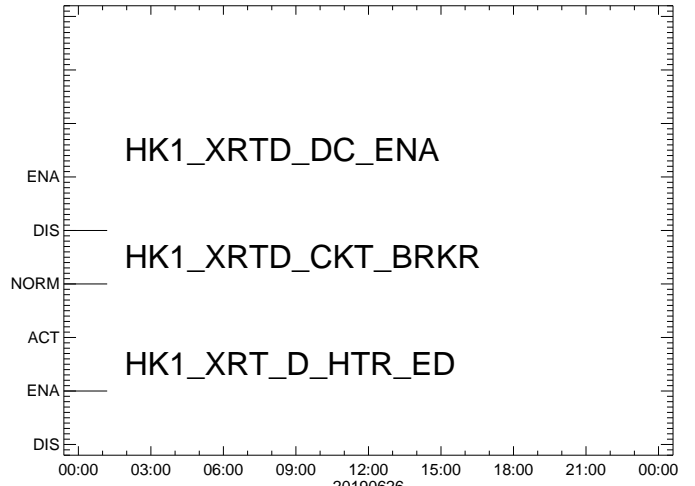


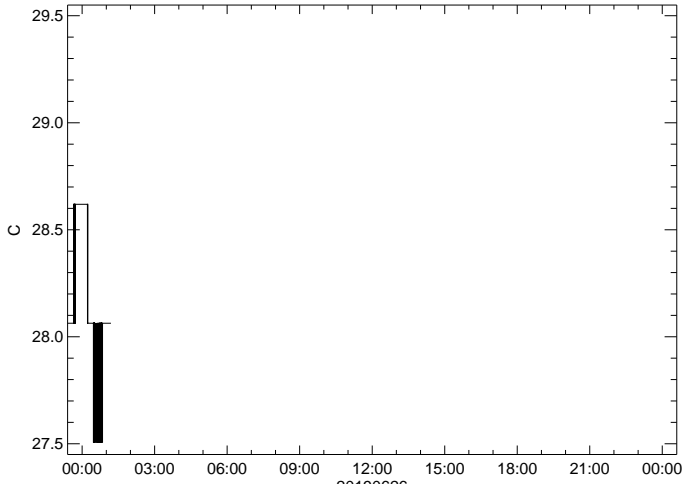
ON/OFF Statuses



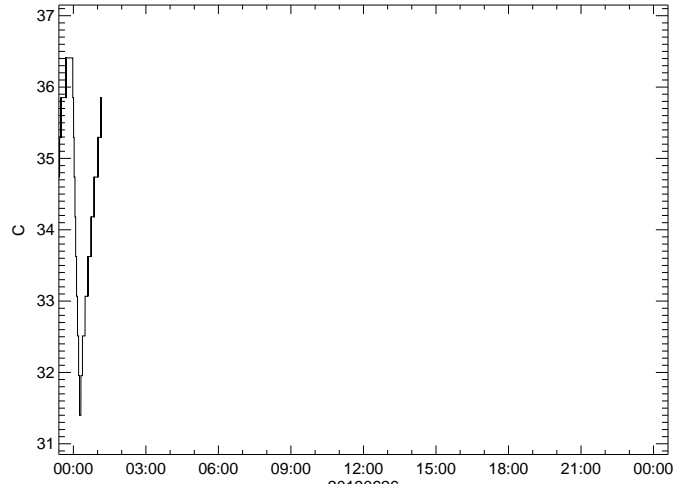
ON/OFF Statuses



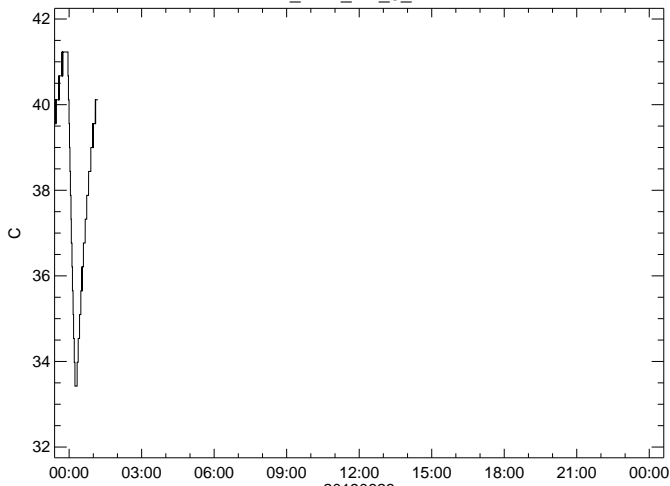
20190626
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HK1_XRT_DT_1_TEMP



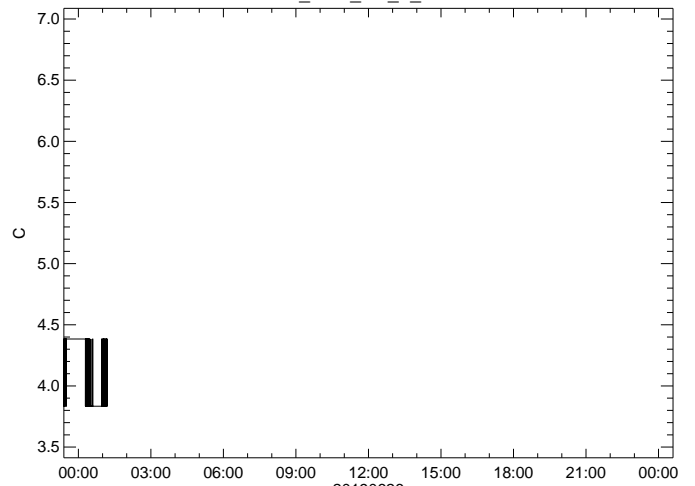
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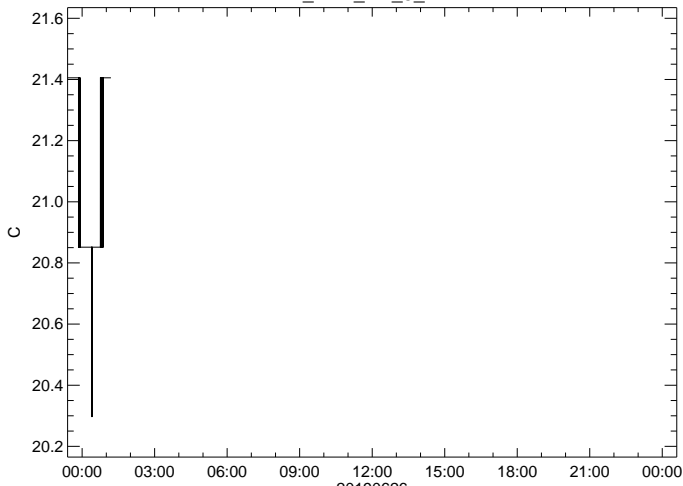
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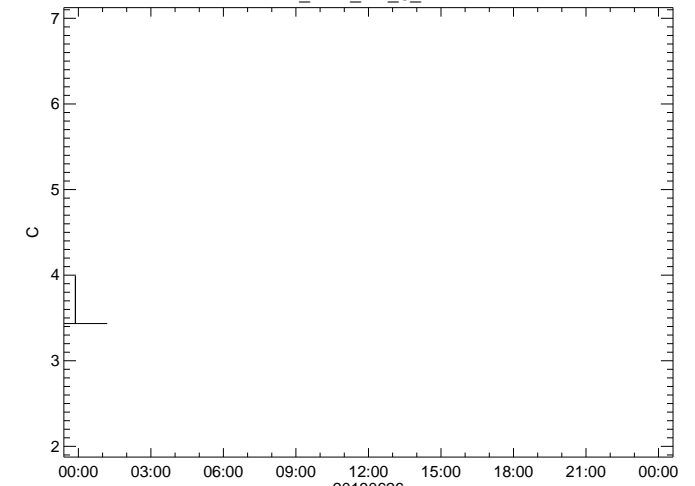
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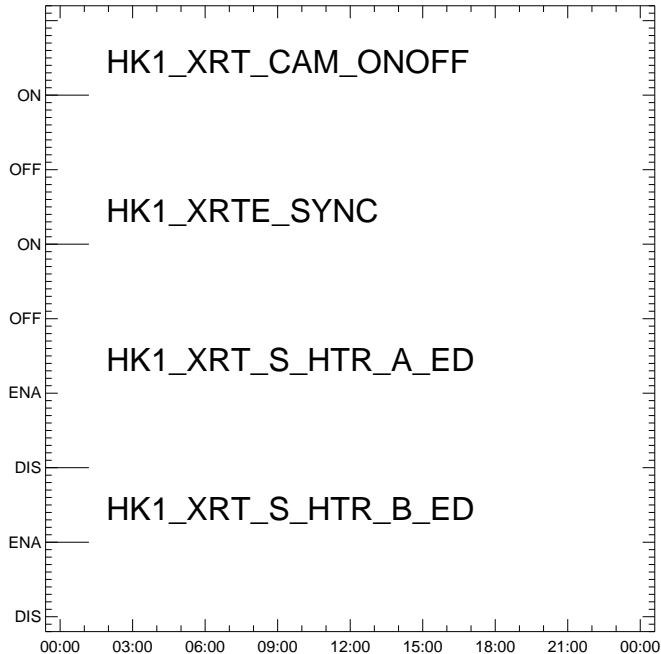
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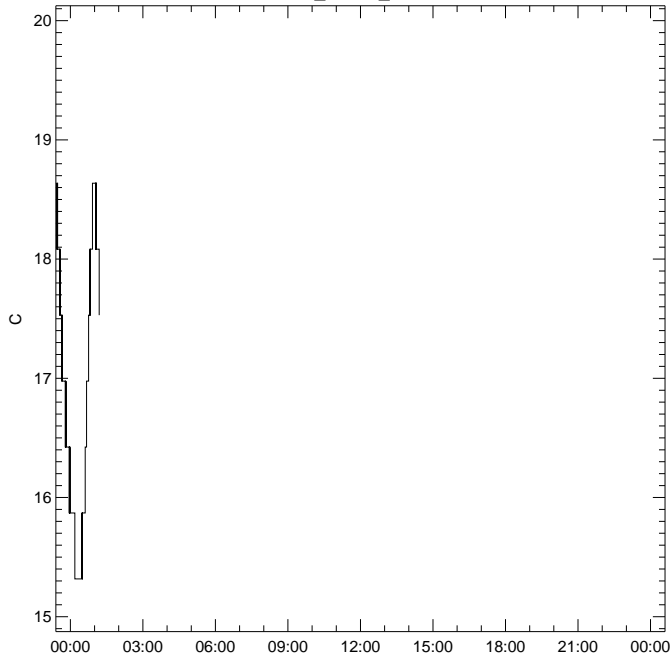
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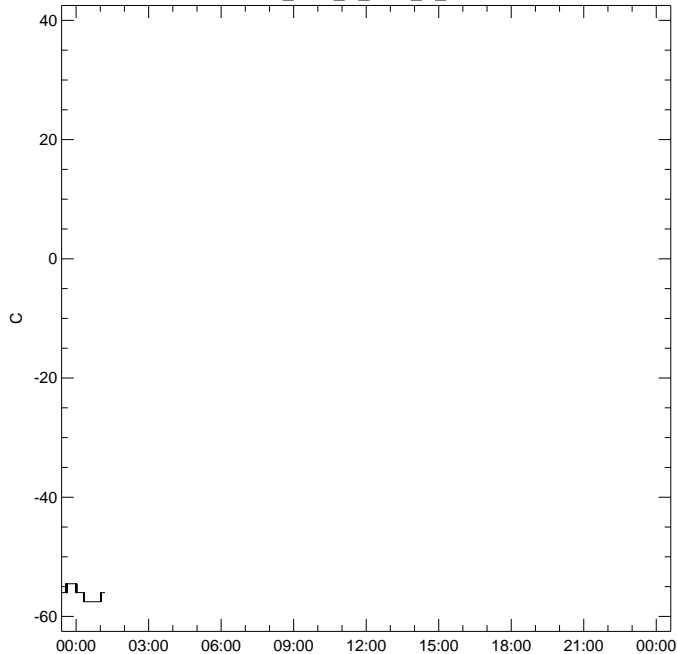
ON/OFF Statuses



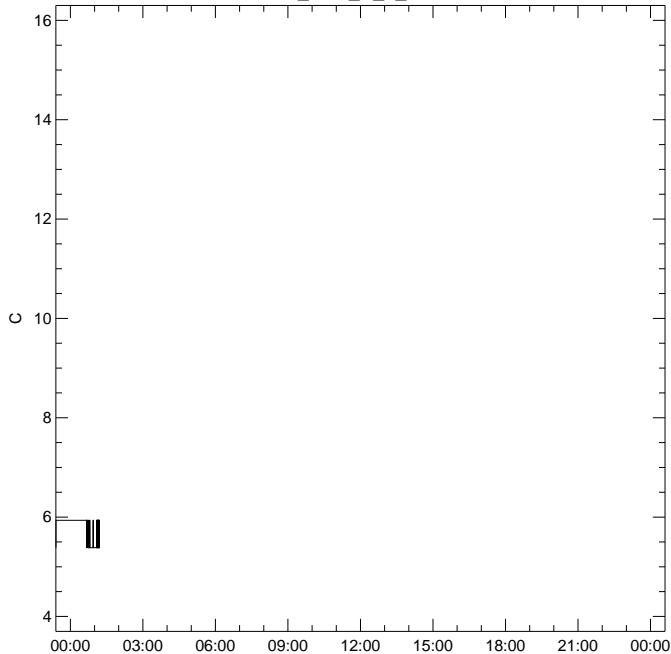
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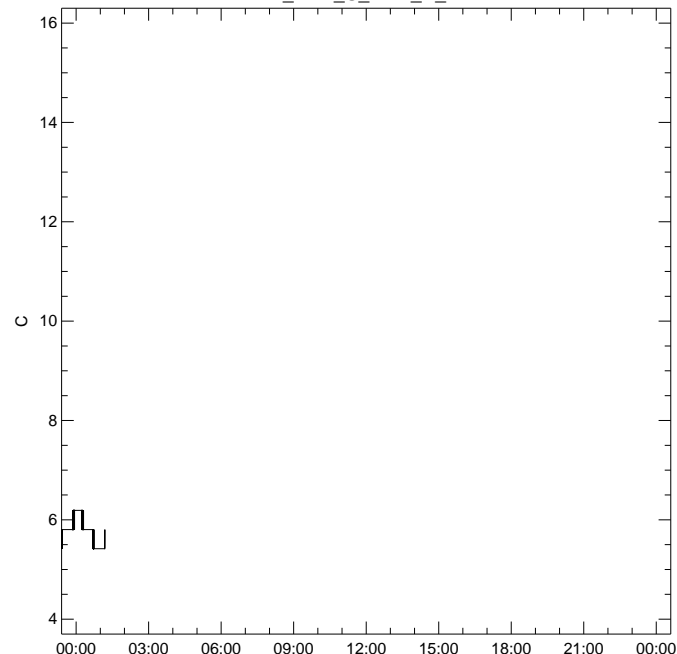
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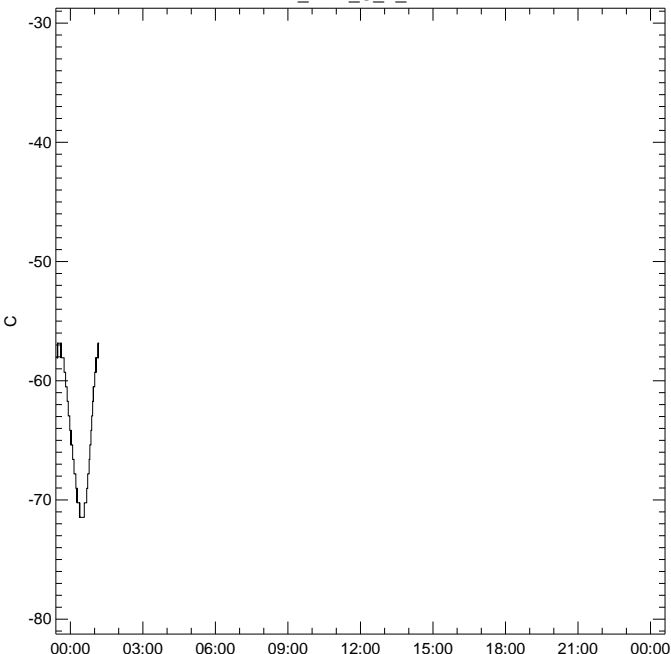
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XRT_TEMP_DL Ver 2008/02/28
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20190626
XRT_TEMP_DL Ver 2008/02/28
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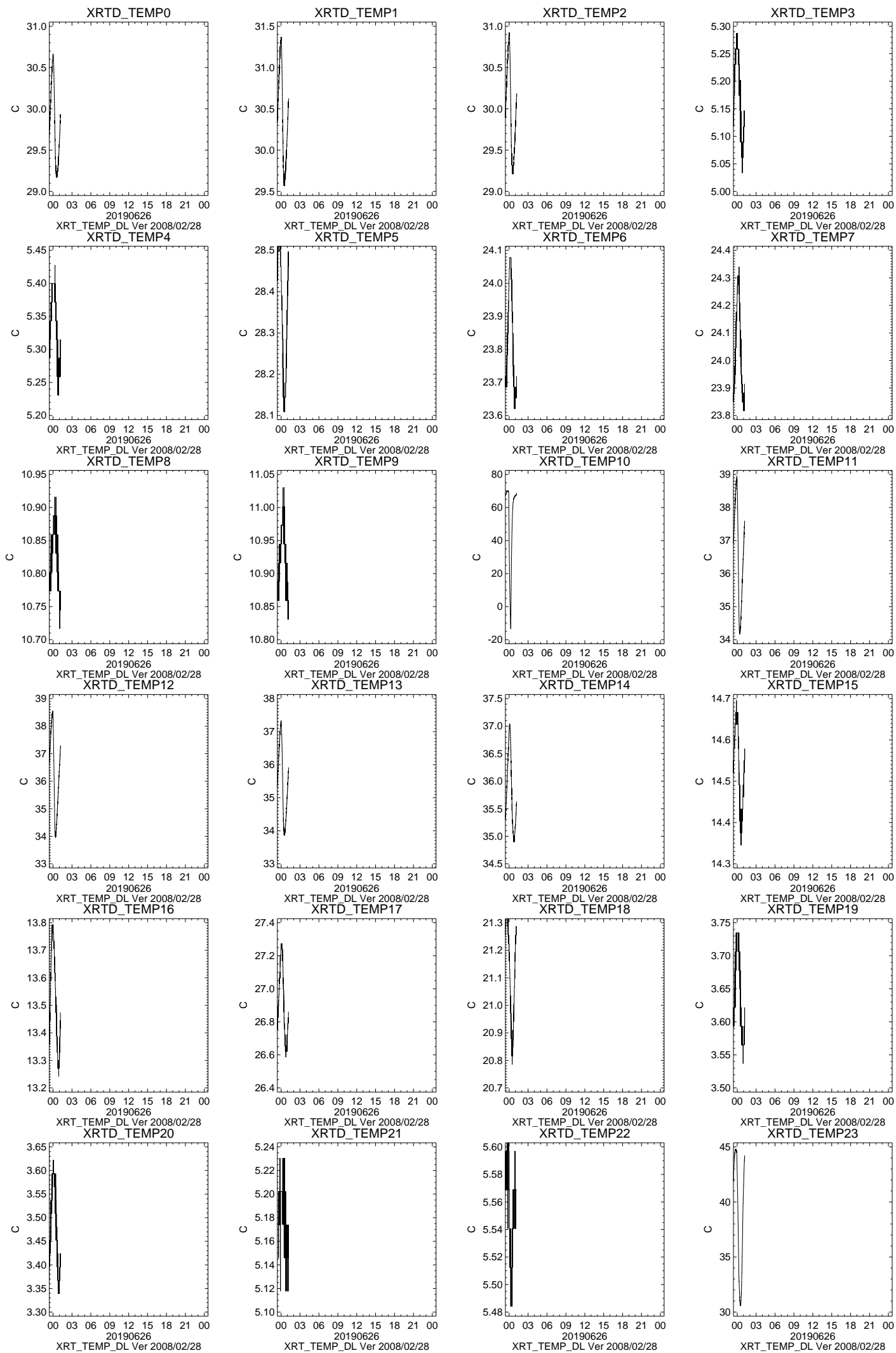


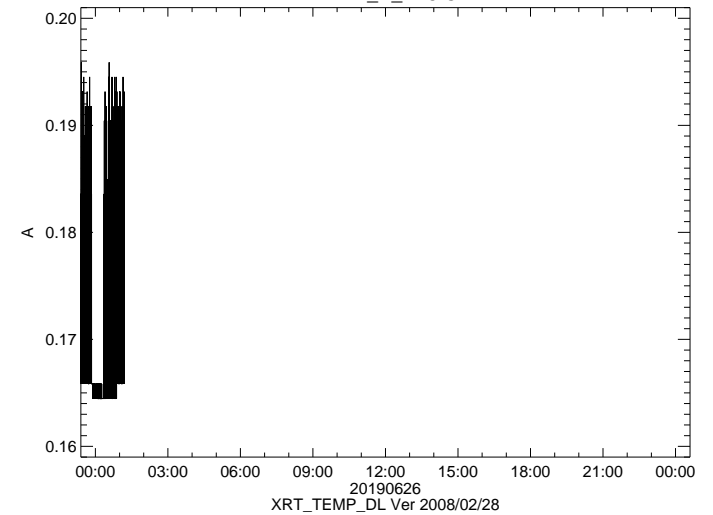
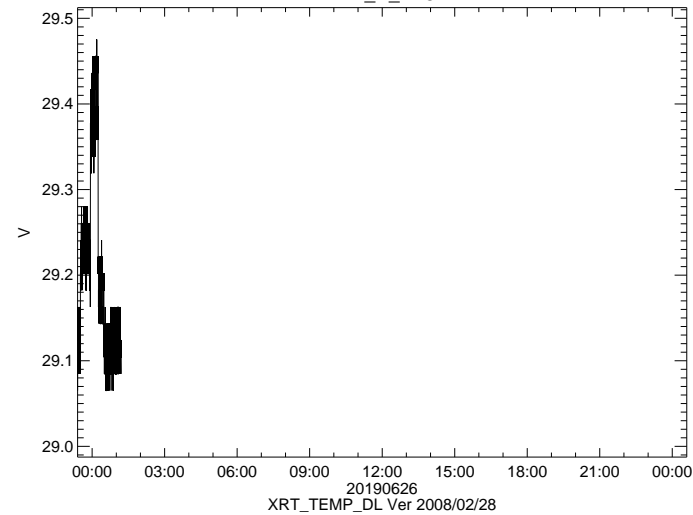
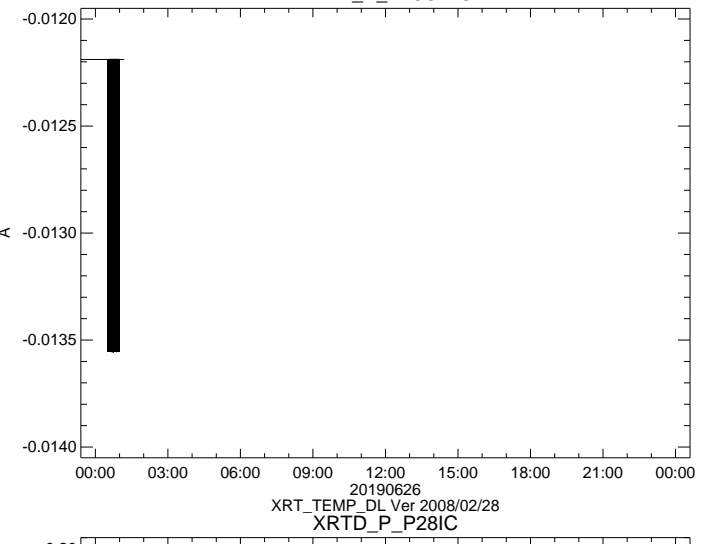
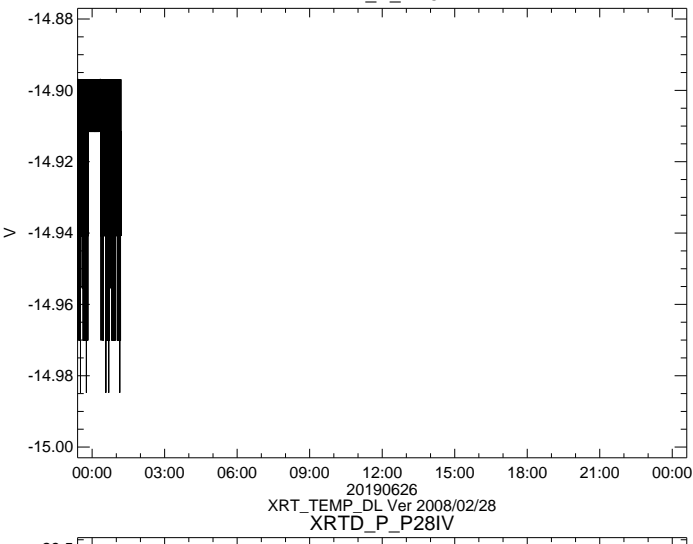
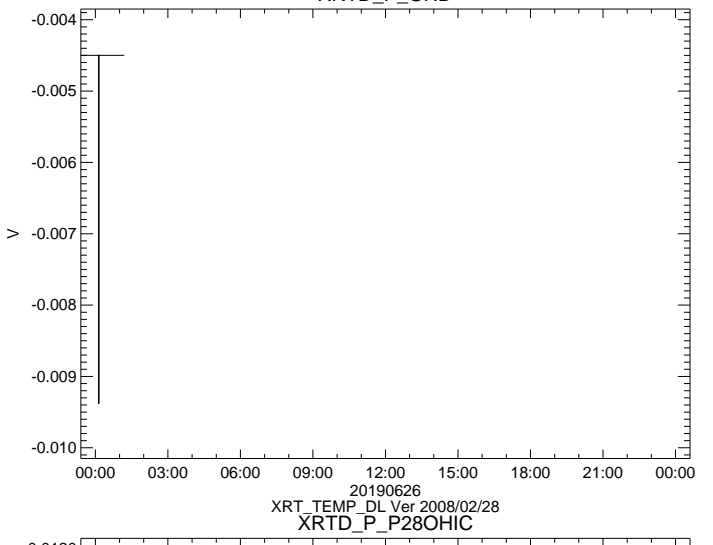
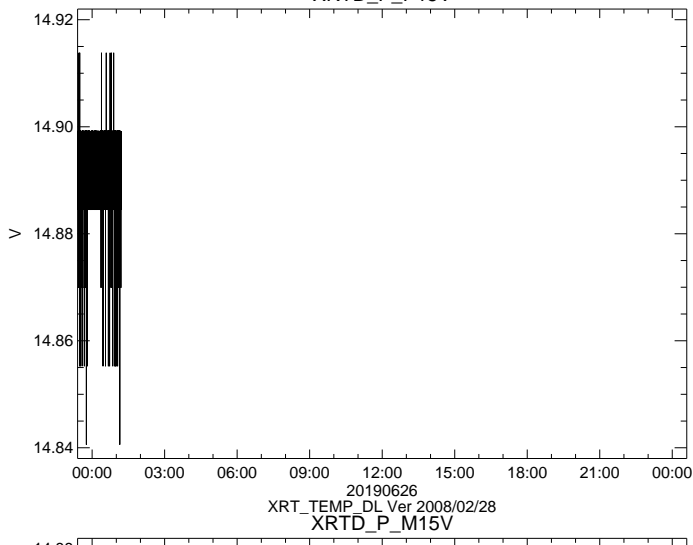
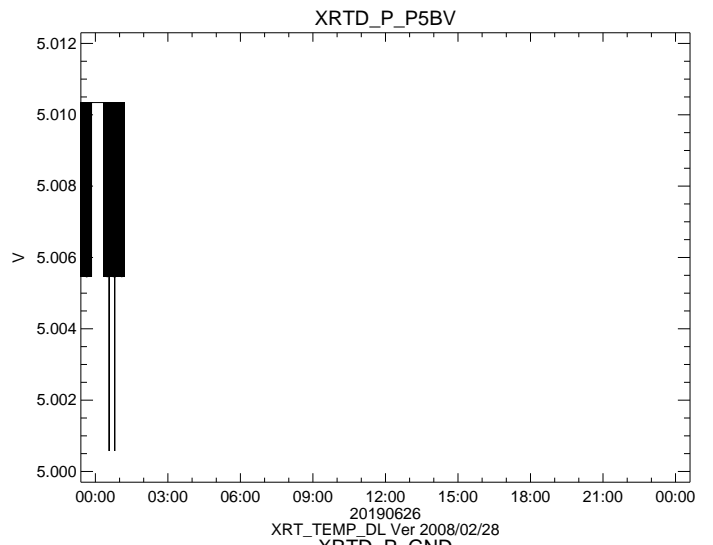
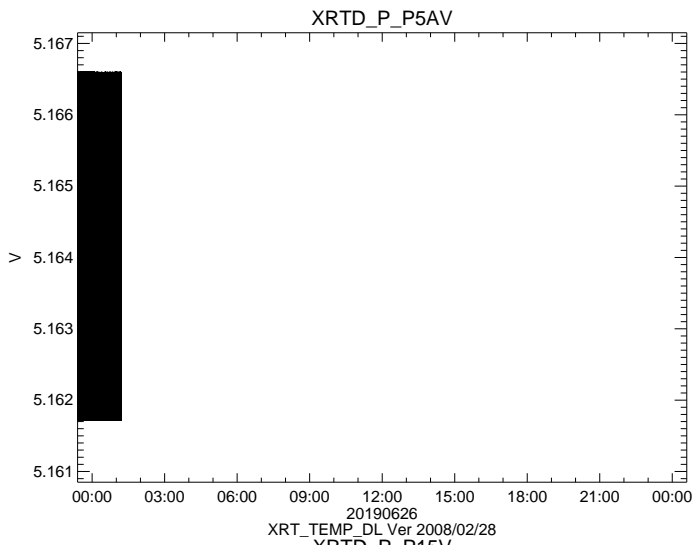
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XRT_TEMP_DL Ver 2008/02/28
HK1_XRT_S_2_TEMP

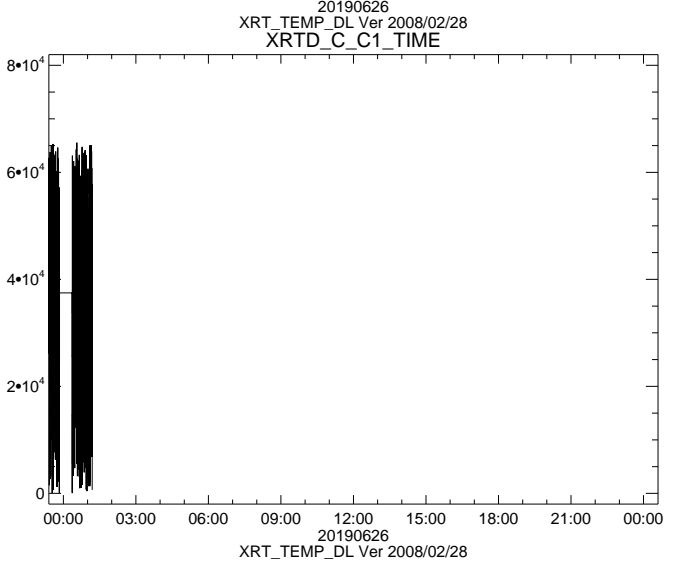
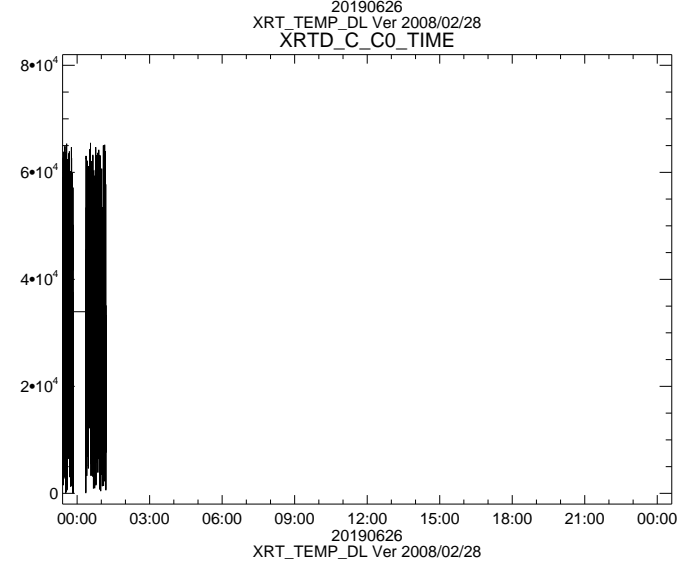
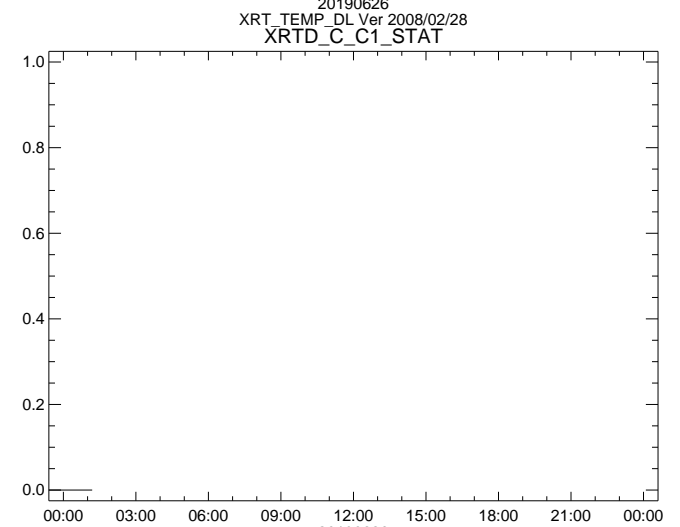
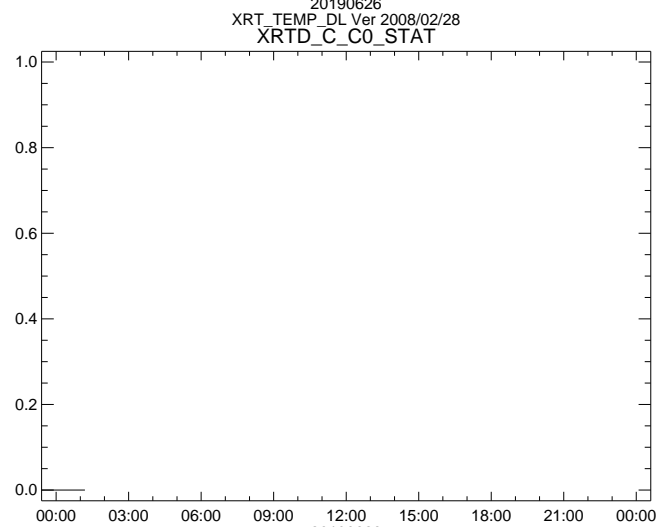
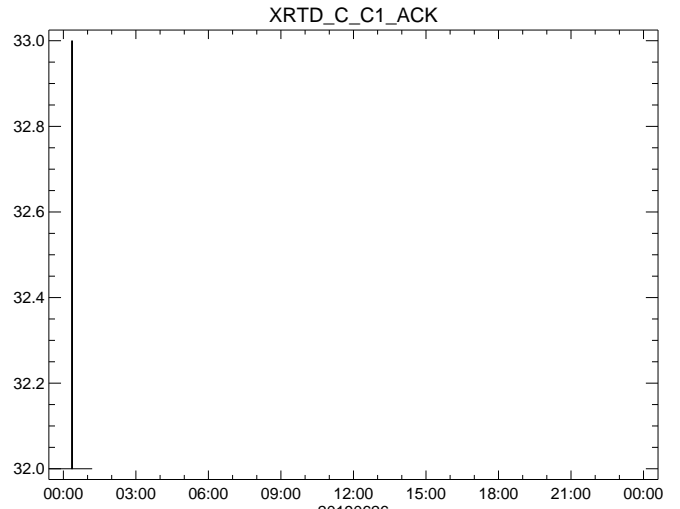
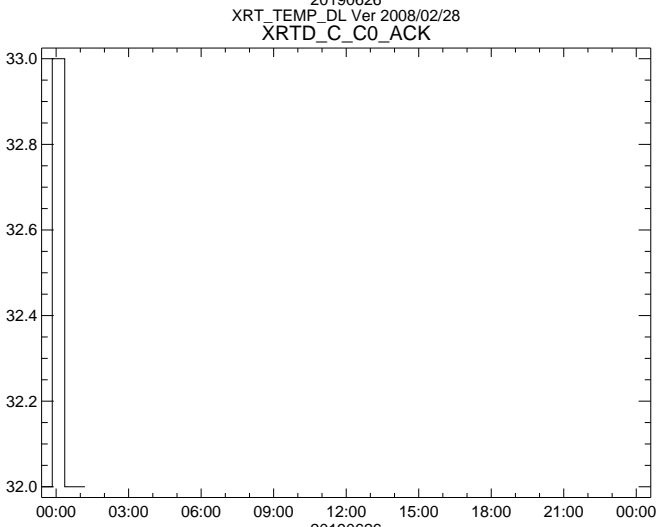
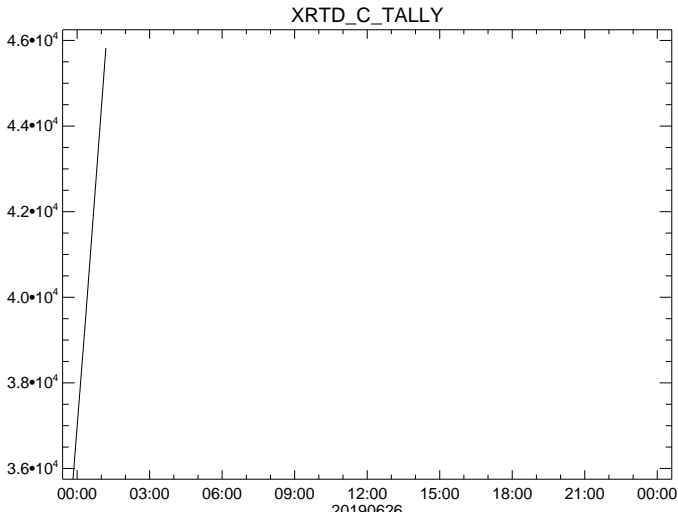


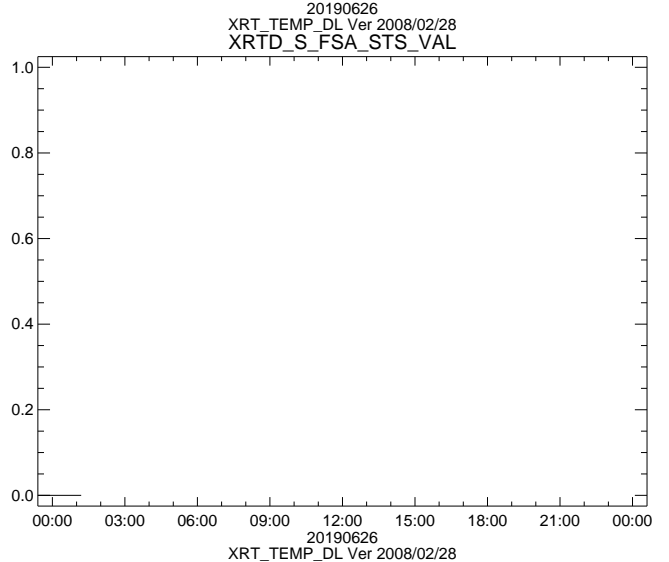
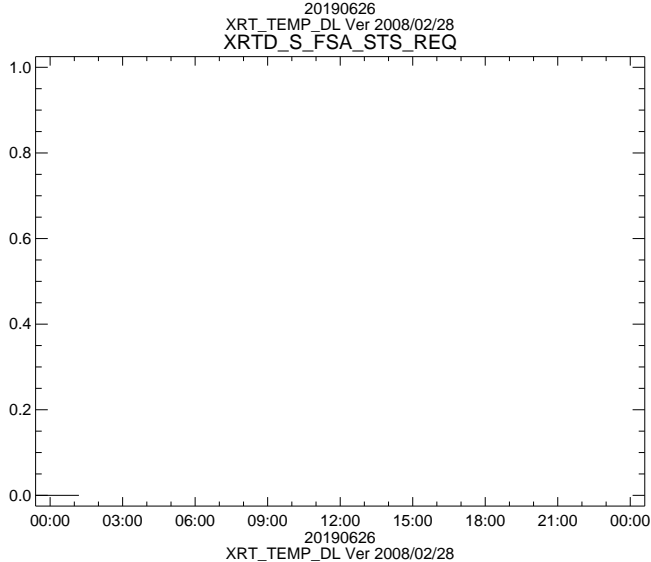
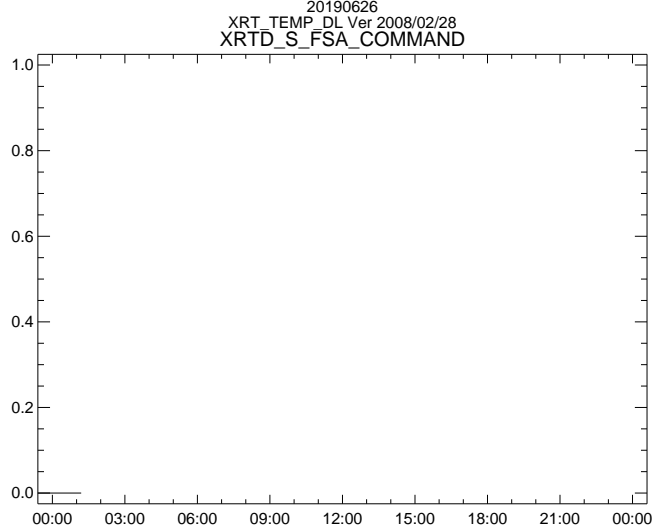
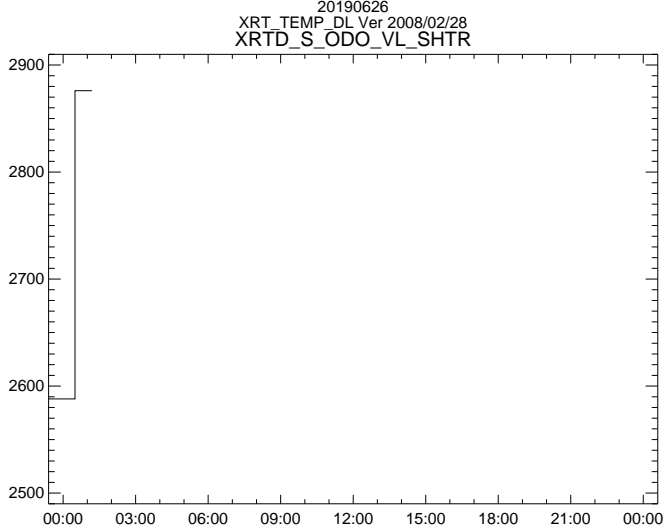
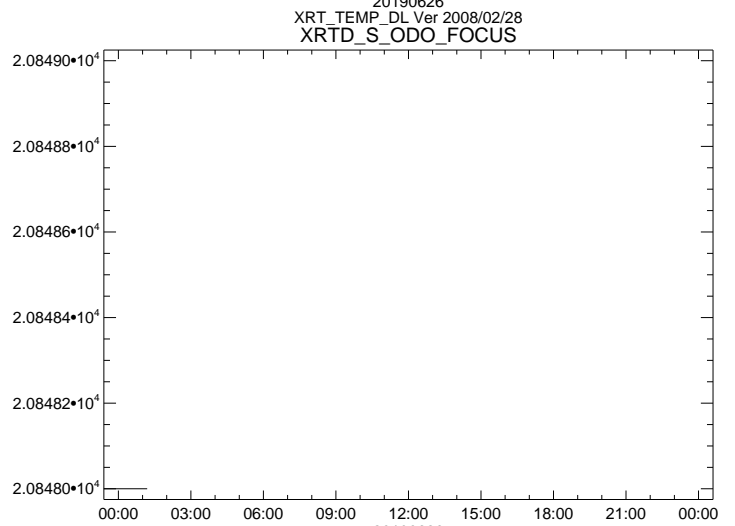
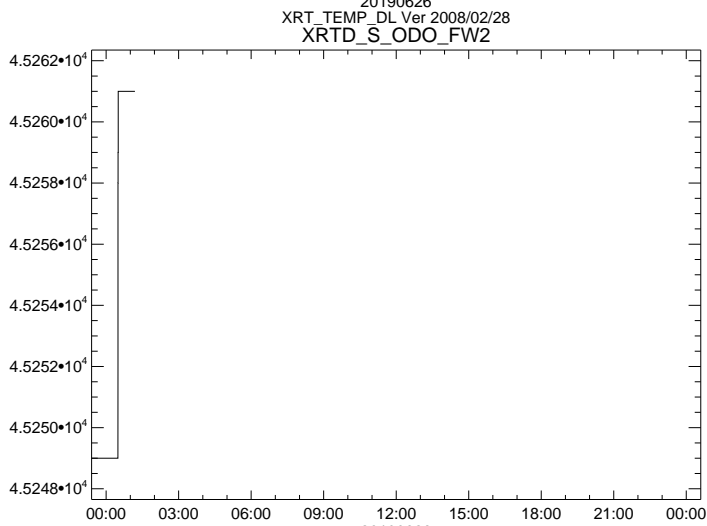
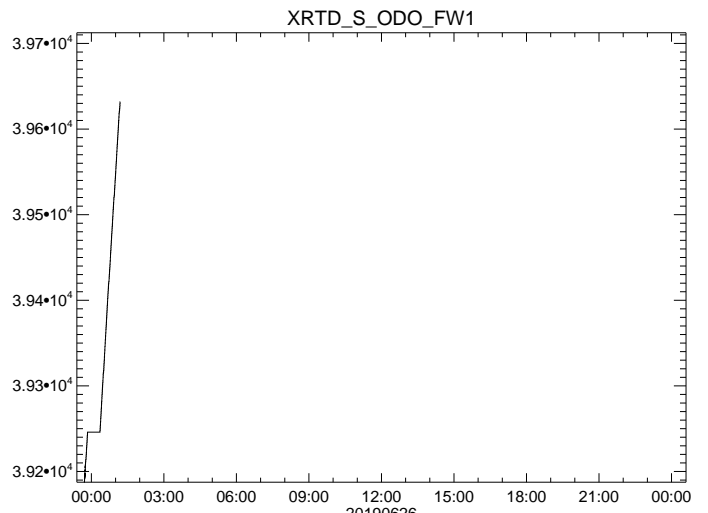
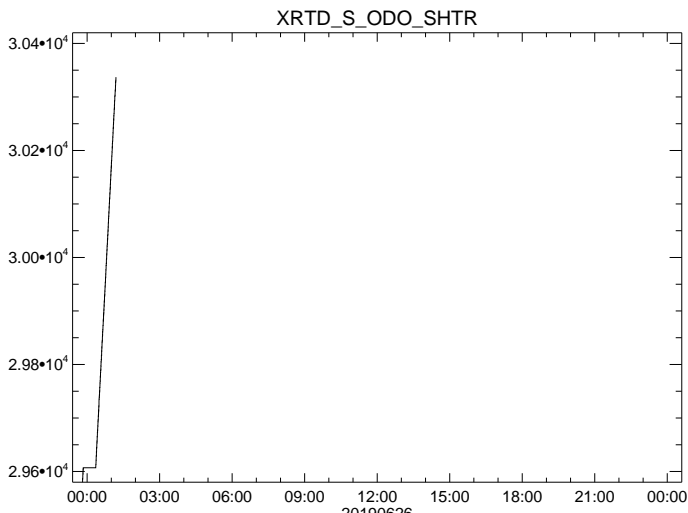
20190626
XRT_TEMP_DL Ver 2008/02/28

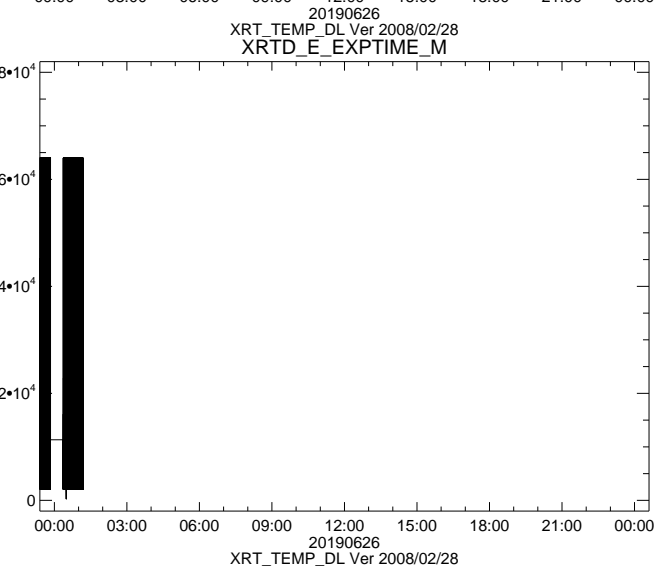
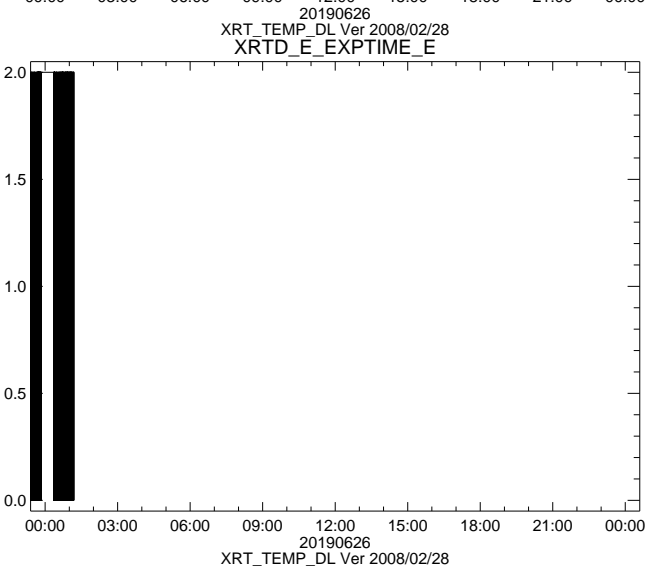
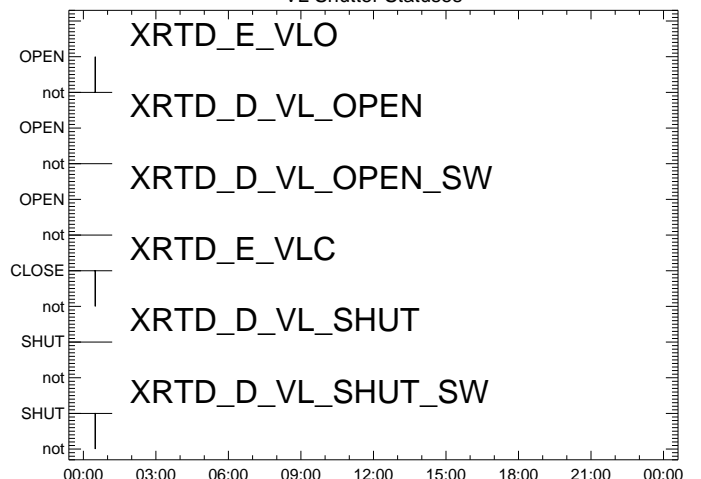
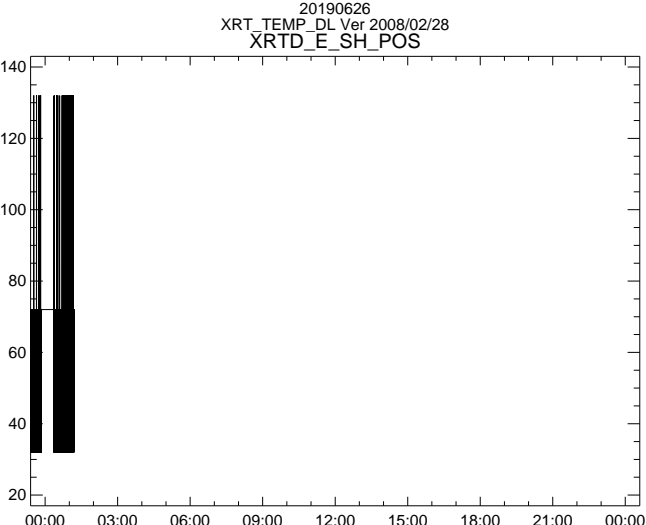
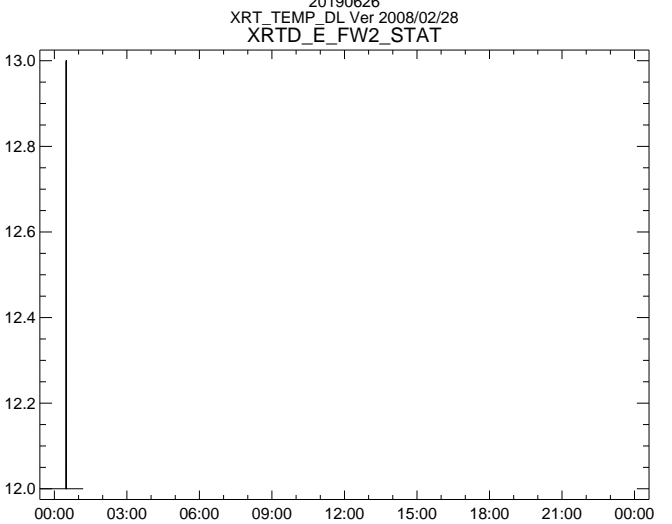
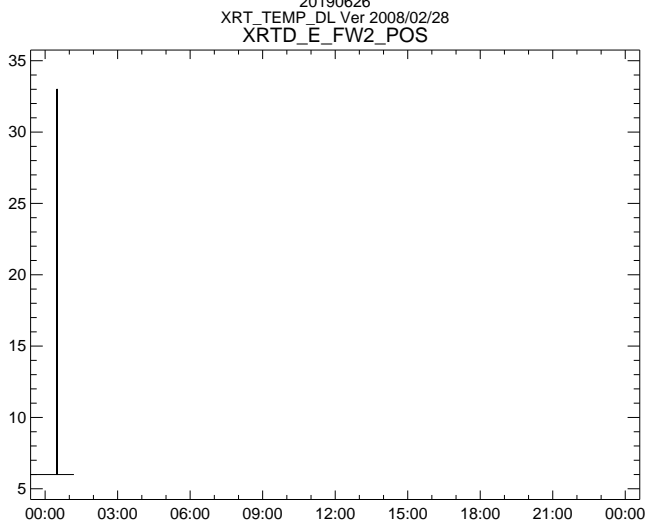
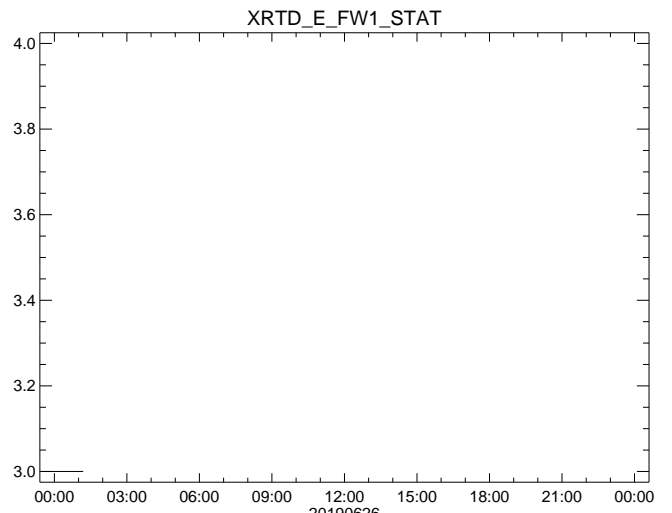
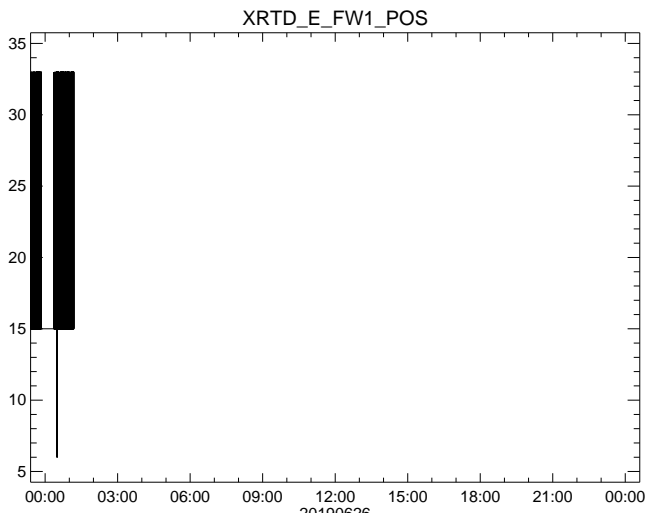
20190626
XRT_TEMP_DL Ver 2008/02/28



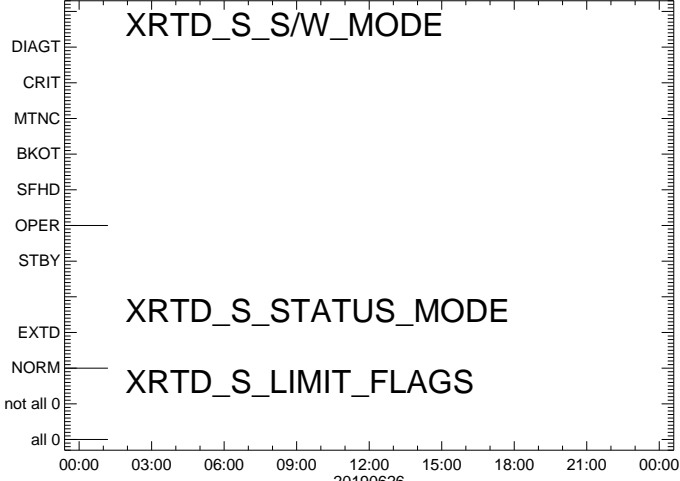




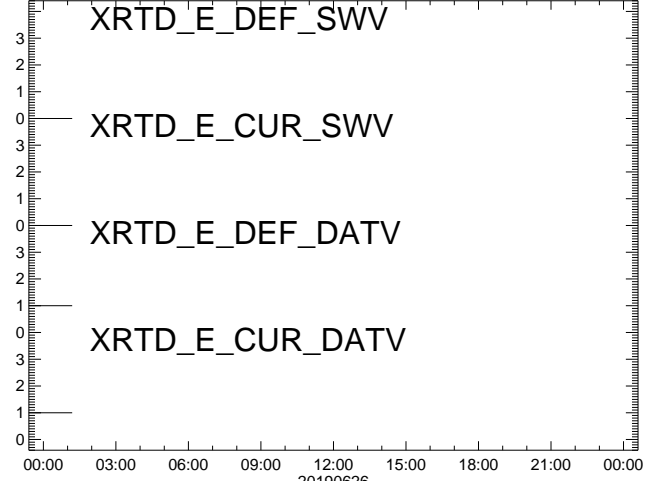




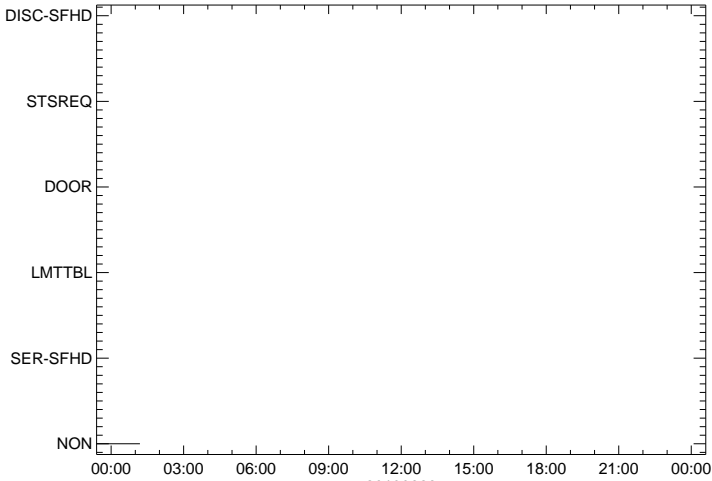
Software Statuses



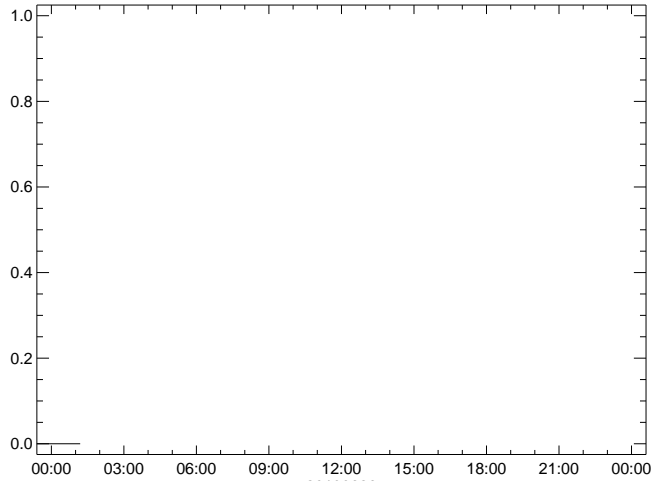
SW/DATA Version



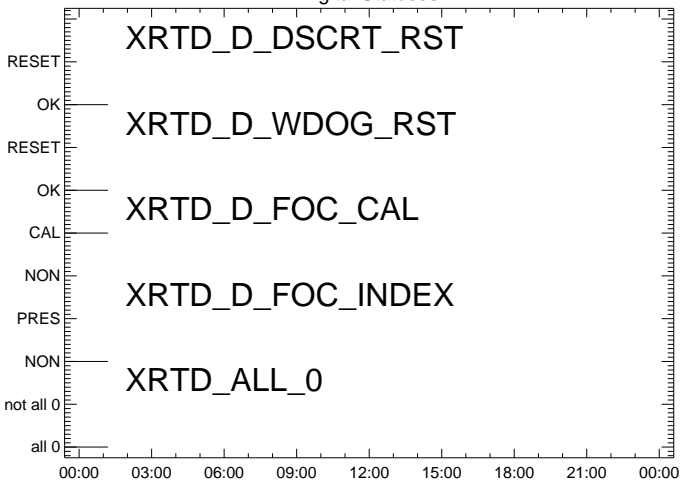
20190626
XRT_TEMP_DL Ver 2008/02/28
XRTD_S_SFHD_CAUSE



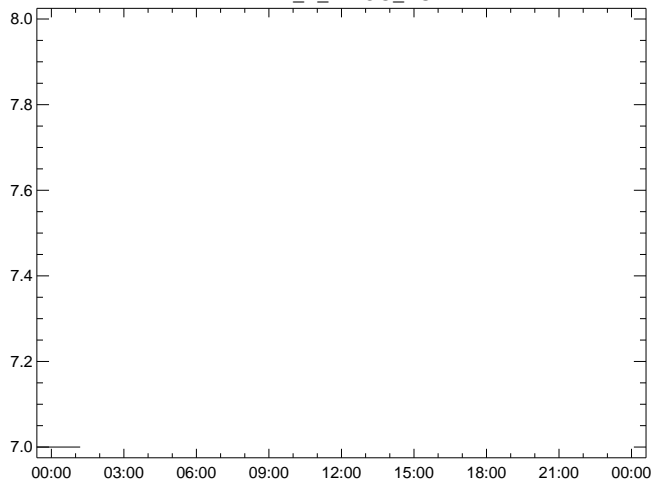
20190626
XRT_TEMP_DL Ver 2008/02/28
XRTD_E_DSCRT_RST



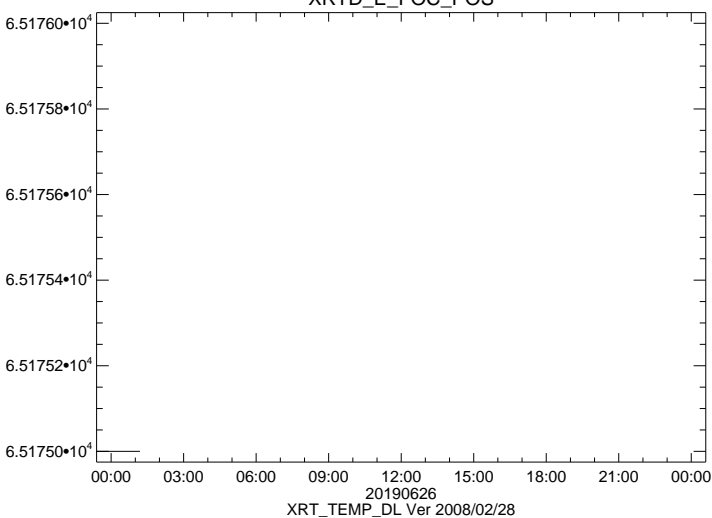
20190626
XRT_TEMP_DL Ver 2008/02/28
Digital Statuses



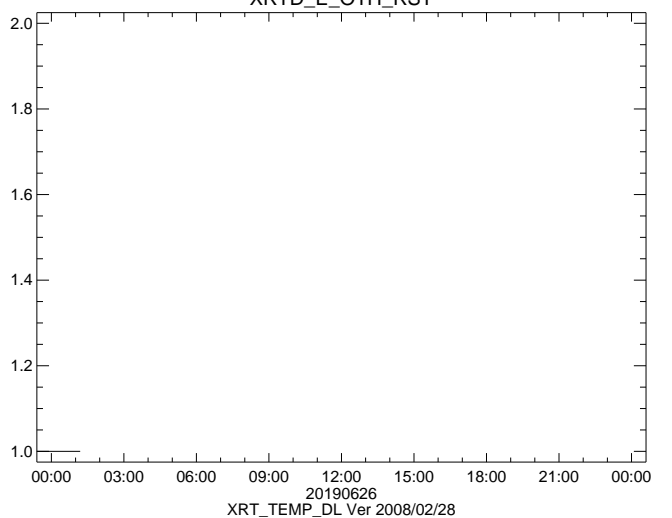
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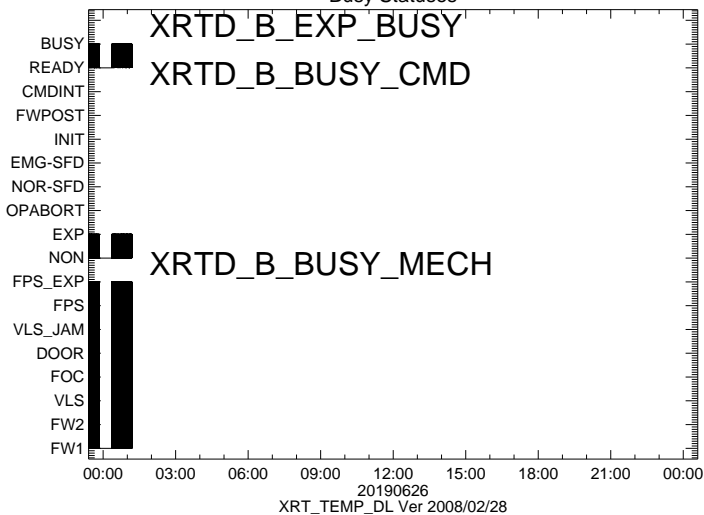
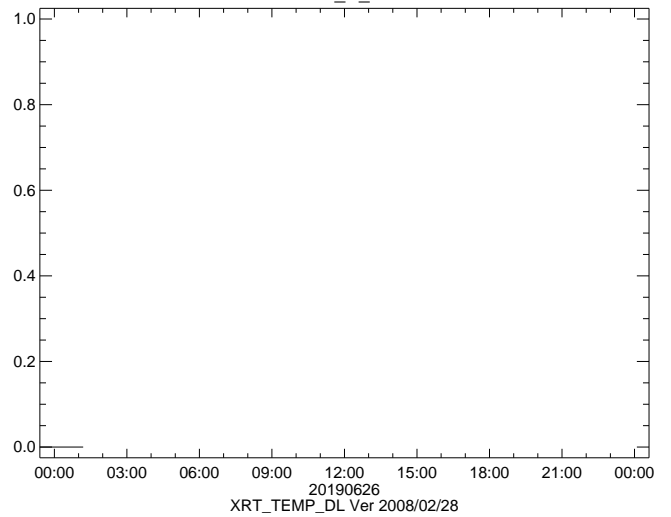
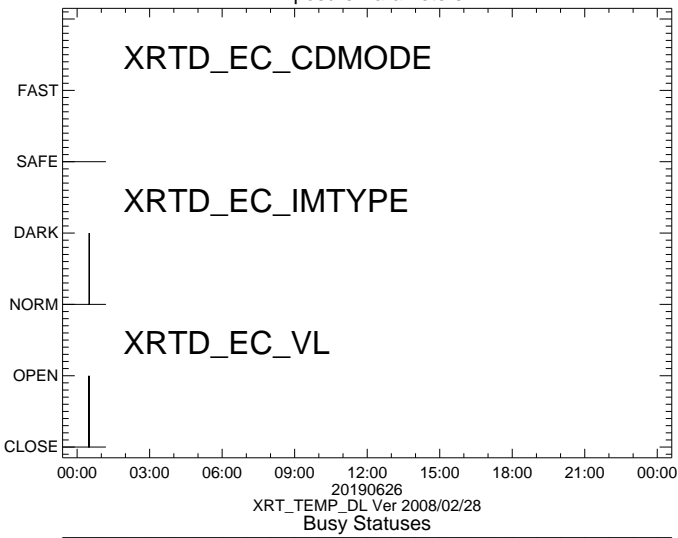
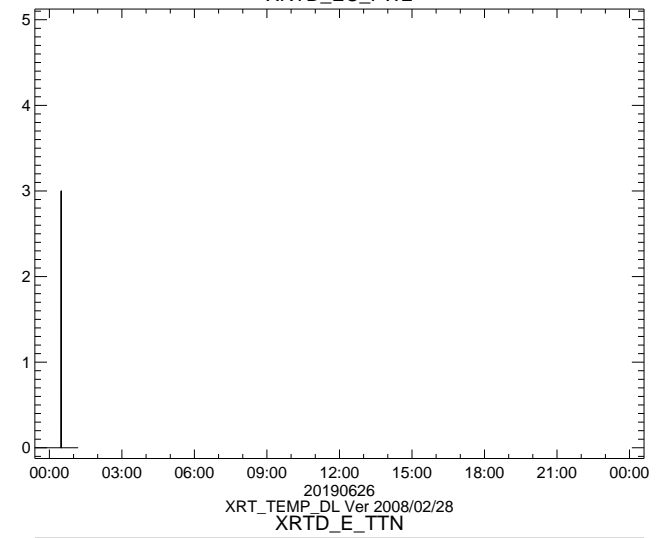
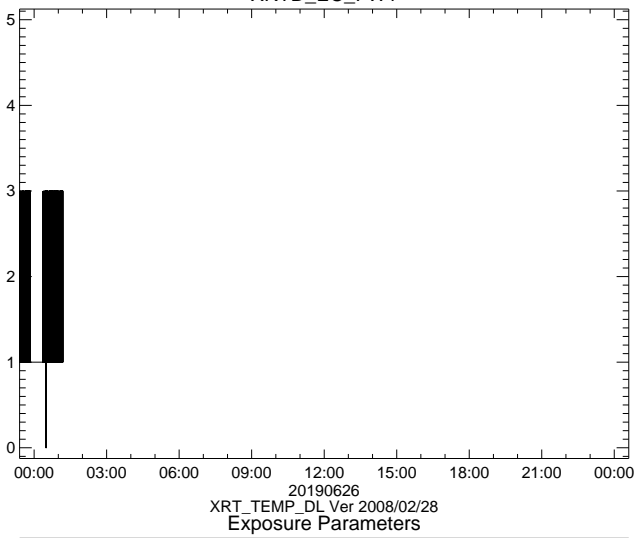
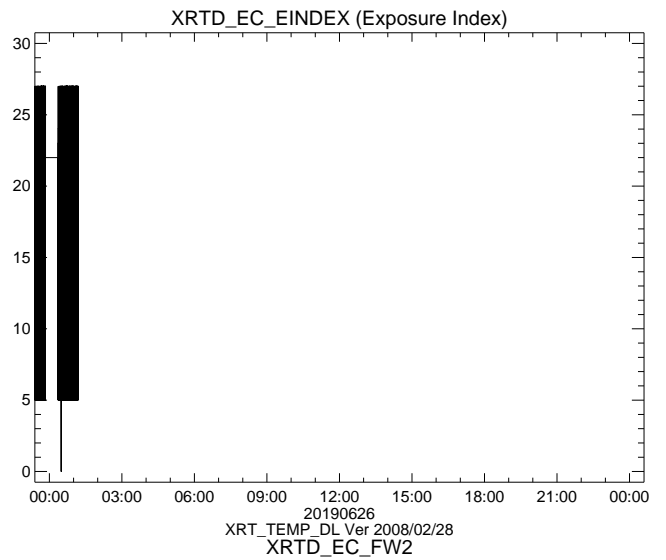
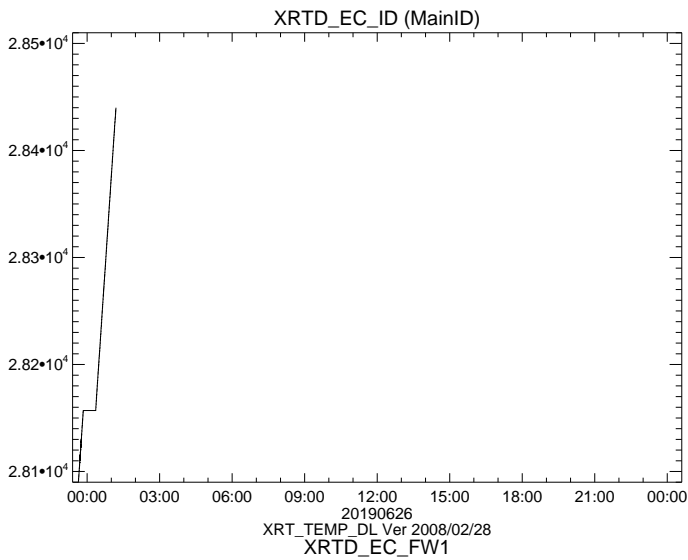


20190626
XRT_TEMP_DL Ver 2008/02/28
XRTD_E_FOC_POS

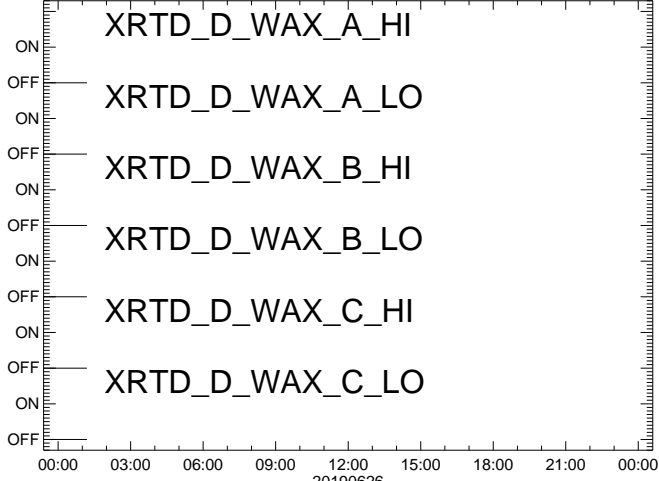


20190626
XRT_TEMP_DL Ver 2008/02/28
XRTD_E_OTH_RST

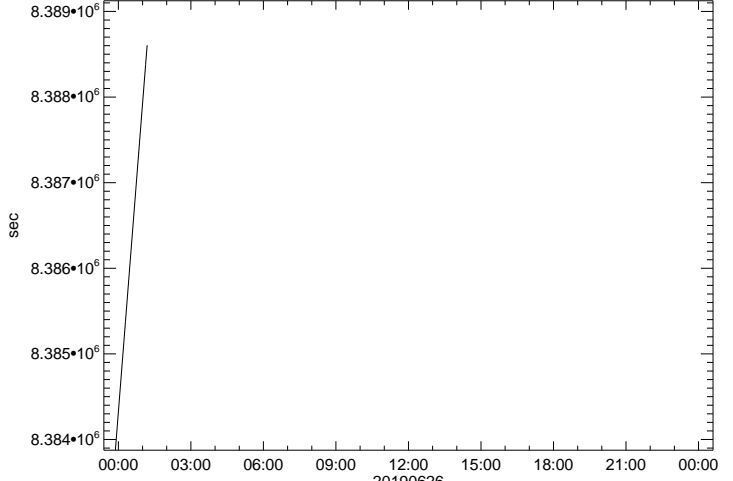




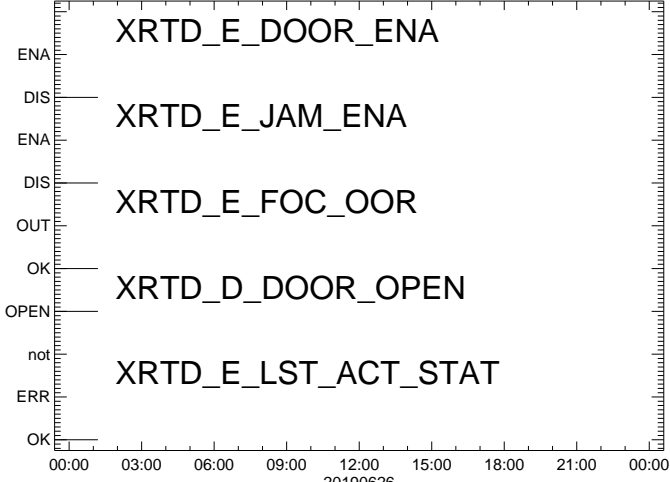
WAX Actuator Statuses



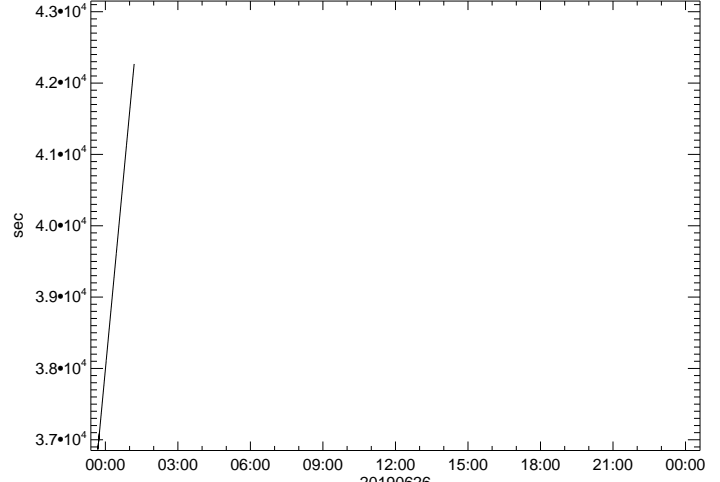
XRTD_T_SC_CLOCK



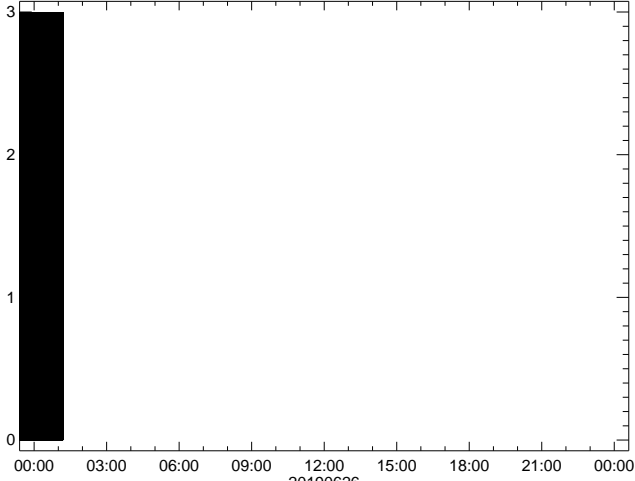
20190626
XRT_TEMP_DL Ver 2008/02/28
E Statuses



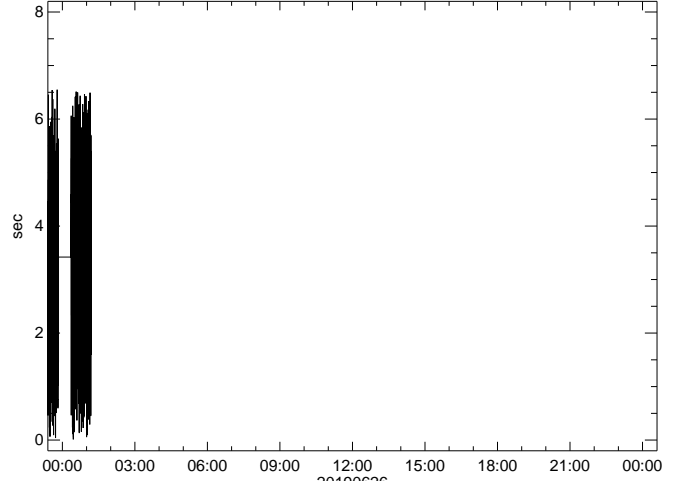
20190626
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XRTD_T_LO_CLOCK



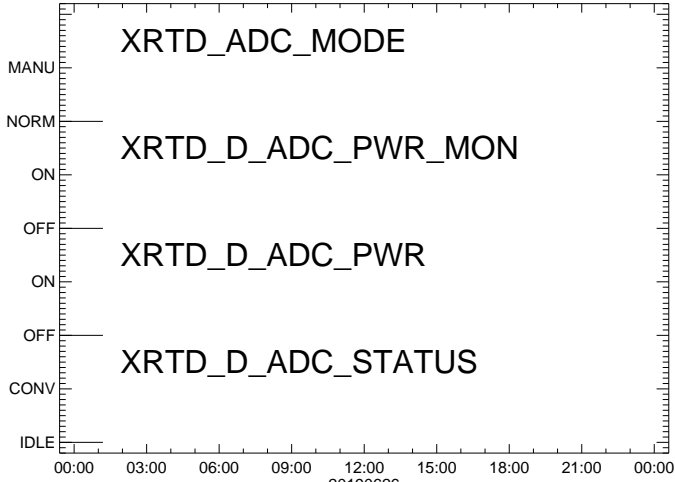
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XRTD_TEMP_SENS_GROUP



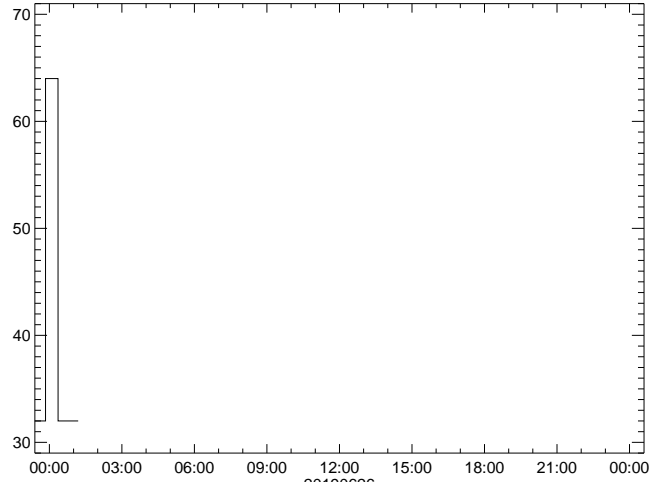
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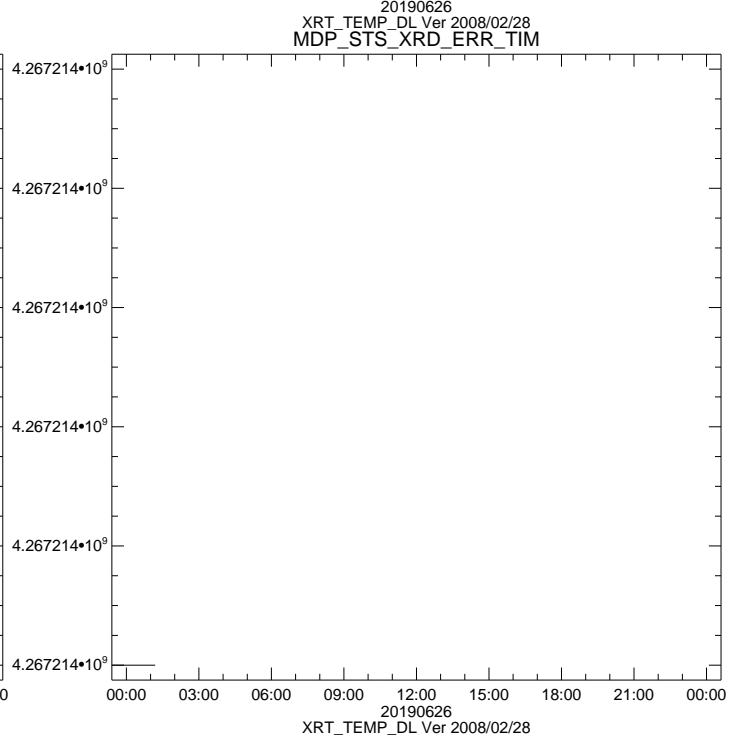
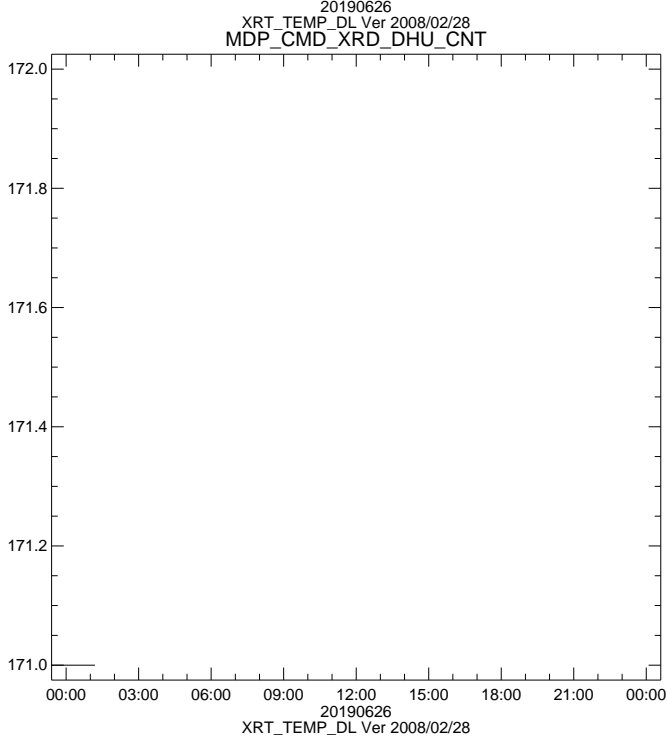
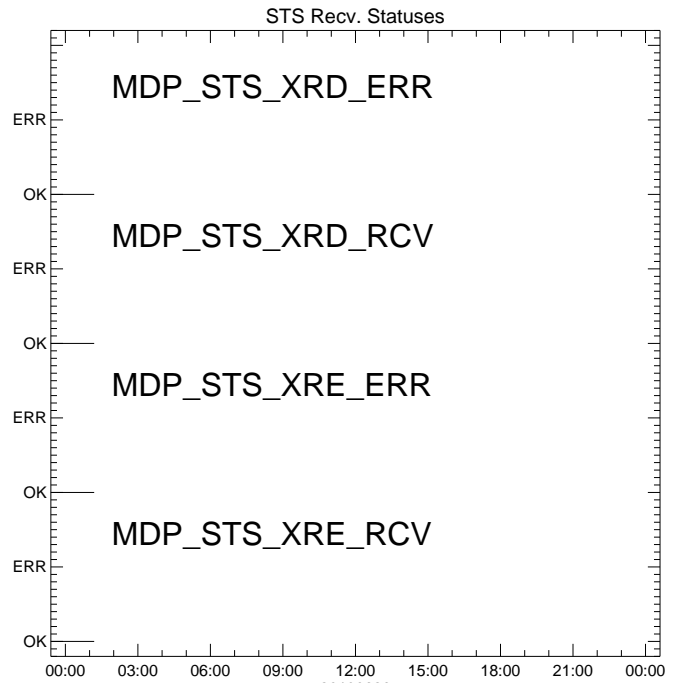
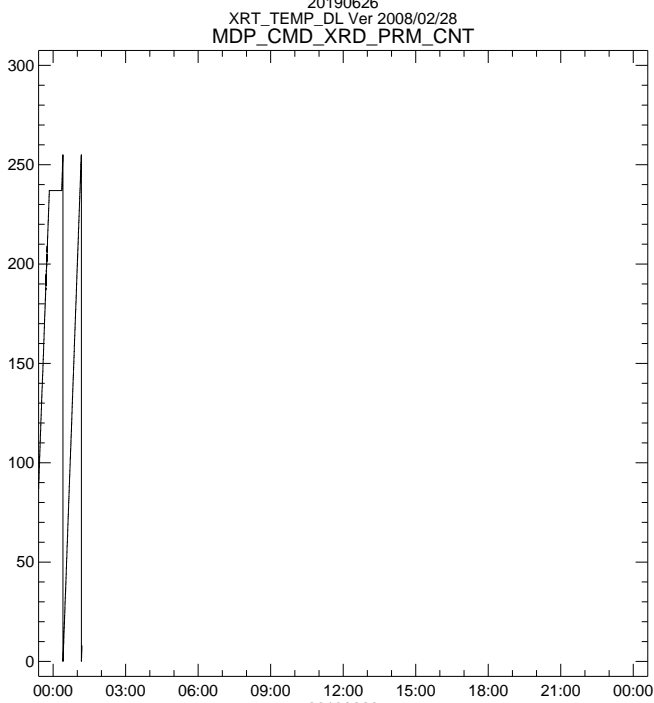
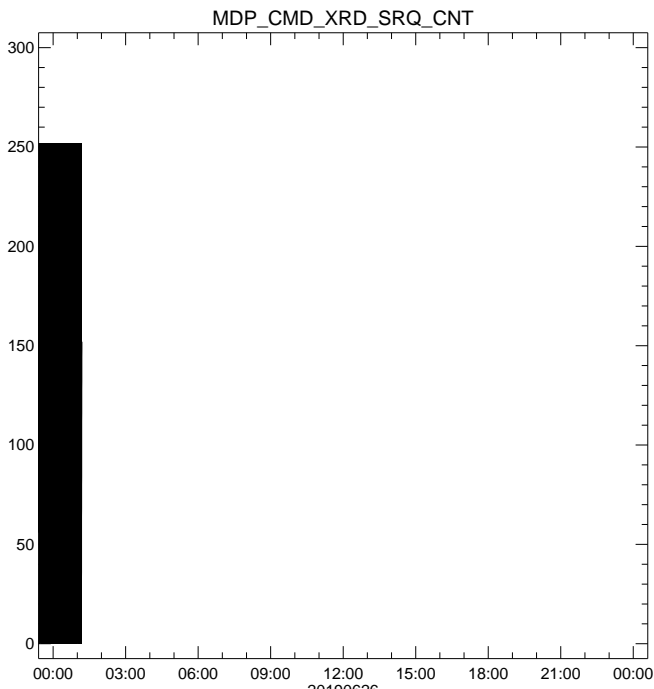


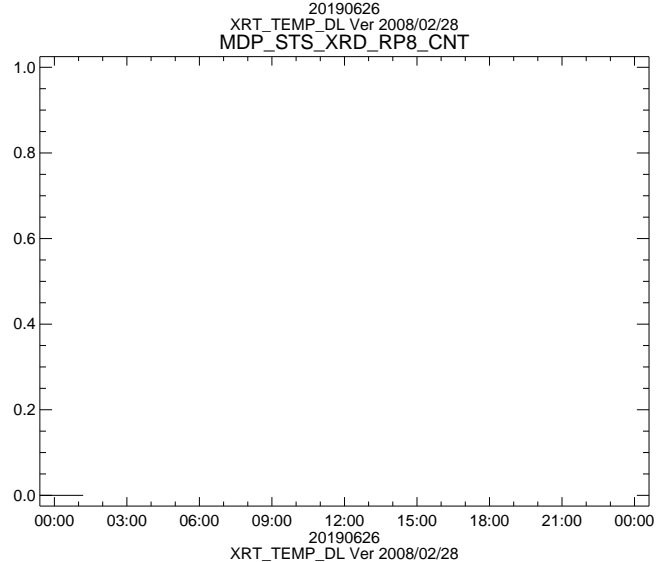
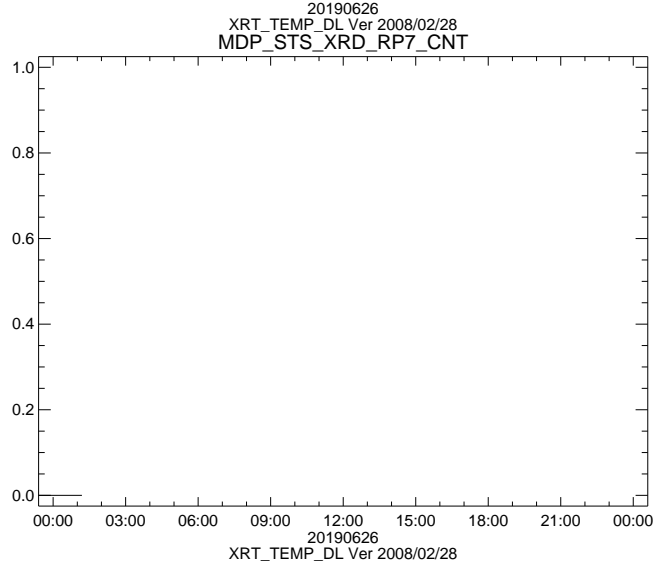
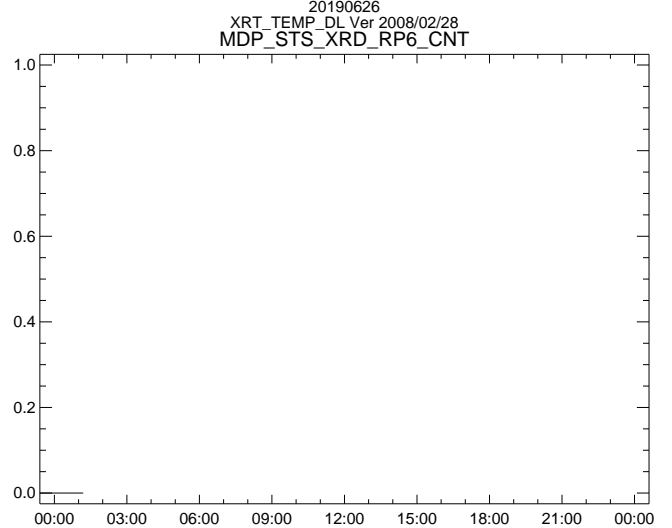
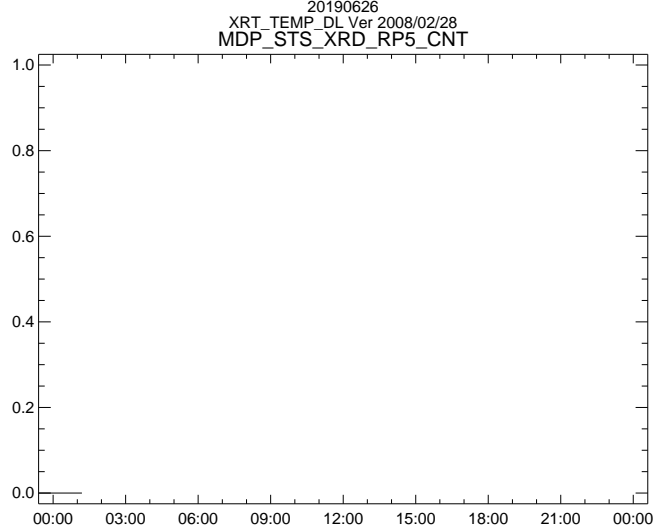
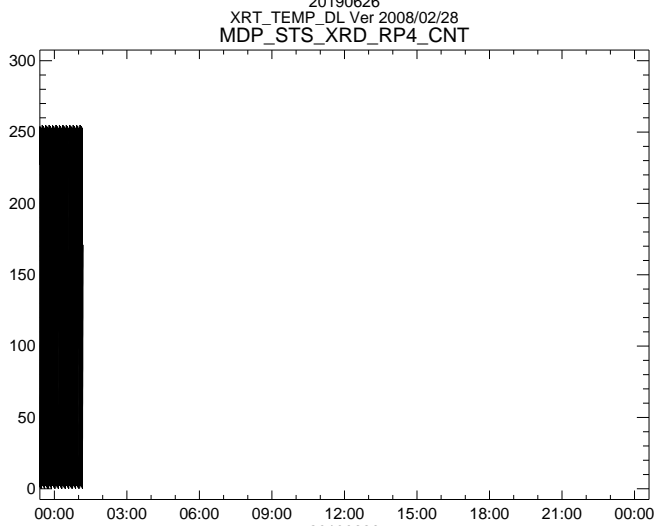
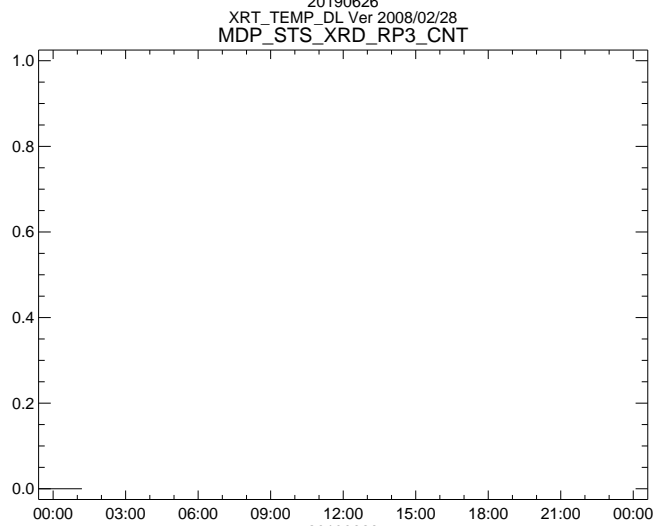
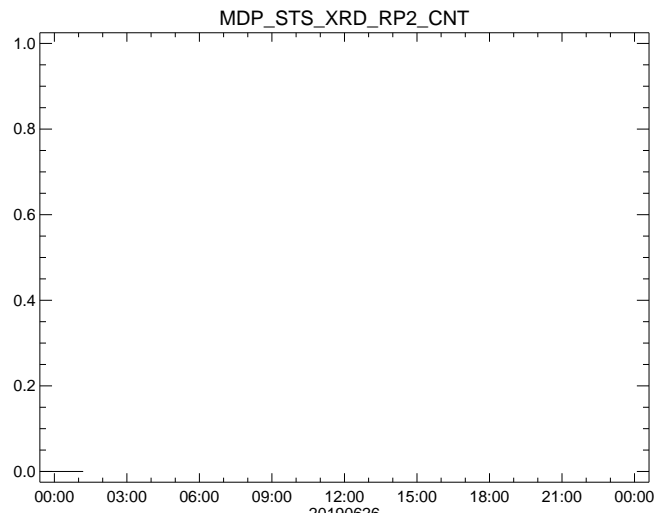
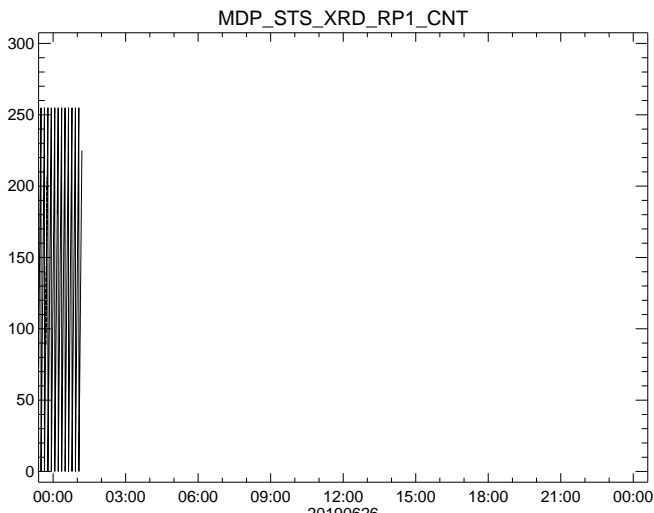
20190626
XRT_TEMP_DL Ver 2008/02/28
ADC Statuses



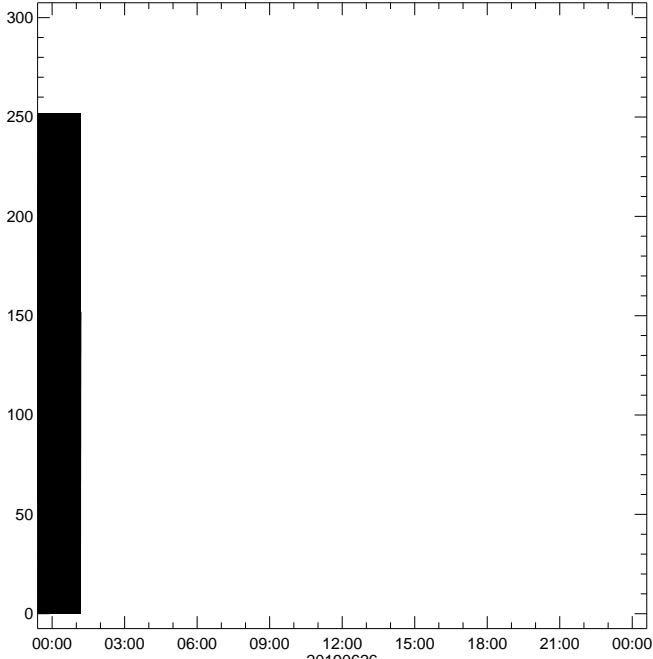
20190626
XRT_TEMP_DL Ver 2008/02/28
XRTD_E_LST_ACT



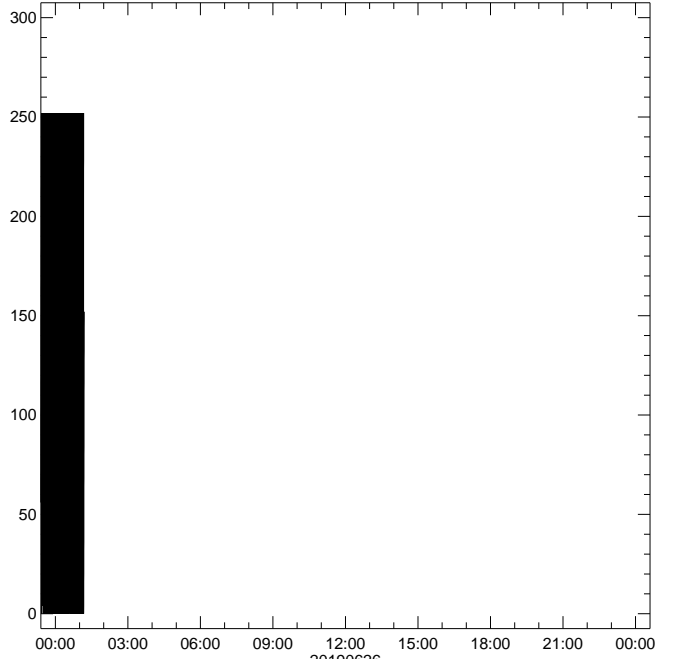




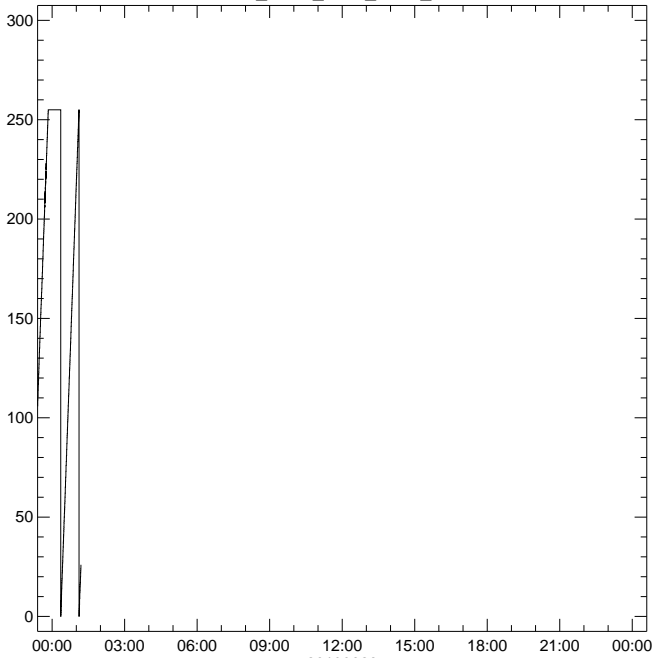
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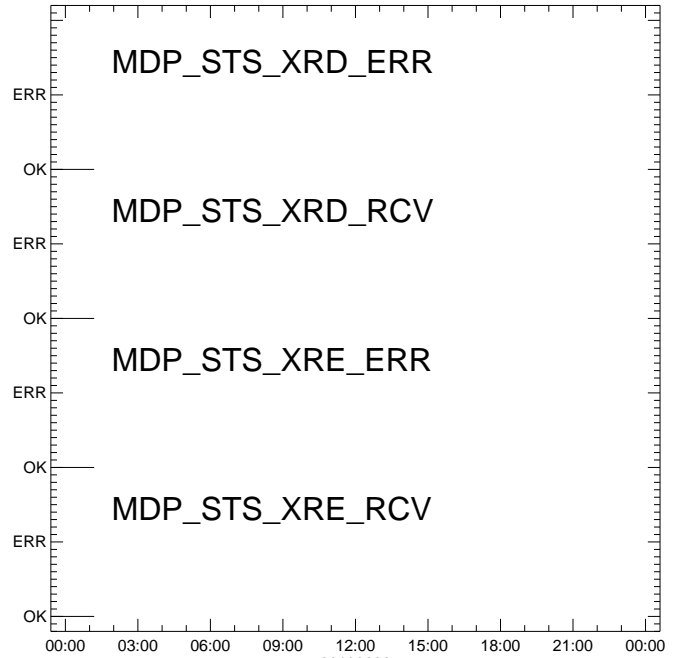
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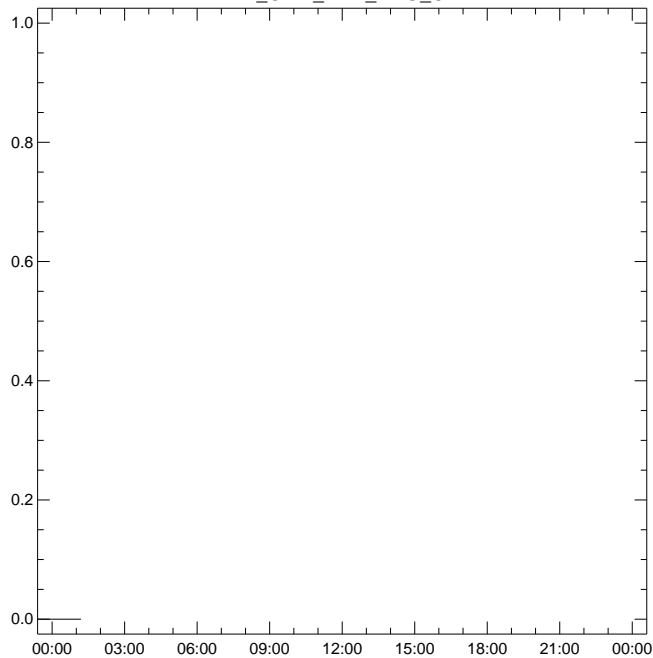
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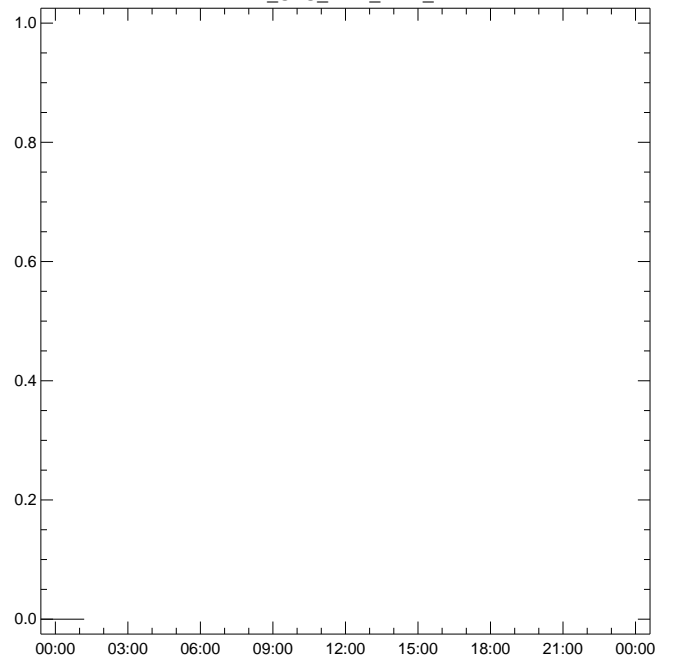
STS Recv. Statuses

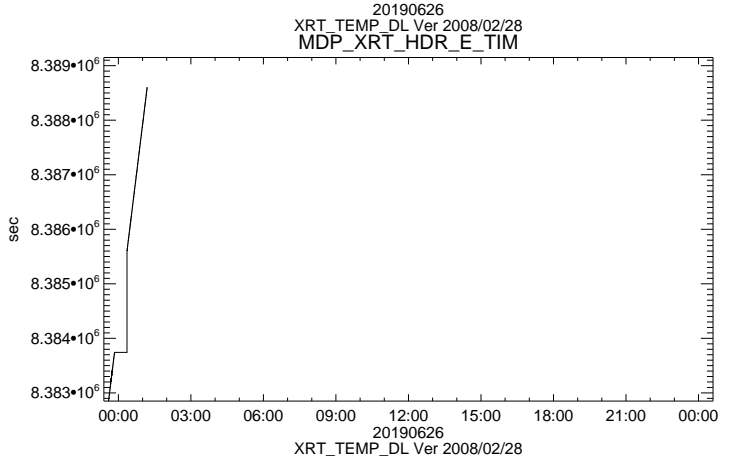
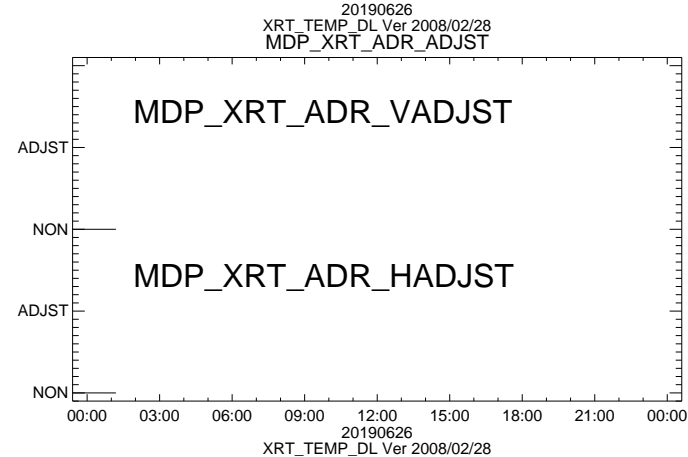
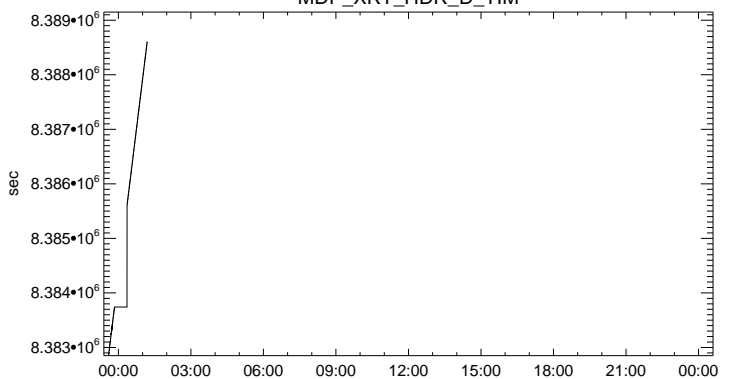
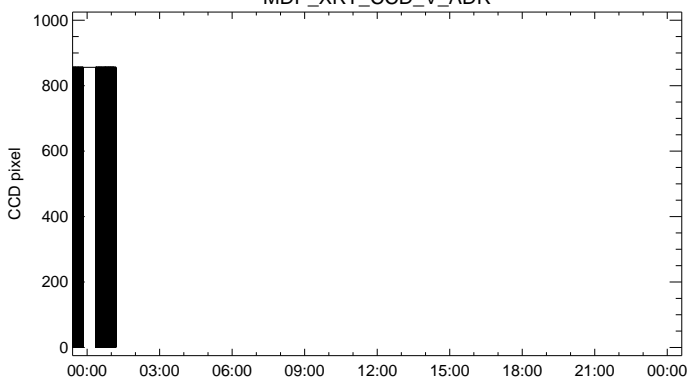
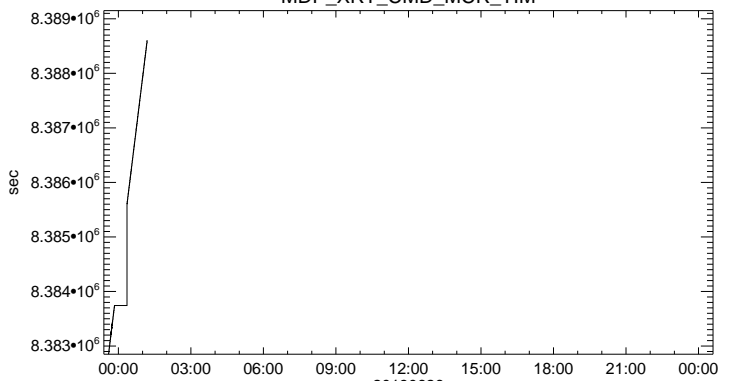
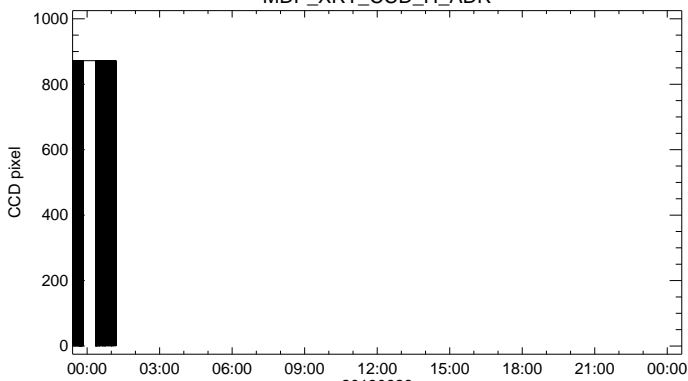
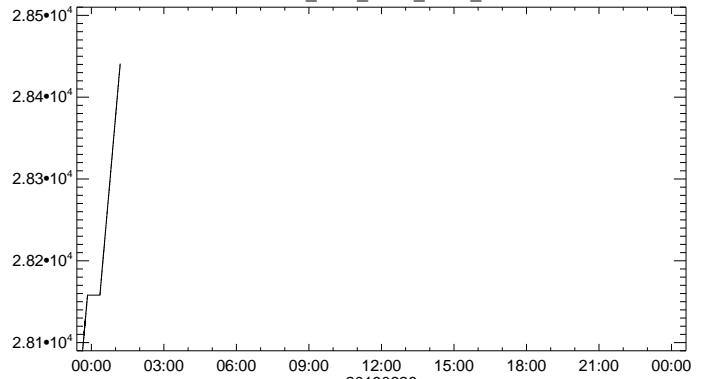
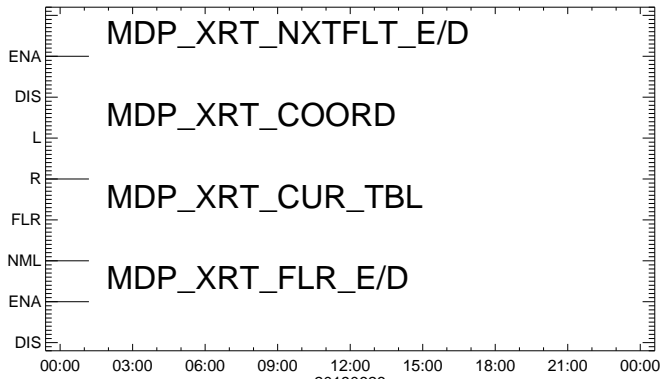
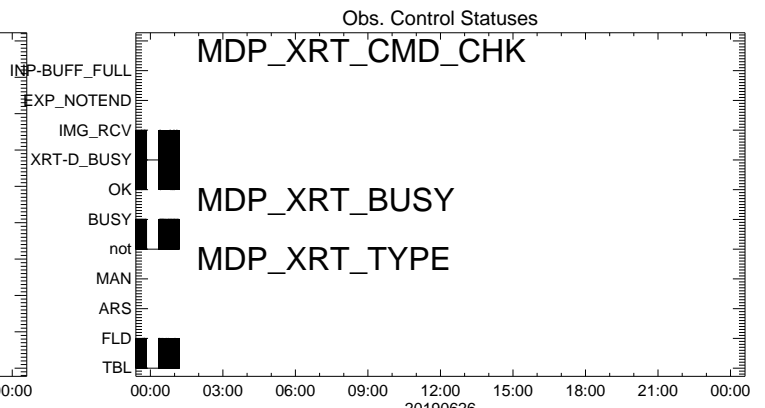
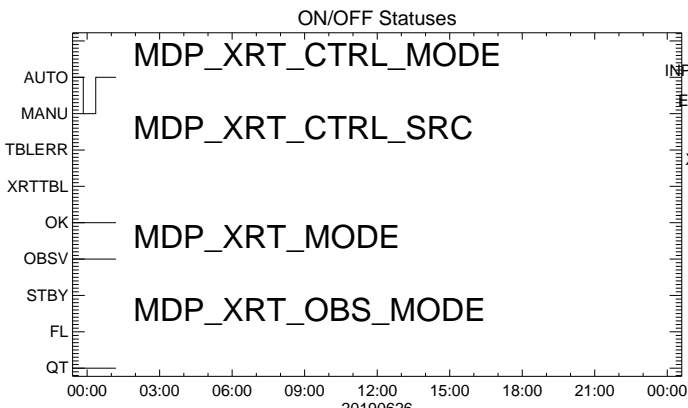


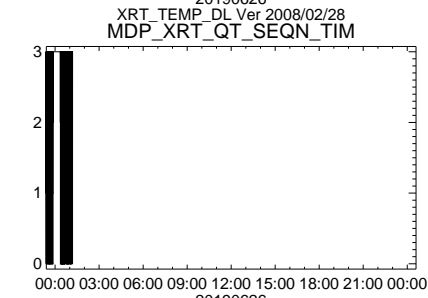
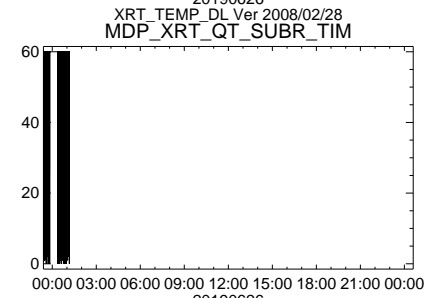
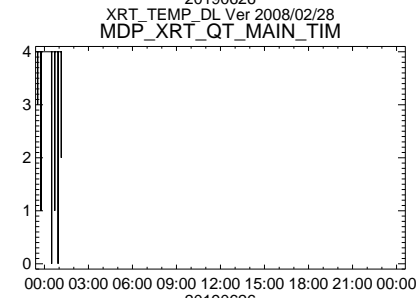
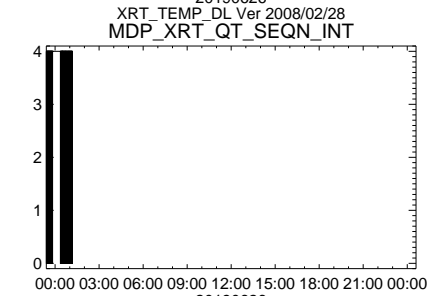
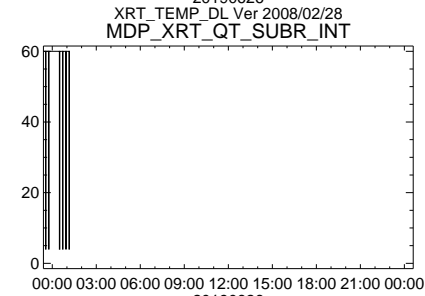
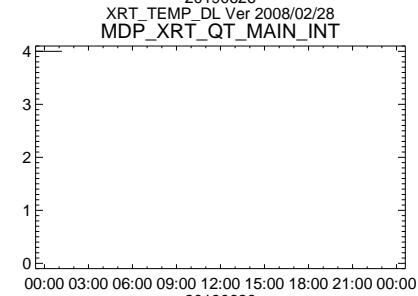
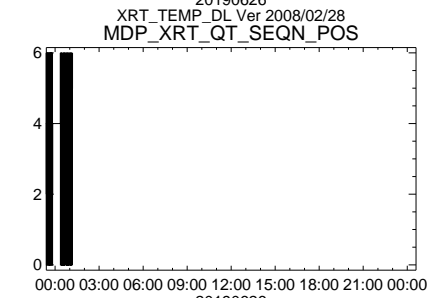
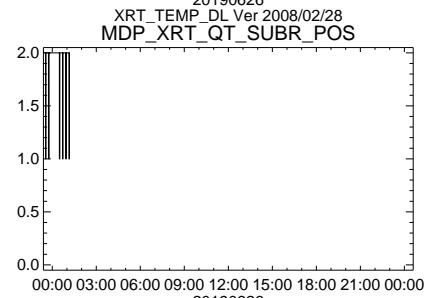
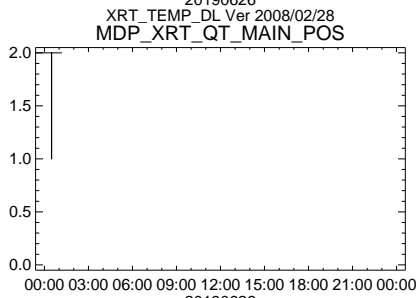
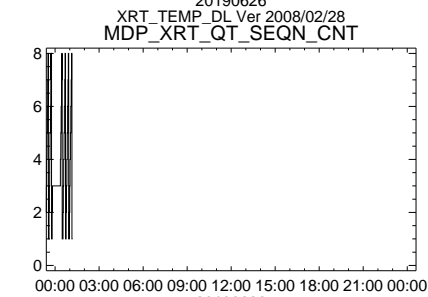
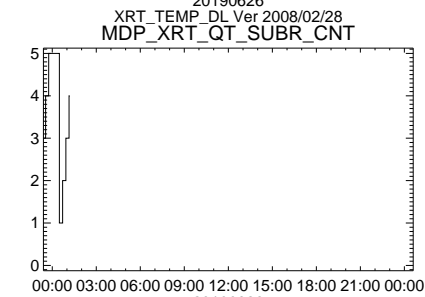
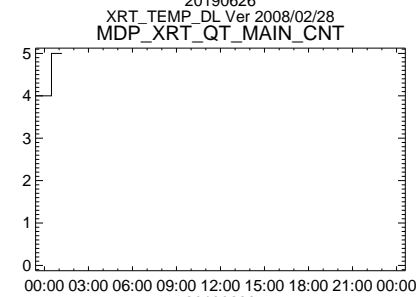
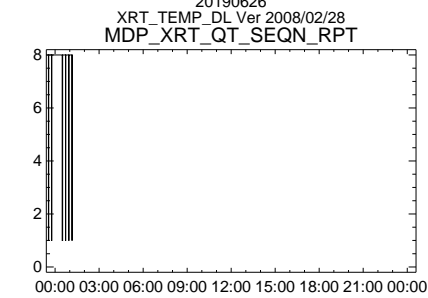
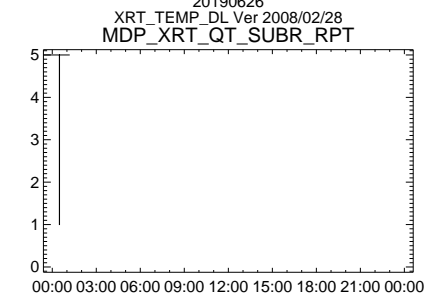
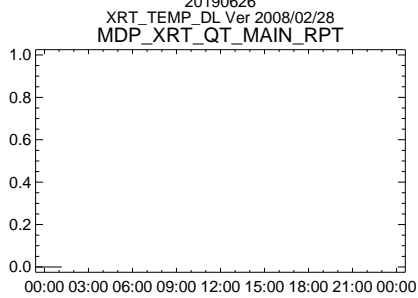
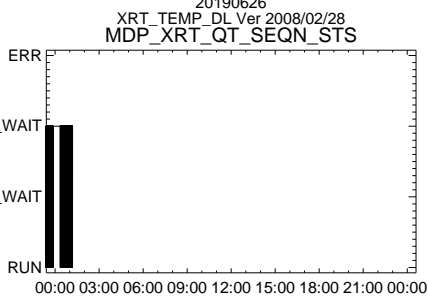
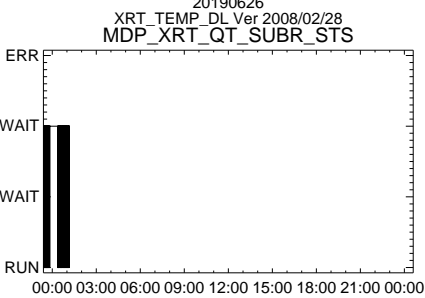
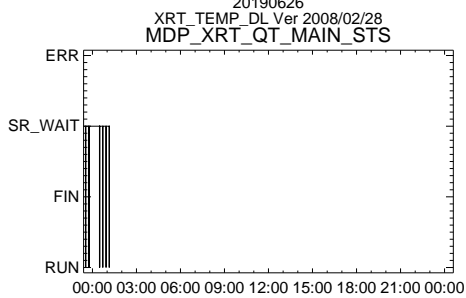
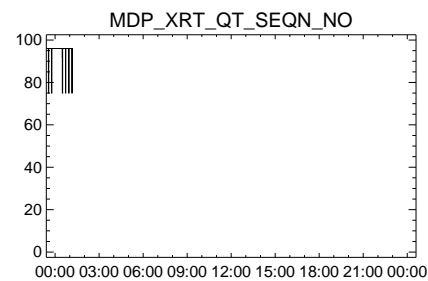
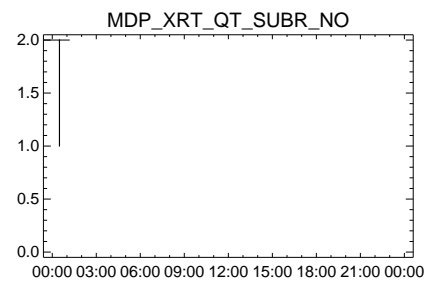
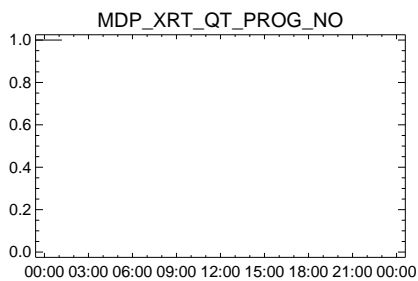
MDP_CMD_XRE_DHU_CNT

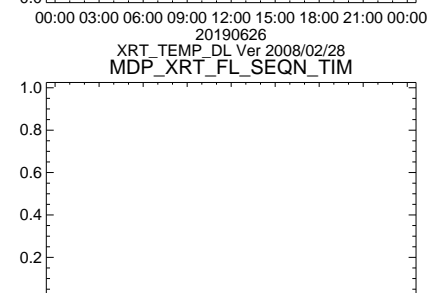
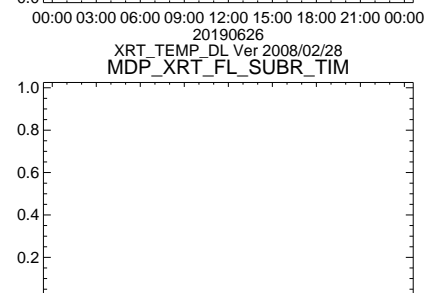
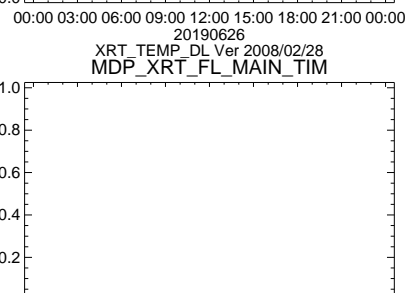
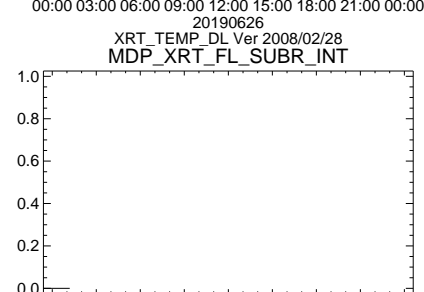
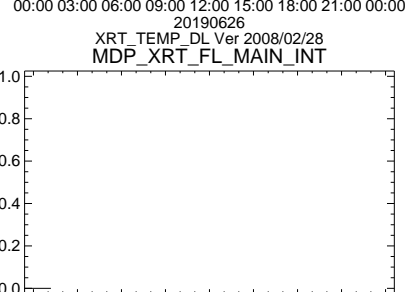
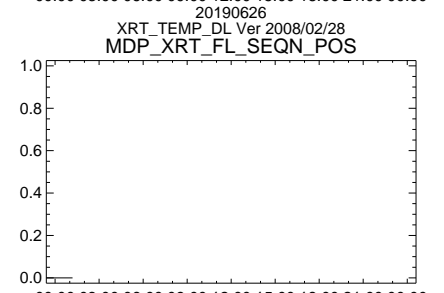
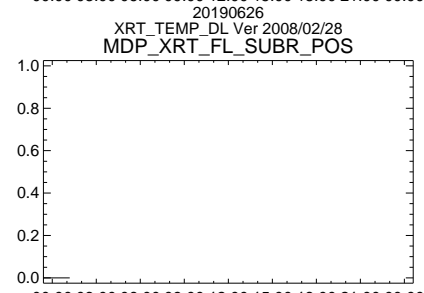
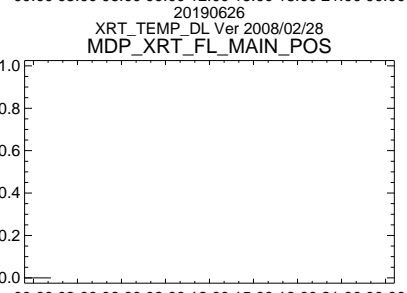
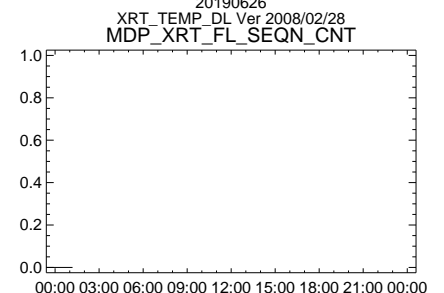
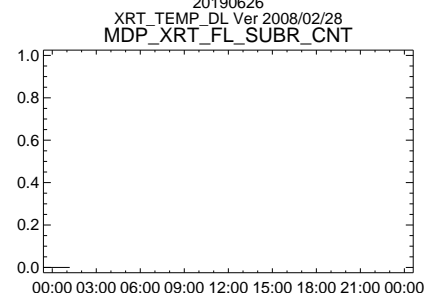
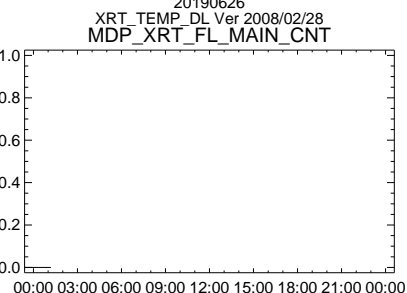
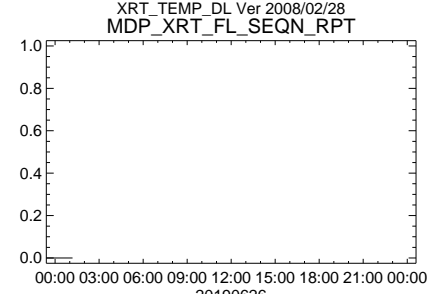
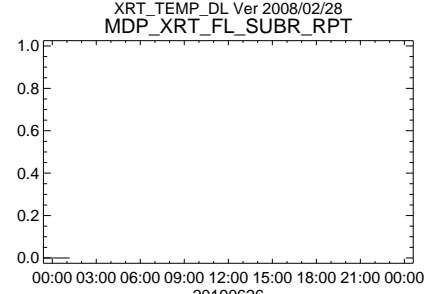
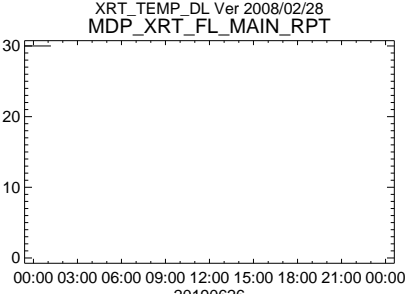
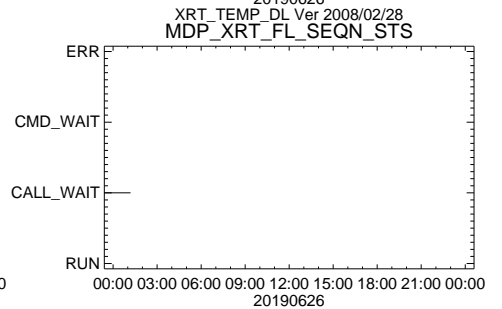
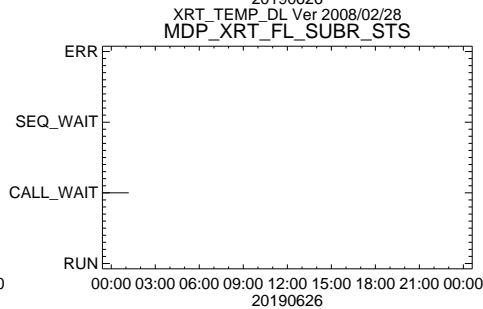
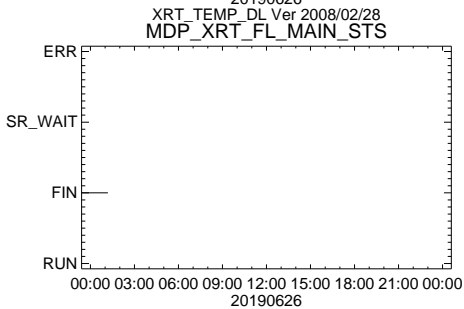
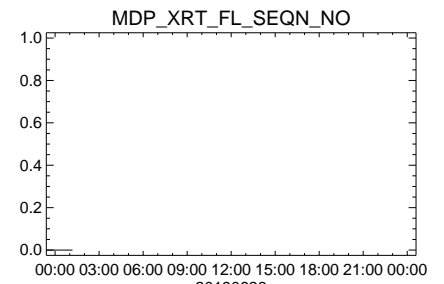
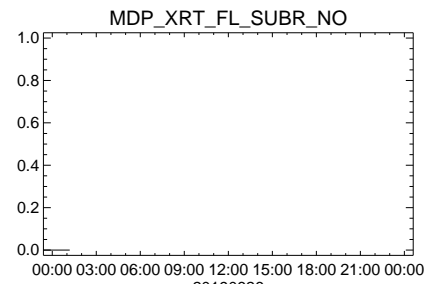
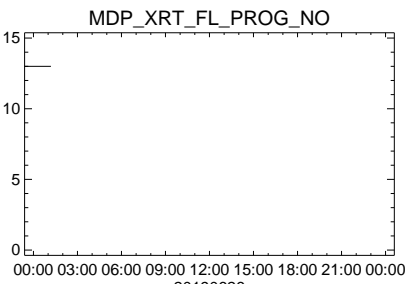


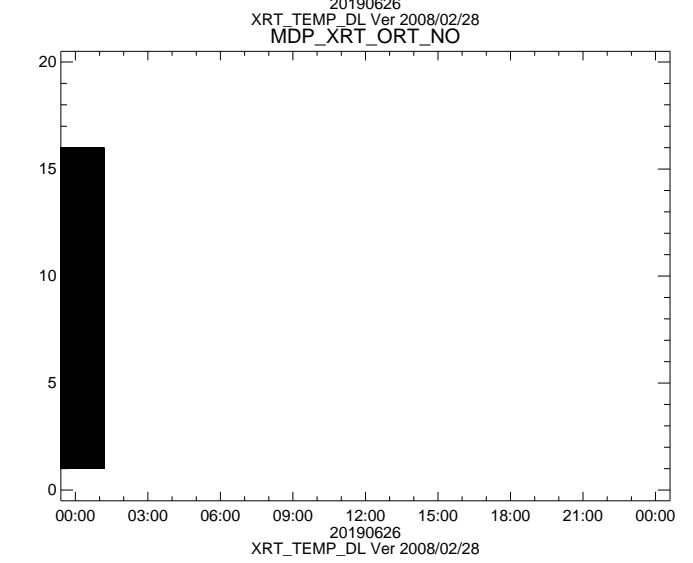
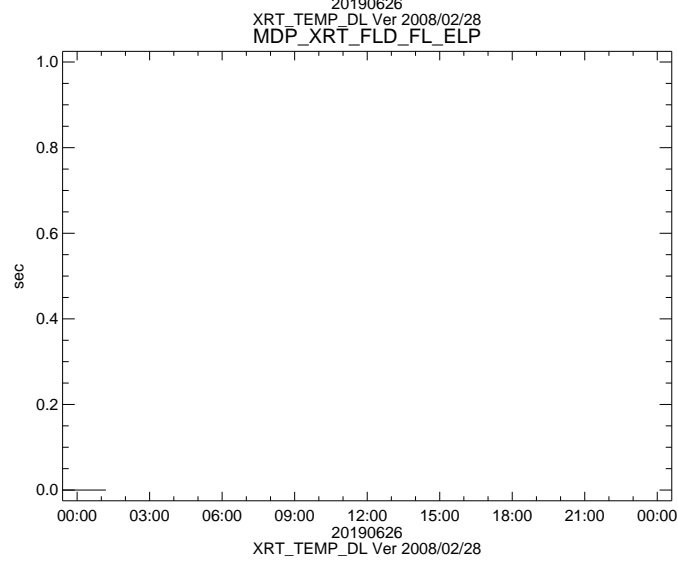
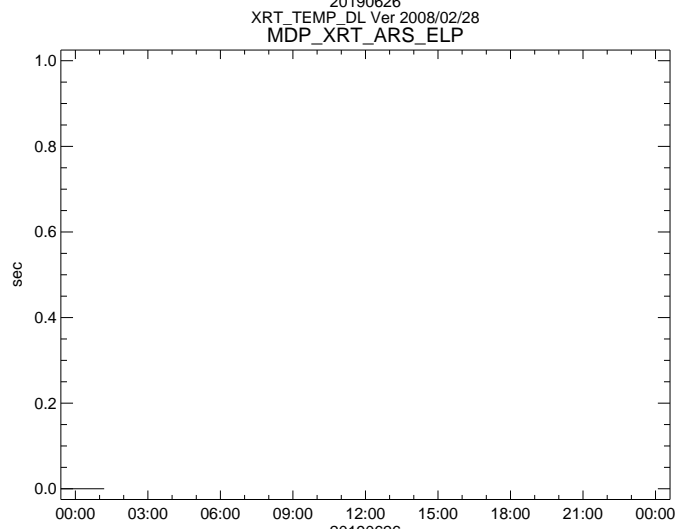
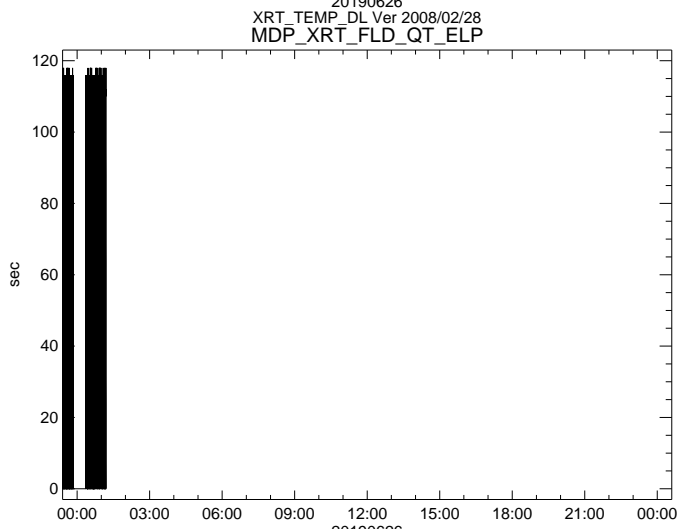
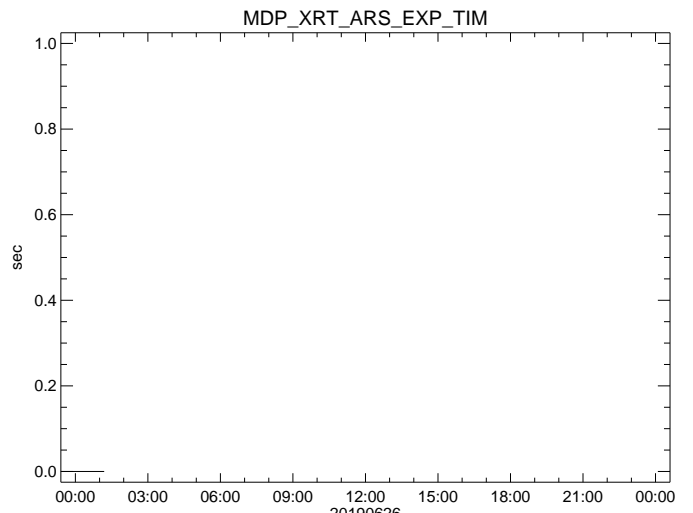
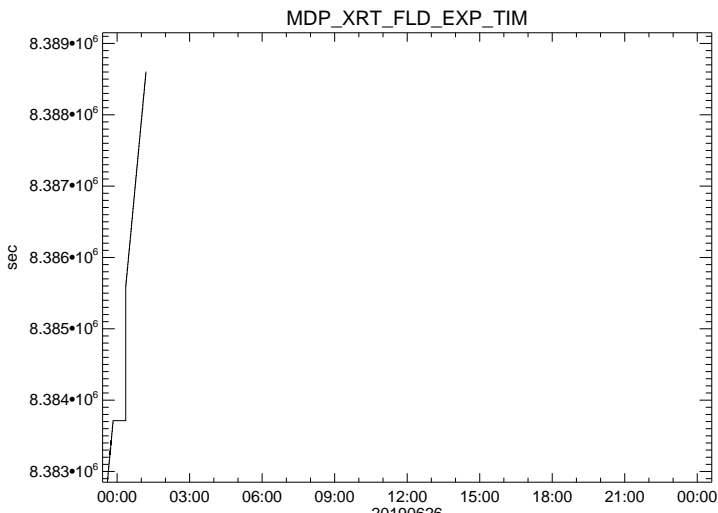
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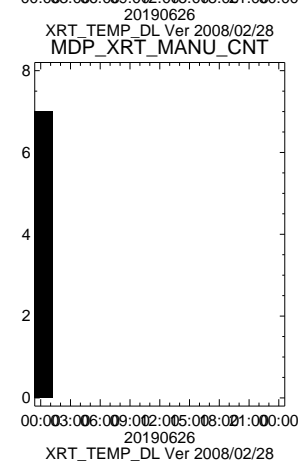
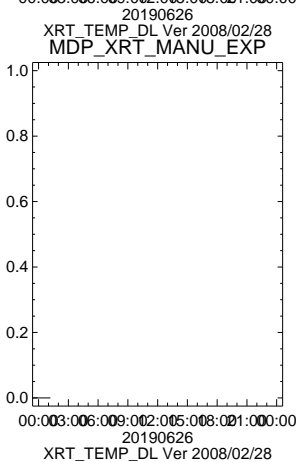
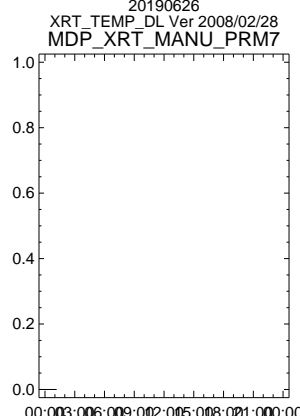
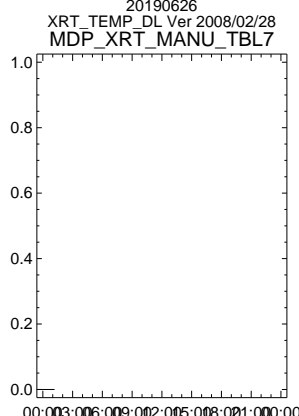
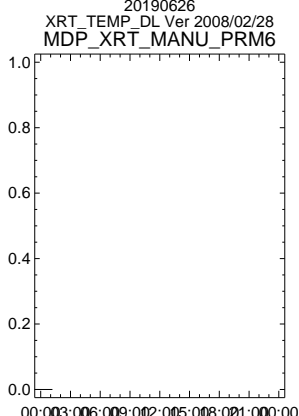
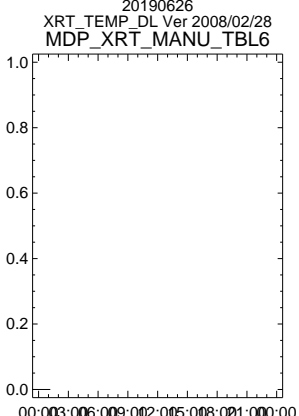
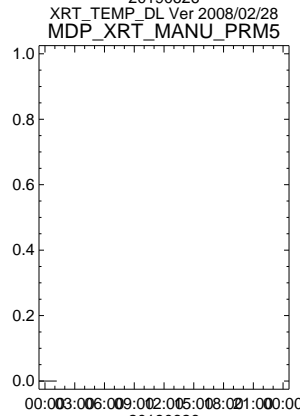
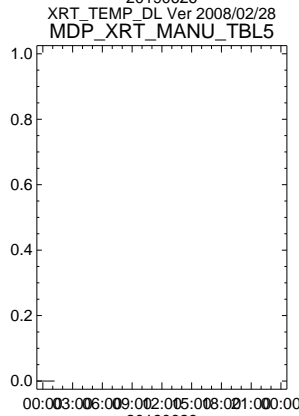
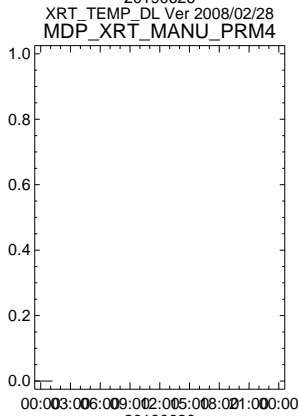
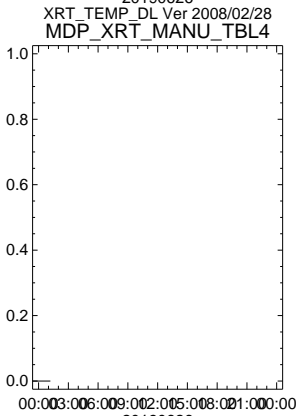
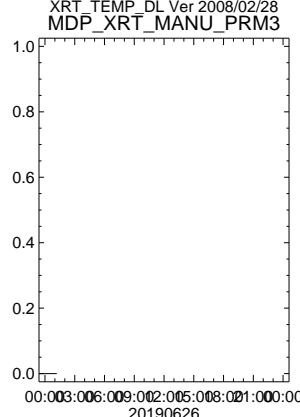
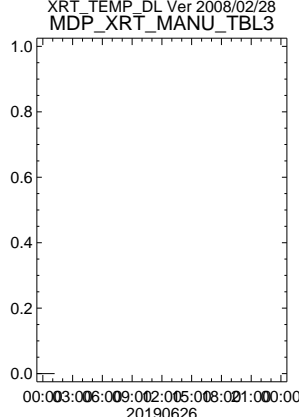
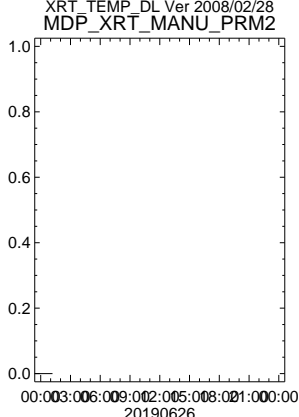
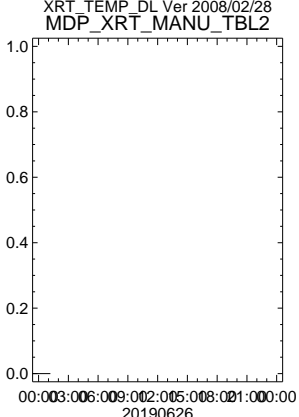
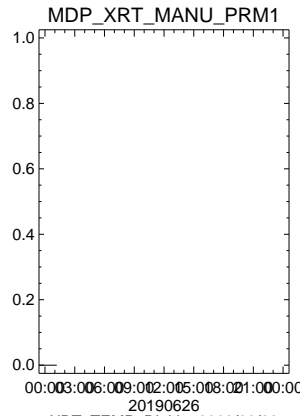
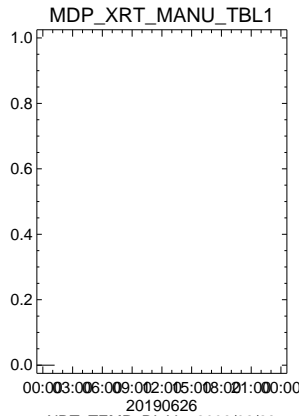
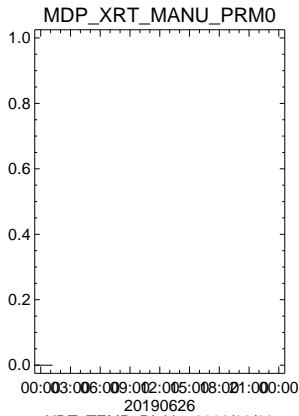
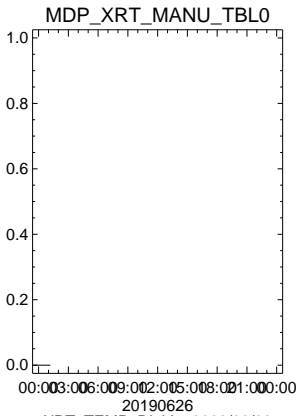
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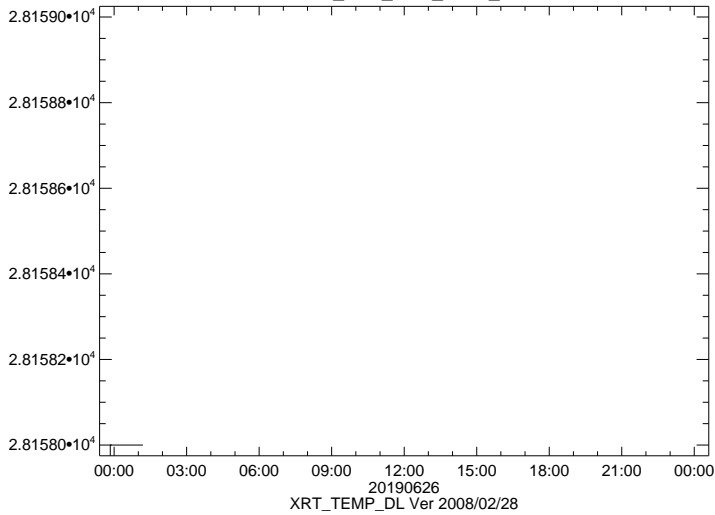
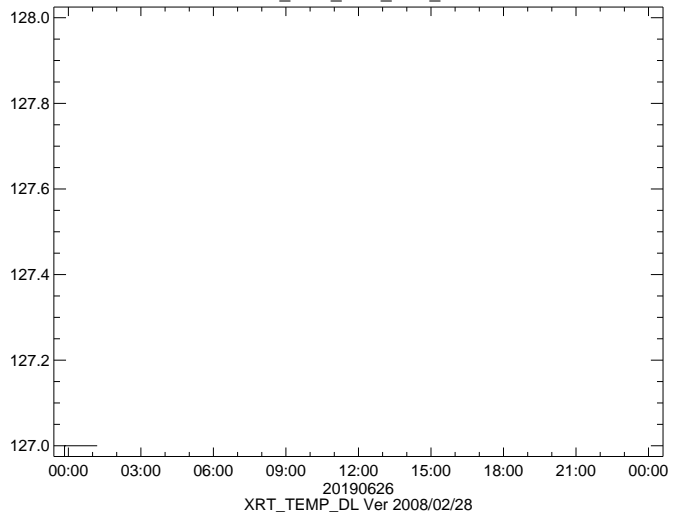
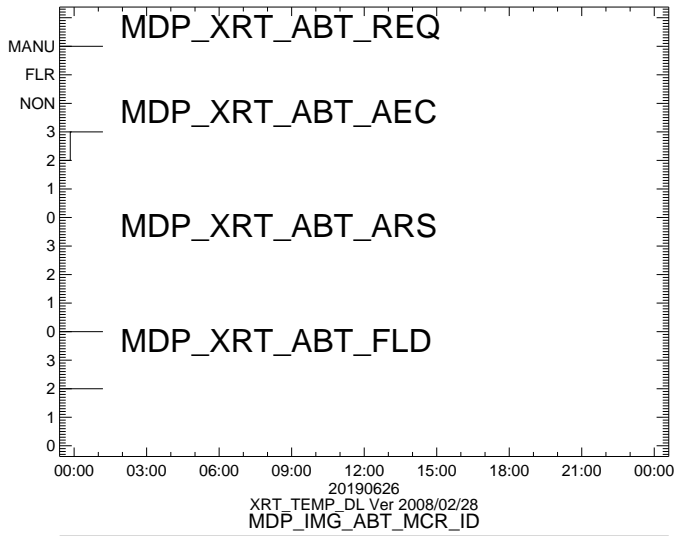
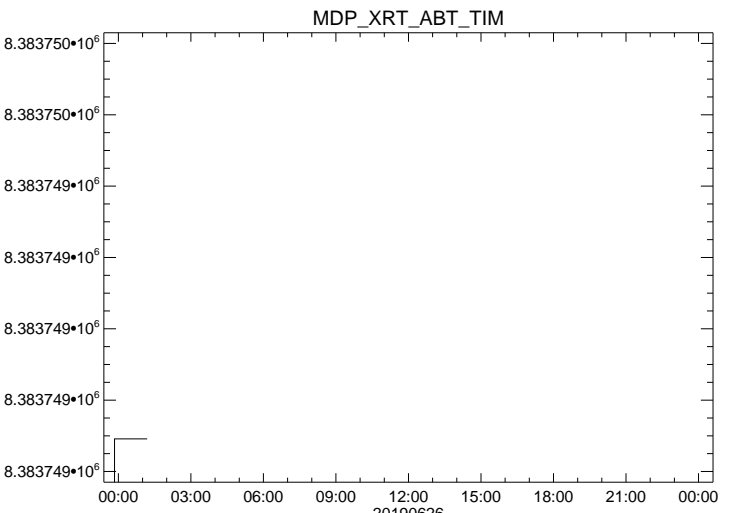
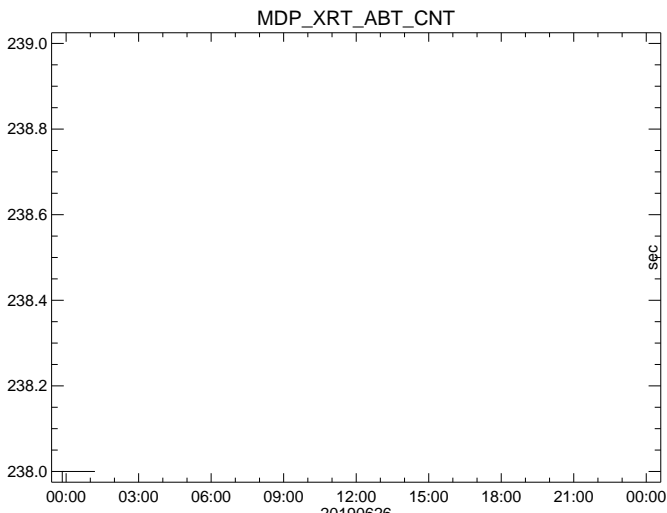
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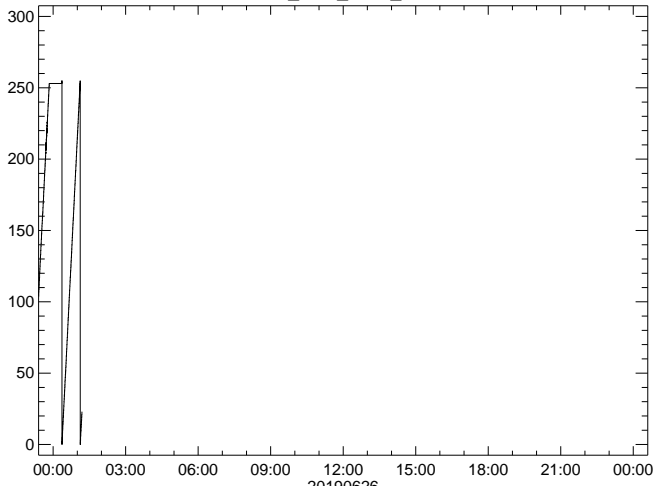
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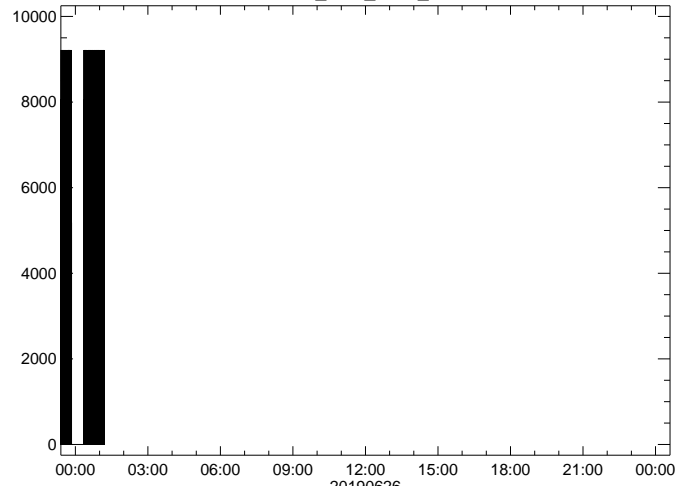




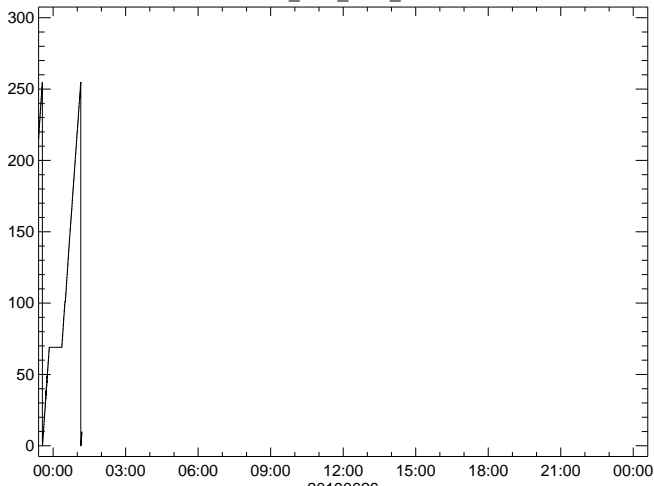
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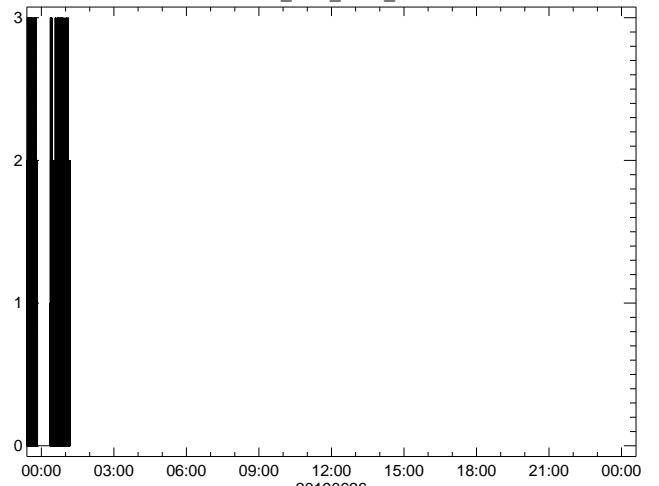
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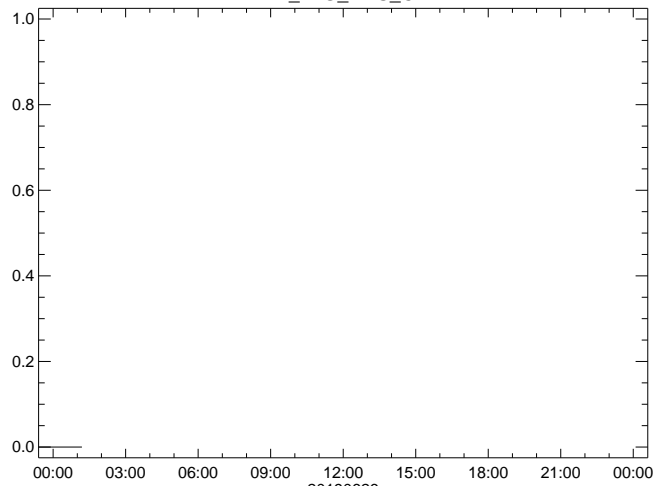
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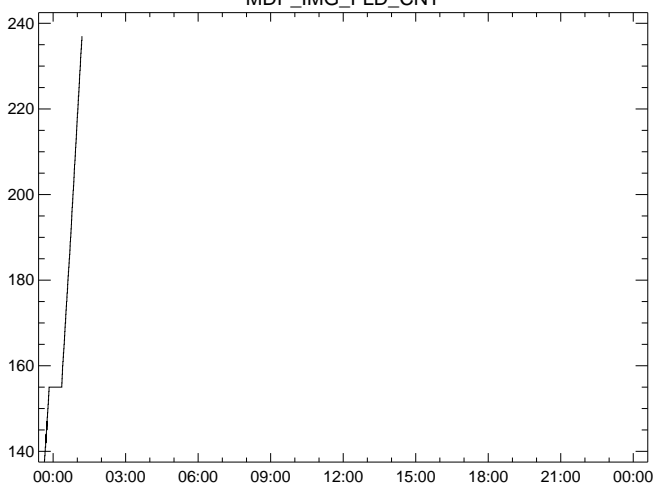
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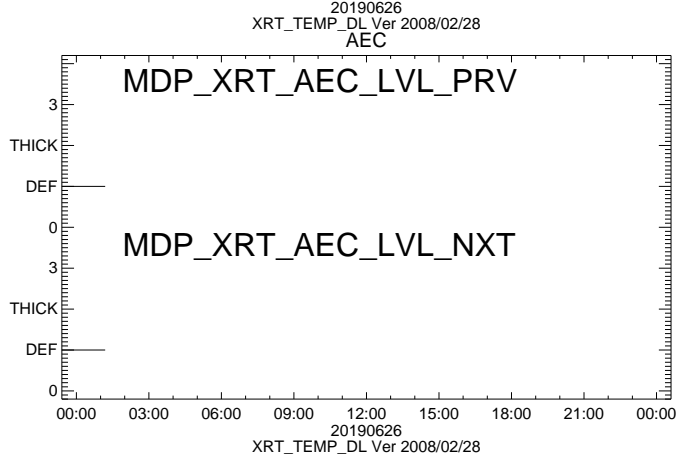
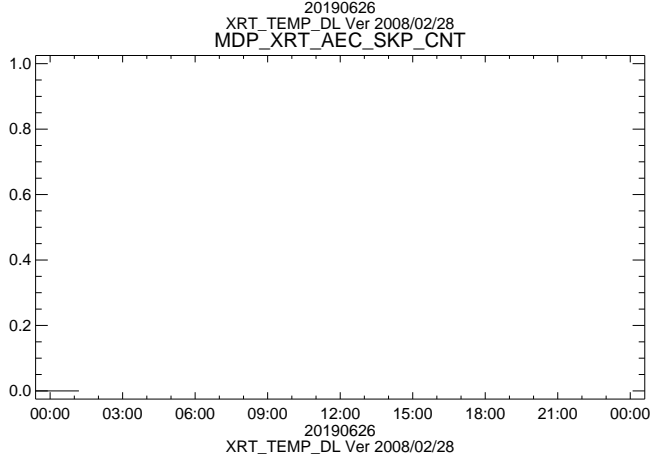
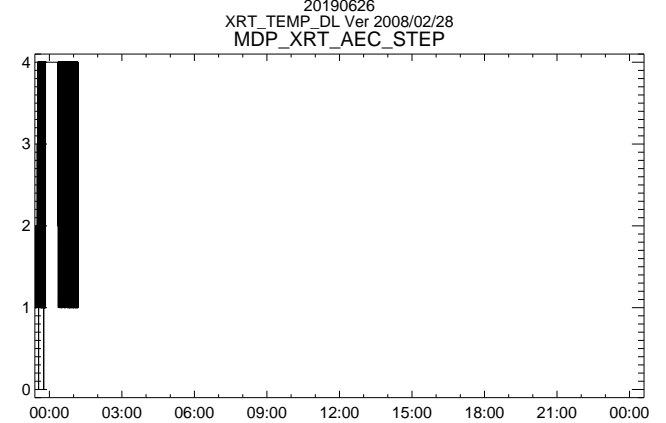
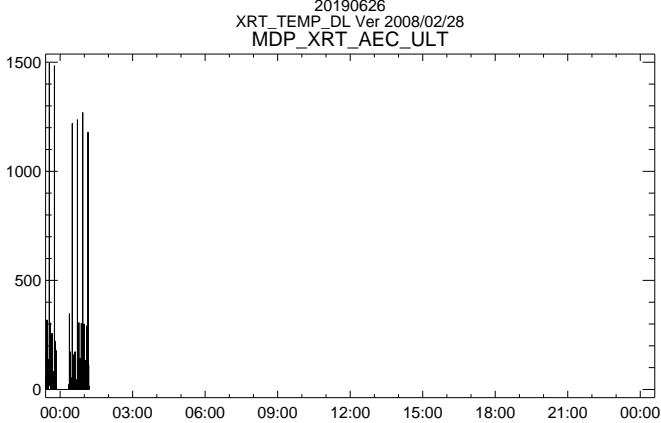
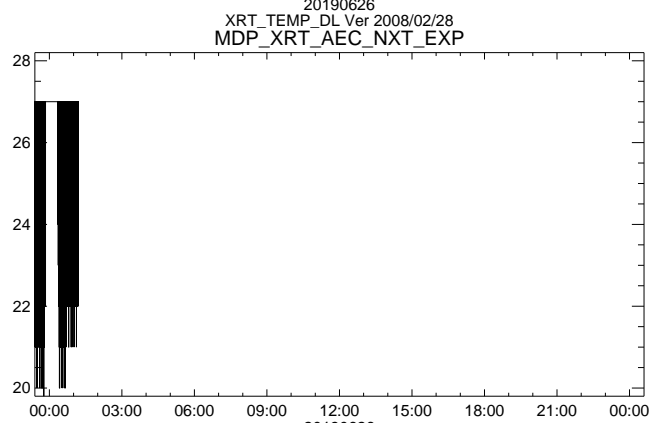
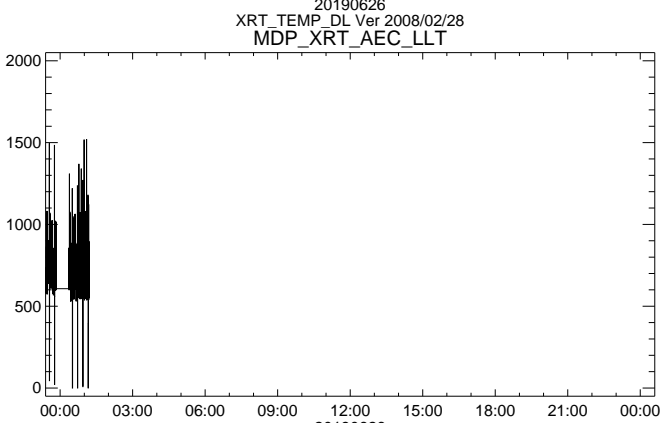
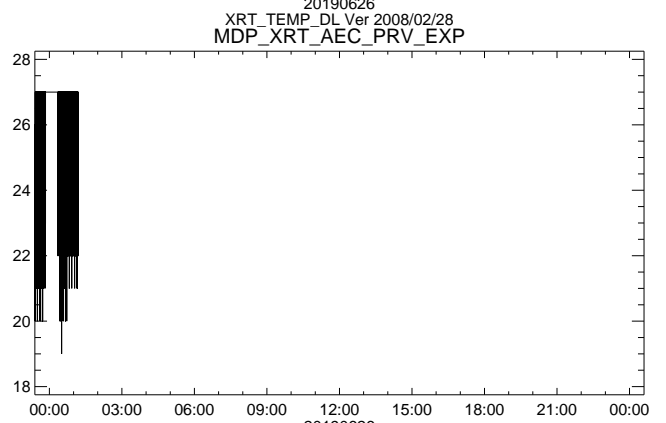
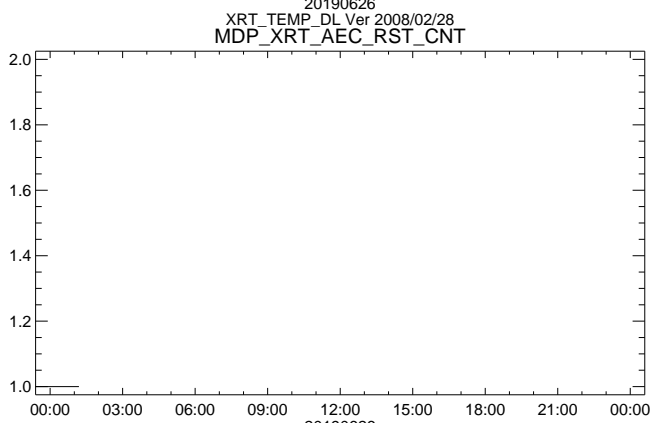
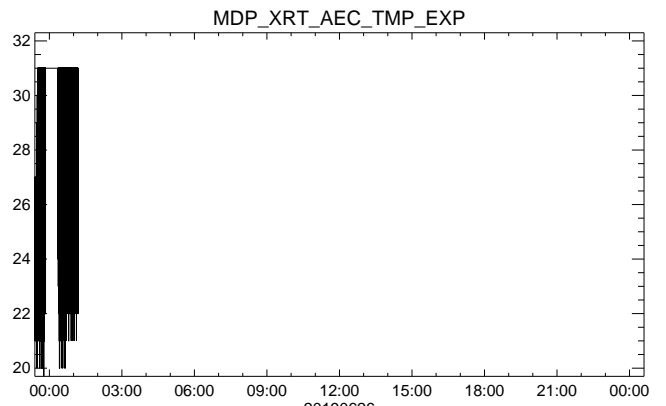
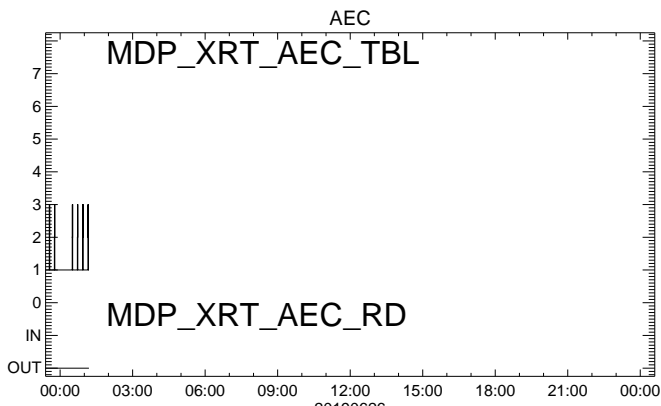


MDP_IMG_ARS_CNT

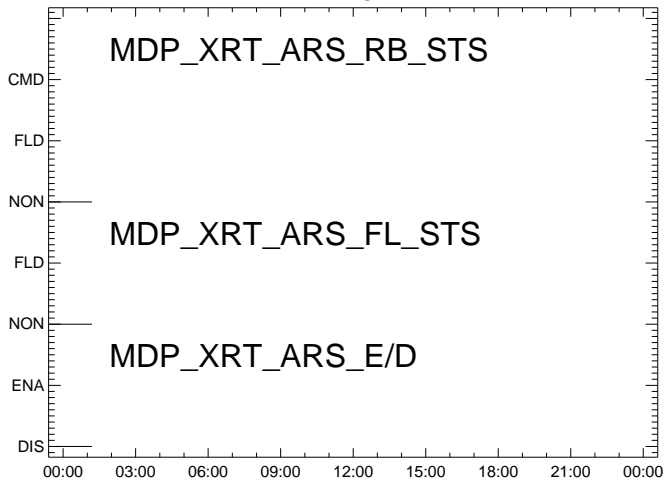


MDP_IMG_FLD_CNT

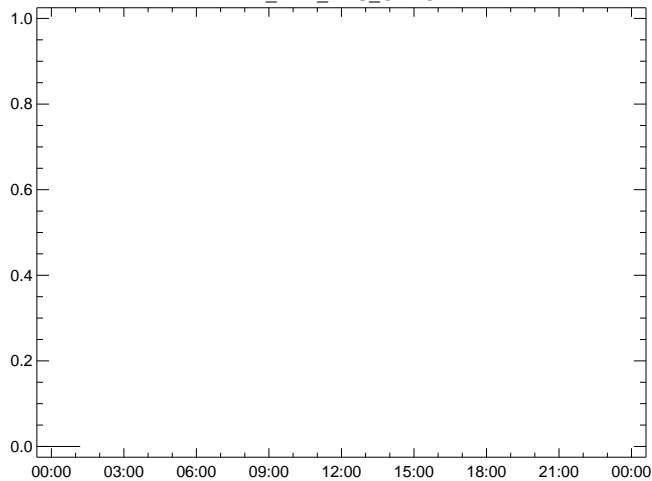




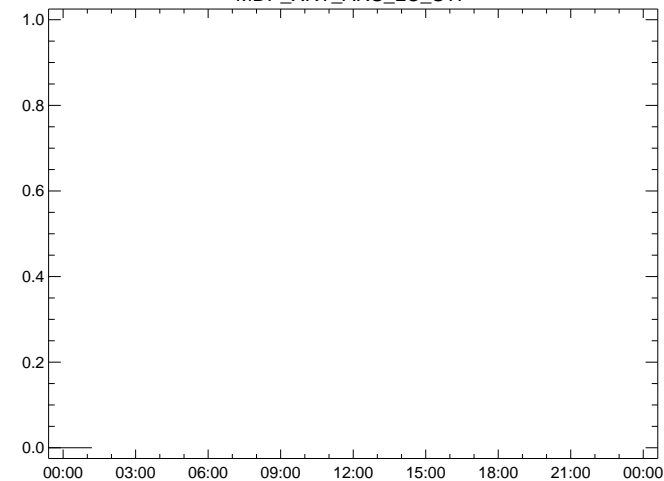
ARS



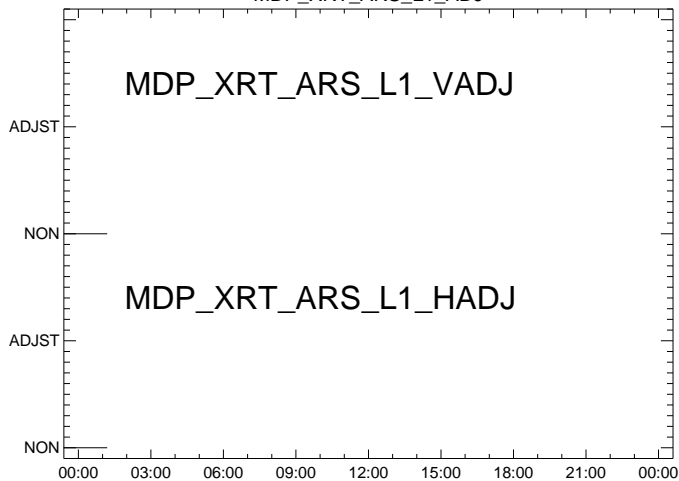
MDP_XRT_ARS_CANCEL



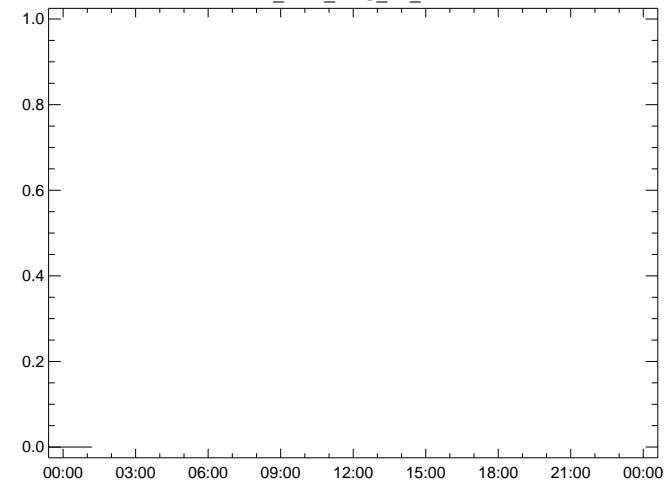
20190626
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_ARS_LC_STP



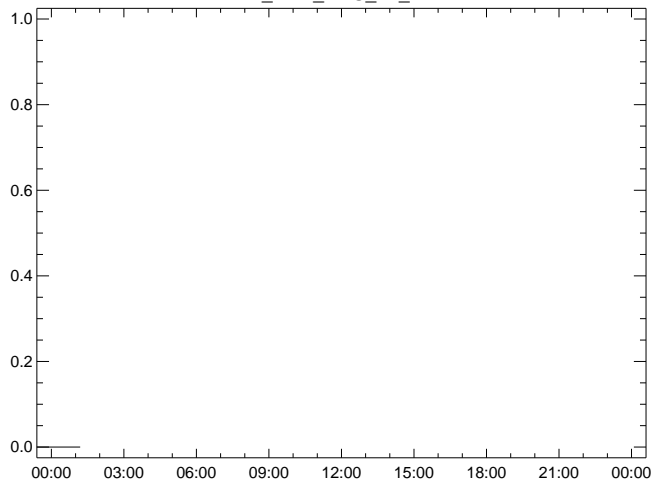
20190626
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_ARS_L1_ADJ



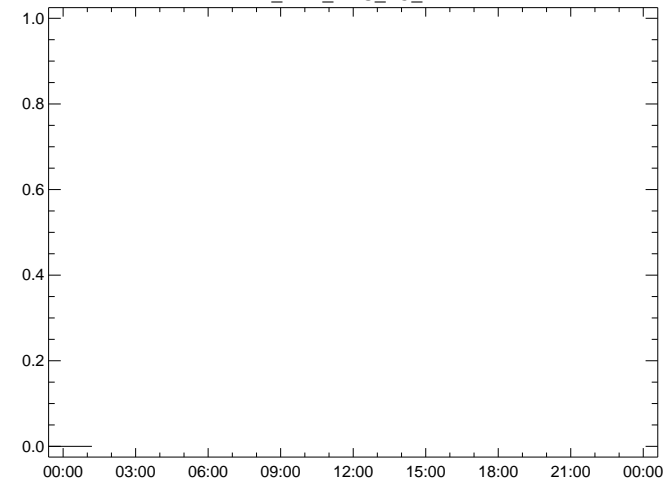
20190626
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_ARS_L2_HADR



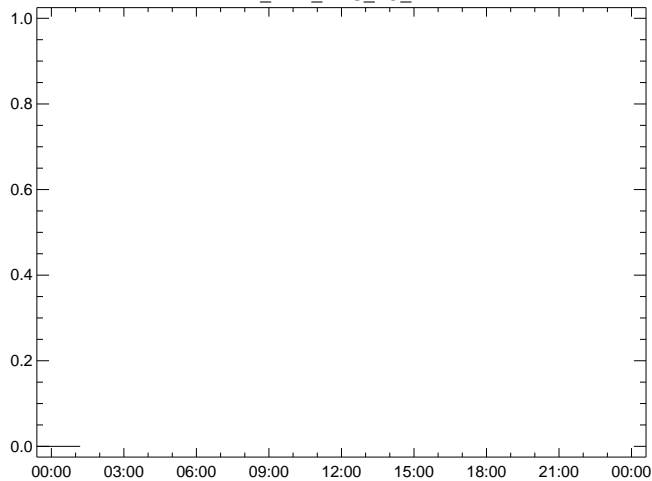
20190626
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_ARS_L2_VADR



20190626
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_ARS_LC_HADR



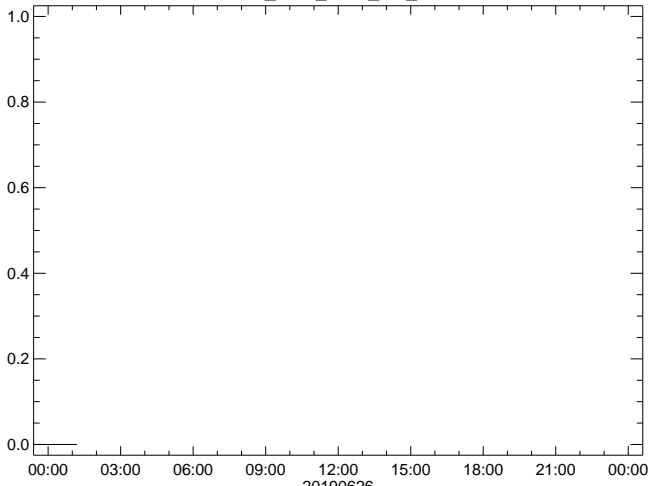
20190626
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_ARS_LC_VADR



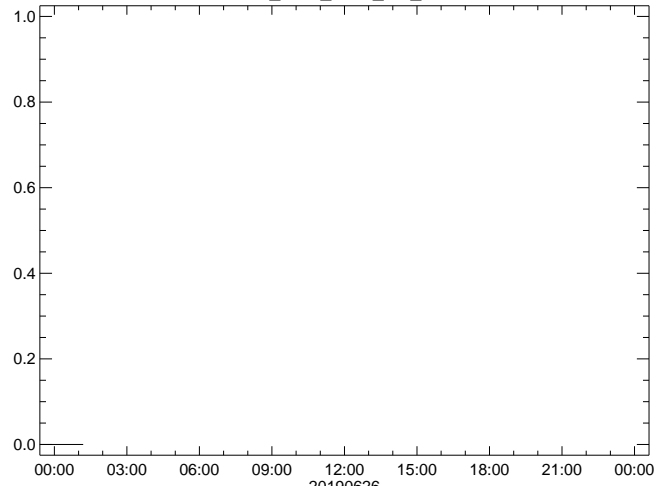
20190626
XRT_TEMP_DL Ver 2008/02/28

20190626
XRT_TEMP_DL Ver 2008/02/28

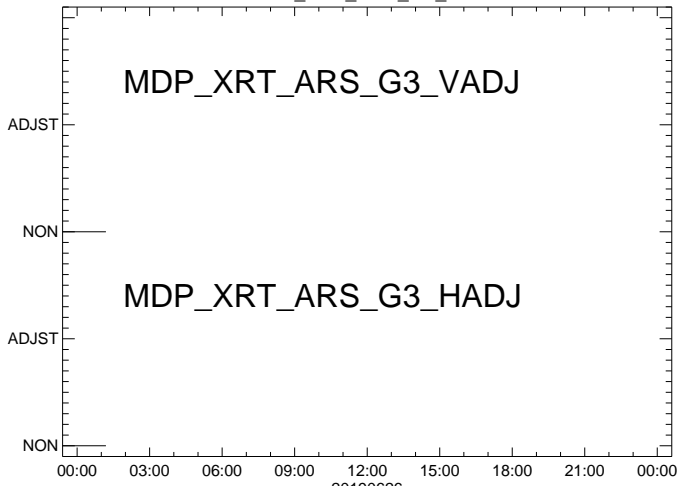
MDP_XRT_ARS_GL_STP



MDP_XRT_ARS_G2_ERR



MDP_XRT_ARS_G3_ADJ



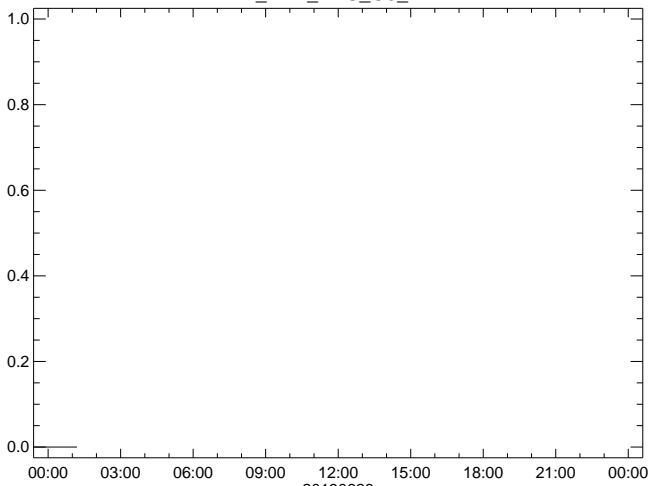
MDP_XRT_ARS_G3_VADJ

MDP_XRT_ARS_G4_VADJ

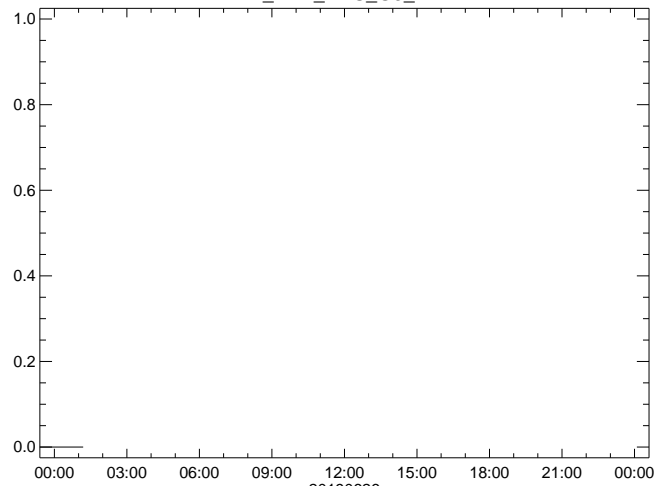
MDP_XRT_ARS_G3_HADJ

MDP_XRT_ARS_G4_HADJ

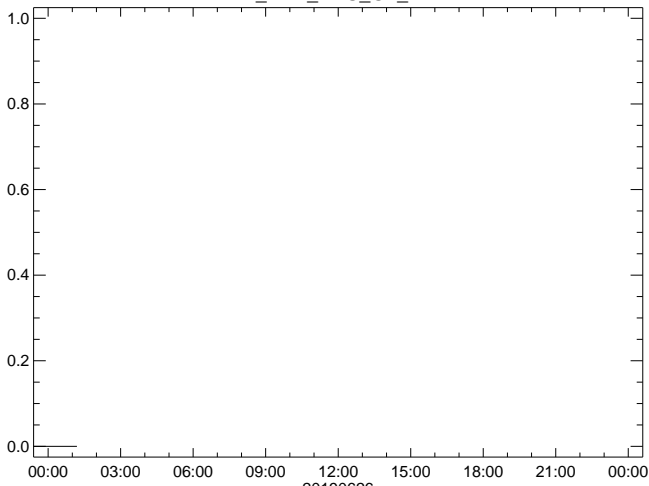
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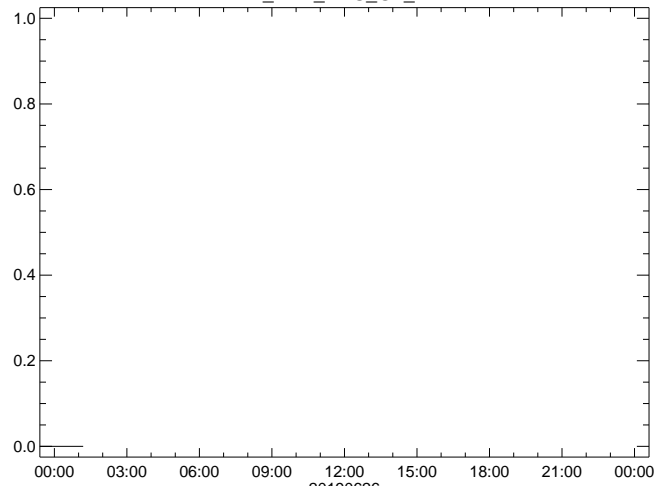
MDP_XRT_ARS_G3_VADR



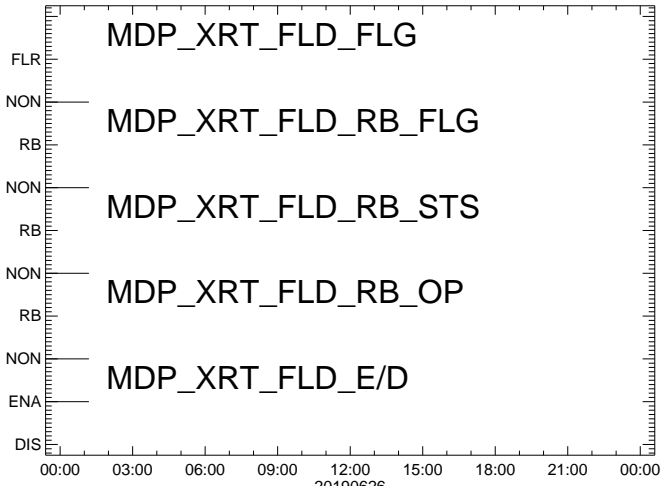
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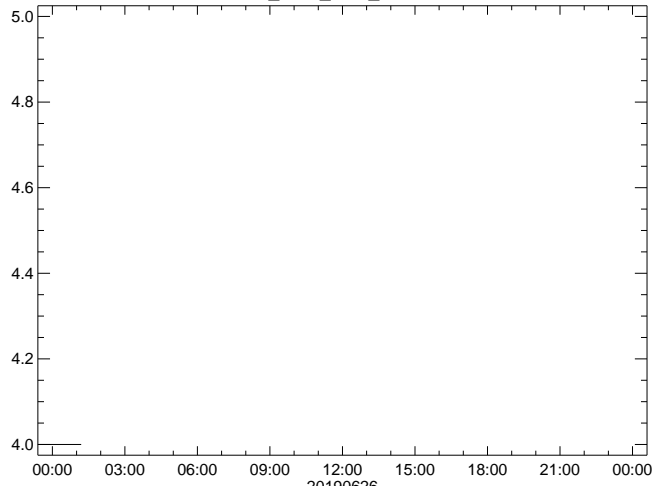
MDP_XRT_ARS_GL_VADR



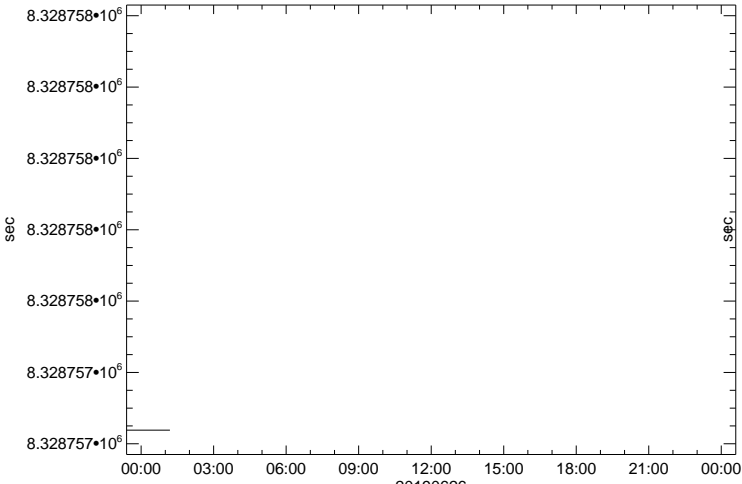
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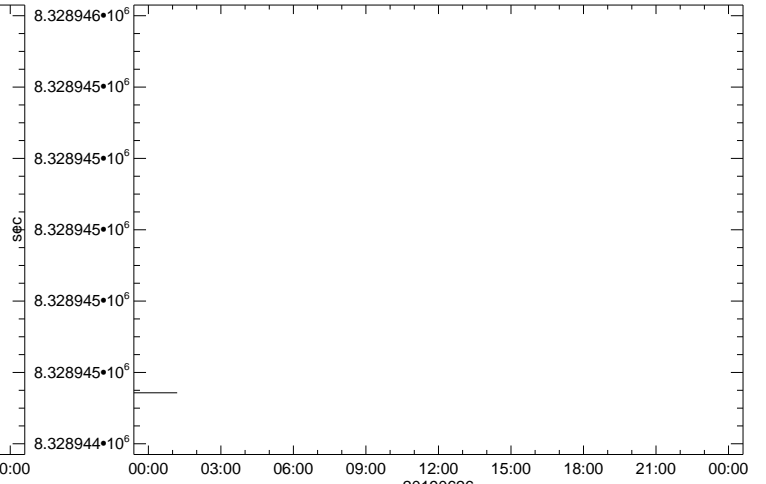
MDP_XRT_FLD_CANCEL



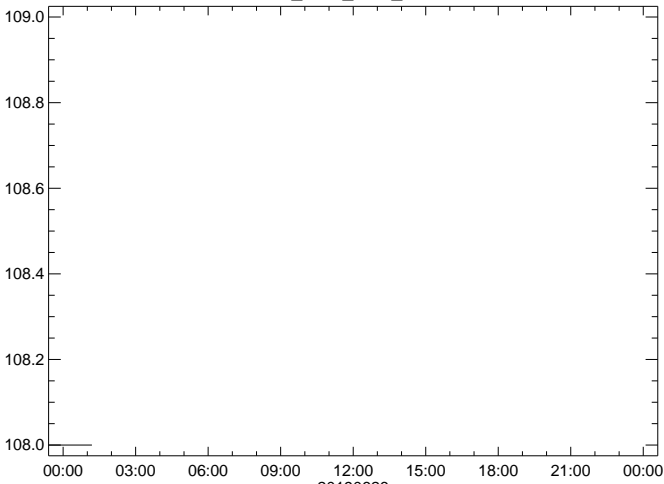
20190626
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_FLR_OCR_TIM



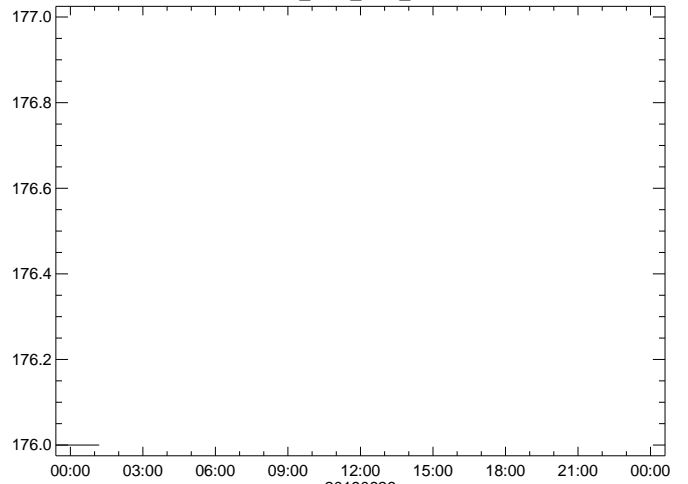
20190626
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_FLR_TRM_TIM



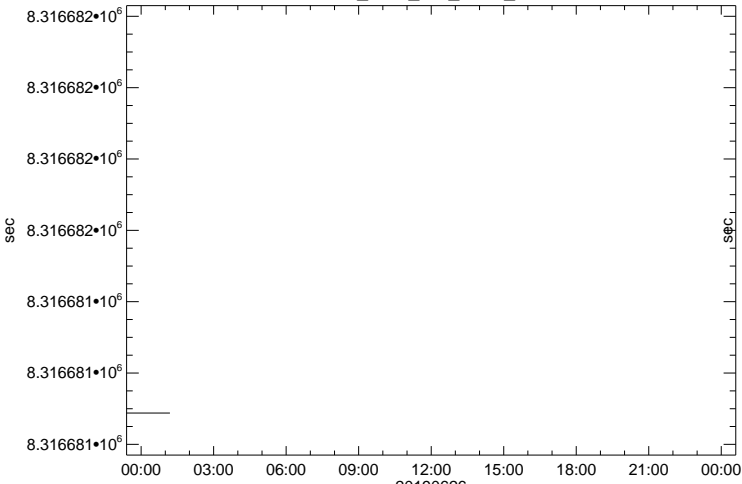
20190626
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_FLD_HADR



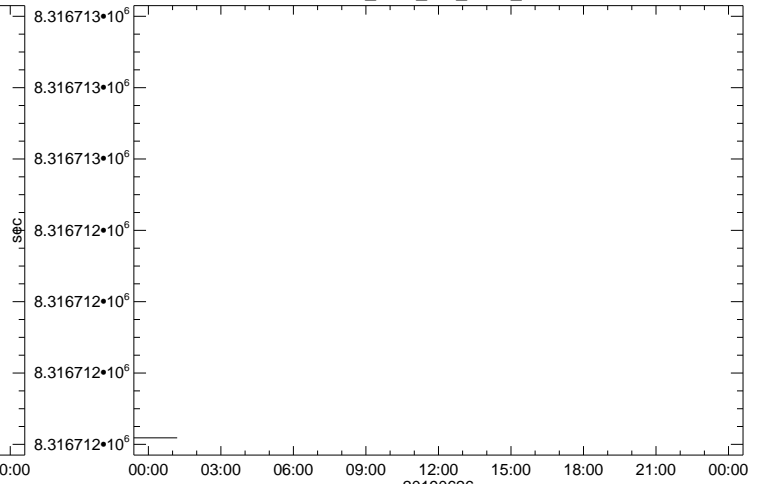
20190626
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_FLD_VADR

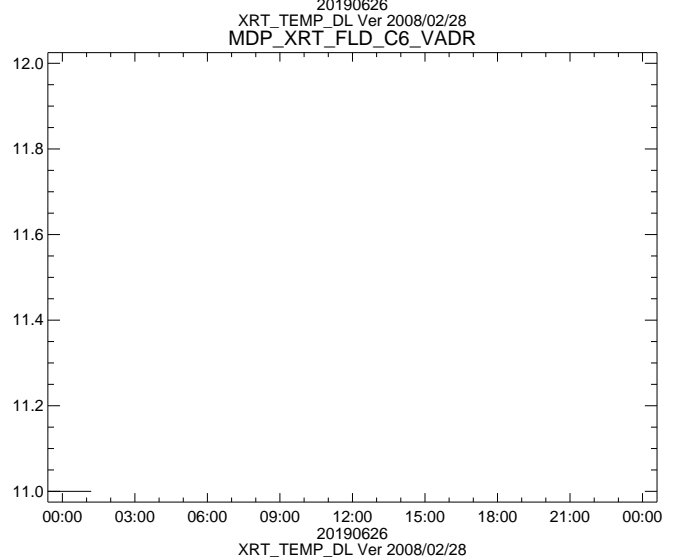
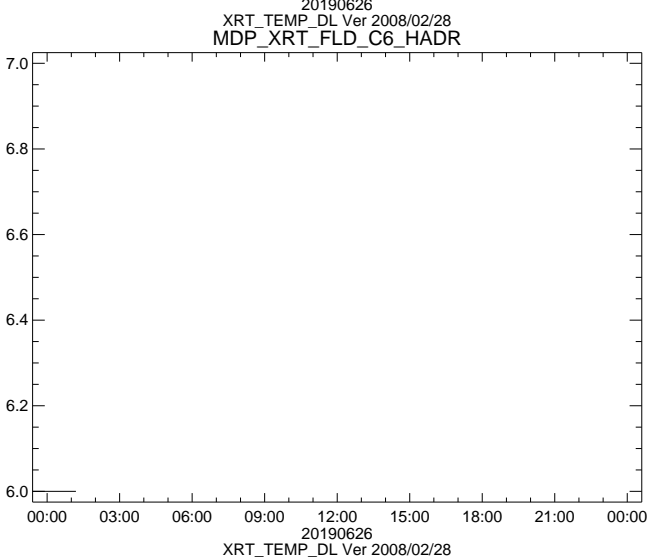
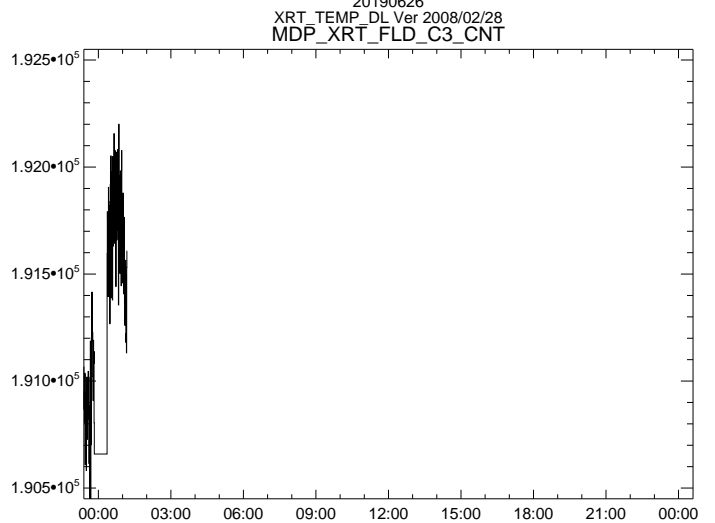
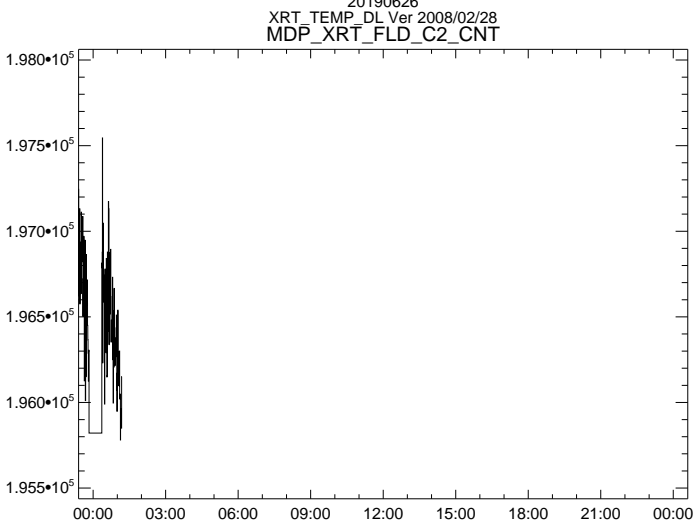
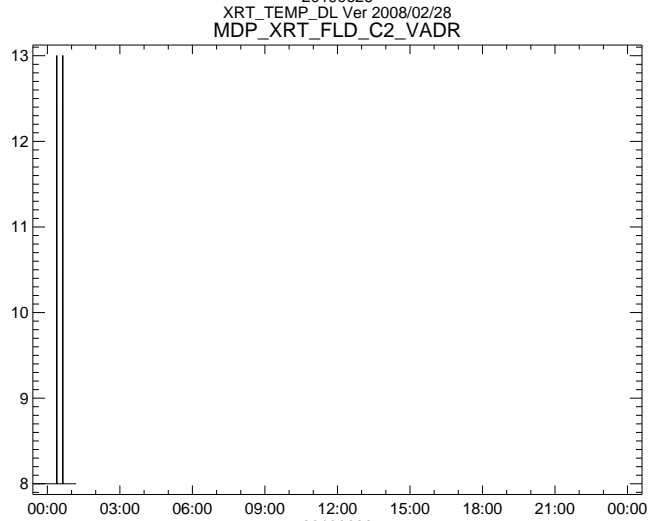
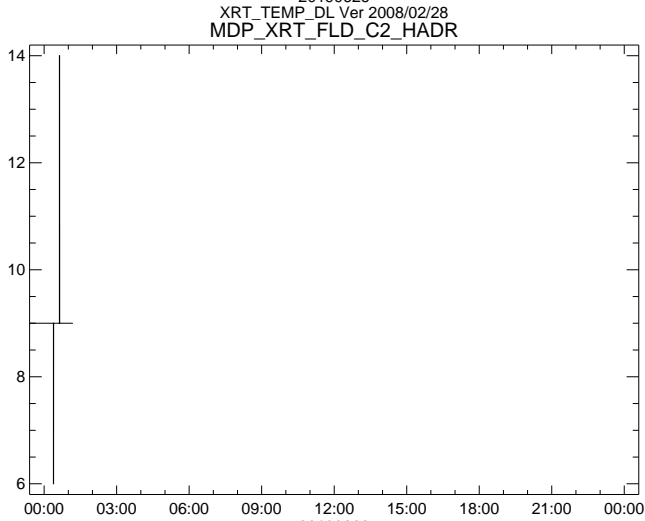
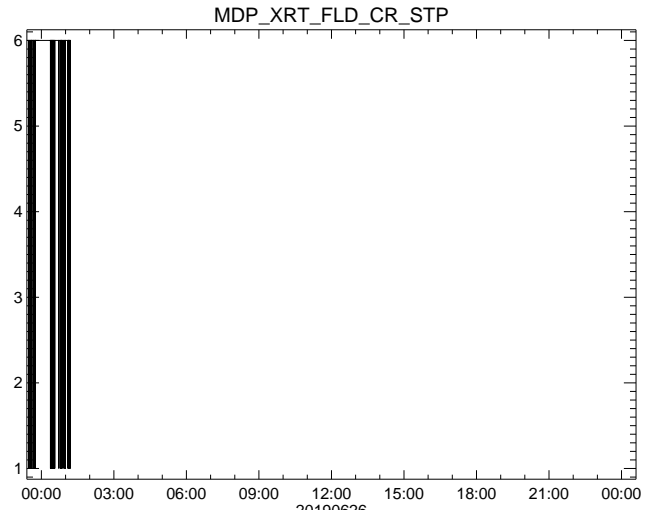
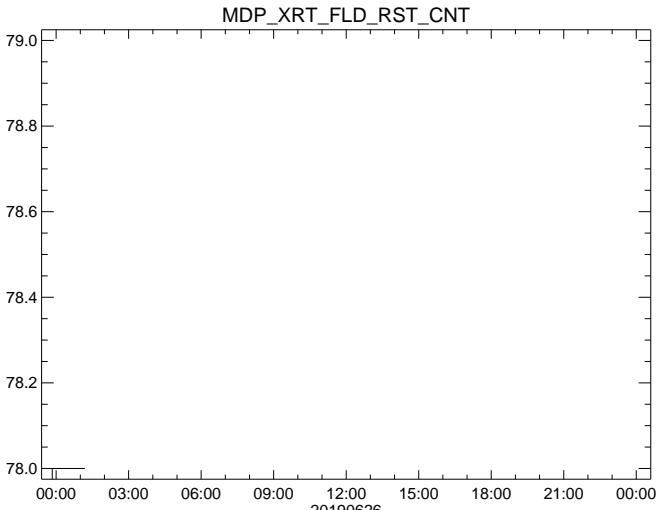


20190626
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_RD_OCR_TIM

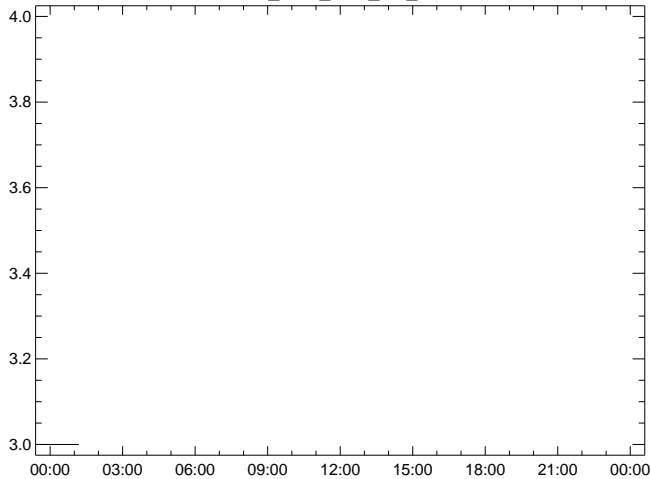


20190626
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_RD_TRM_TIM

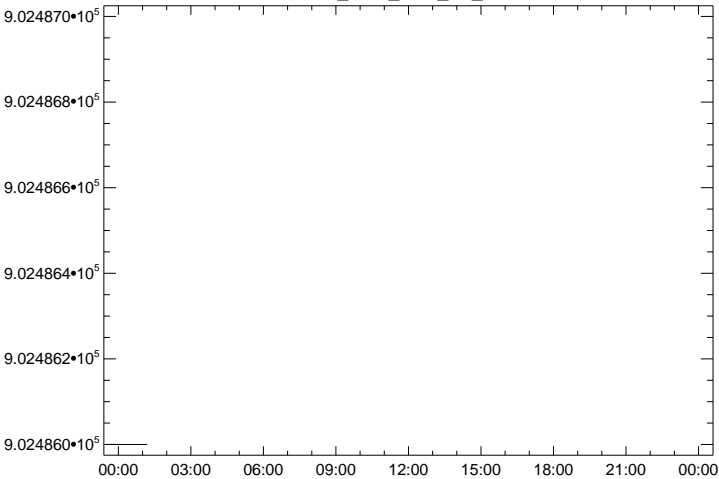




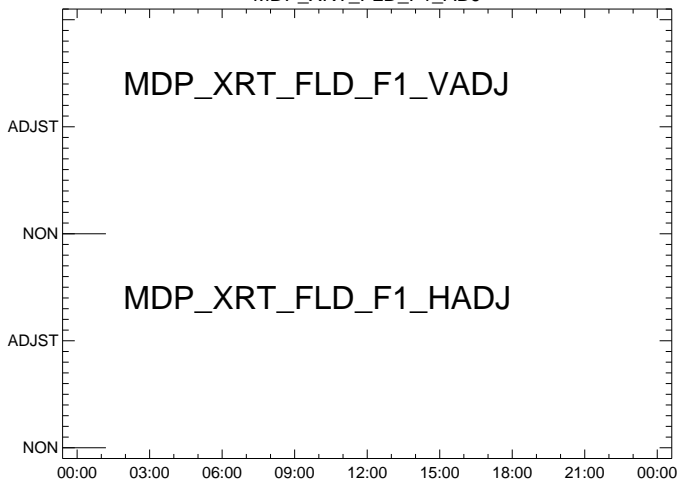
MDP_XRT_FLD_FN_STP



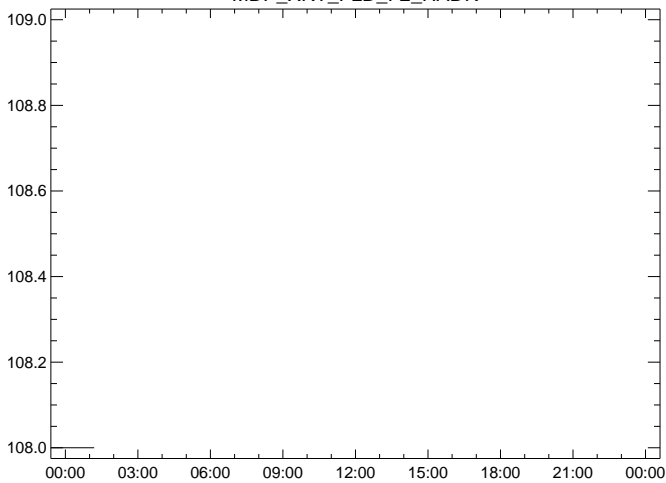
MDP_XRT_FLD_F1_CNT



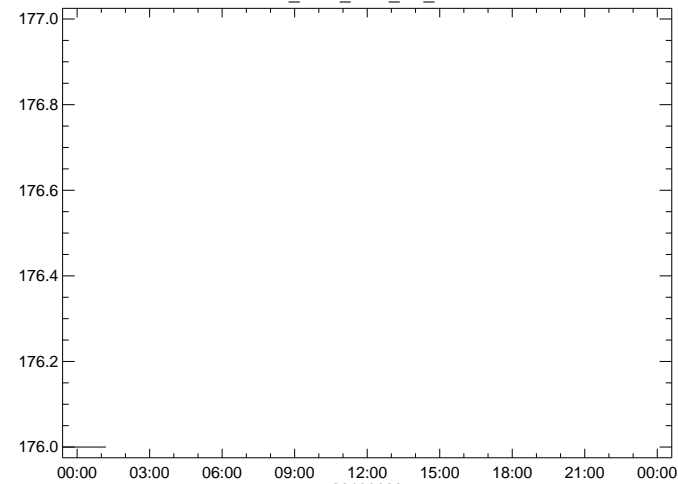
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_FLD_F1_ADJ



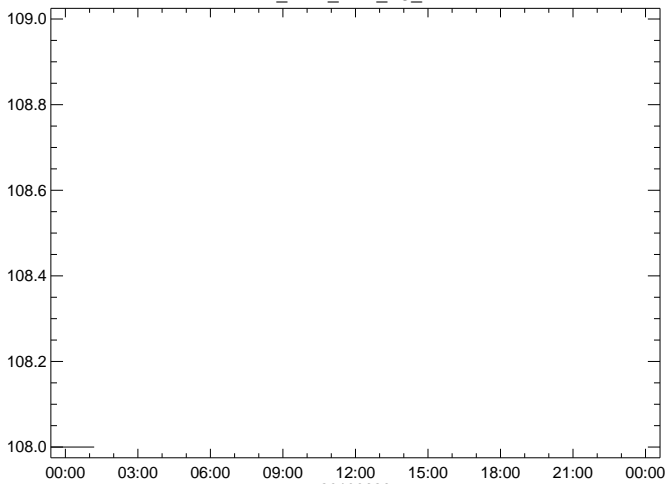
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_FLD_F2_HADR



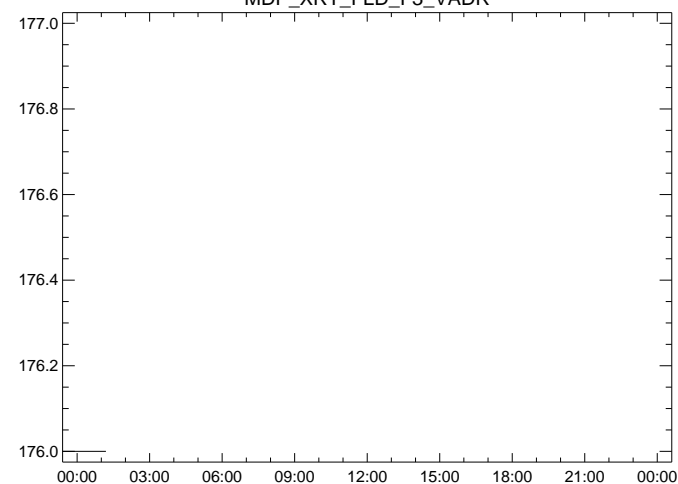
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_FLD_F2_VADR



XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_FLD_F3_HADR

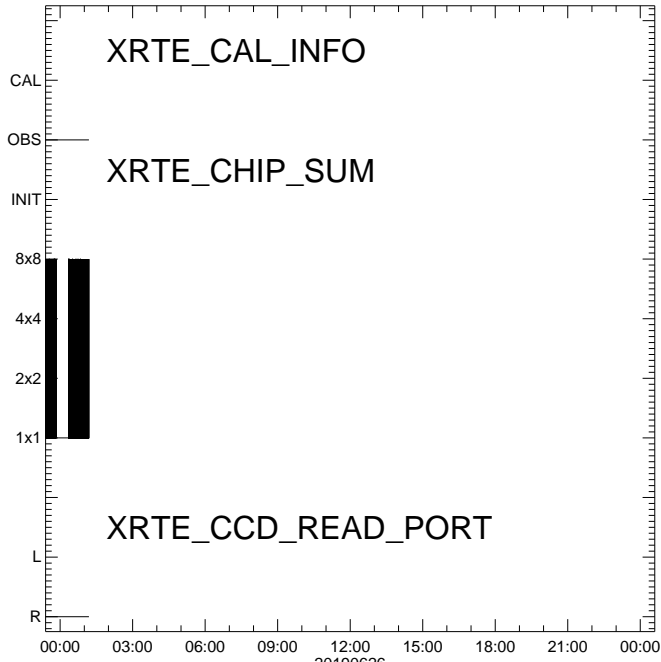


XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_FLD_F3_VADR

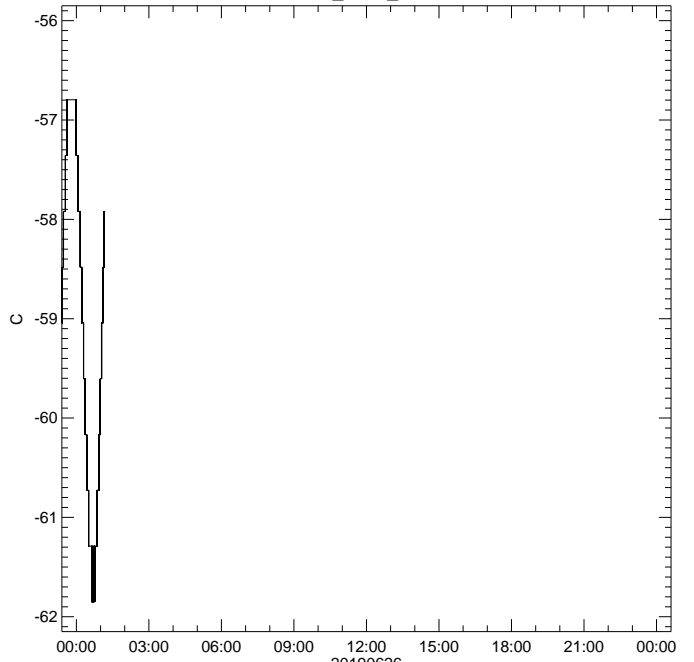


XRT_TEMP_DL Ver 2008/02/28

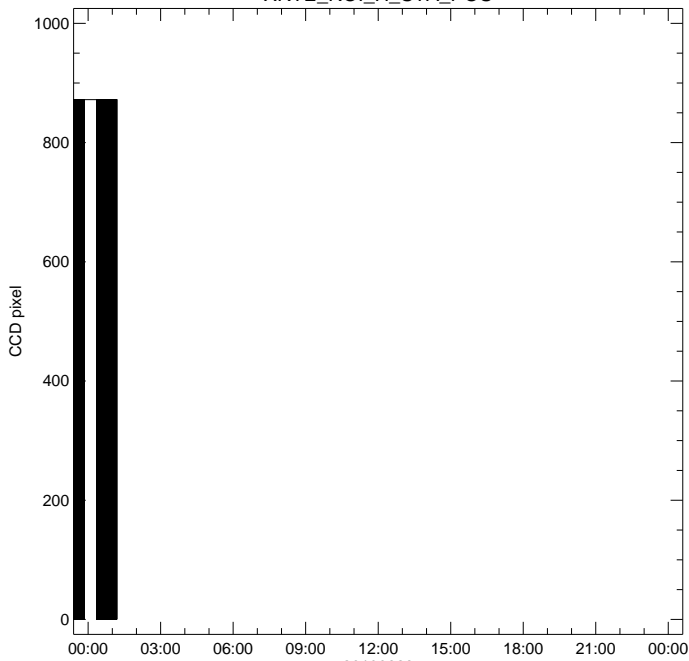
ON/OFF Statuses



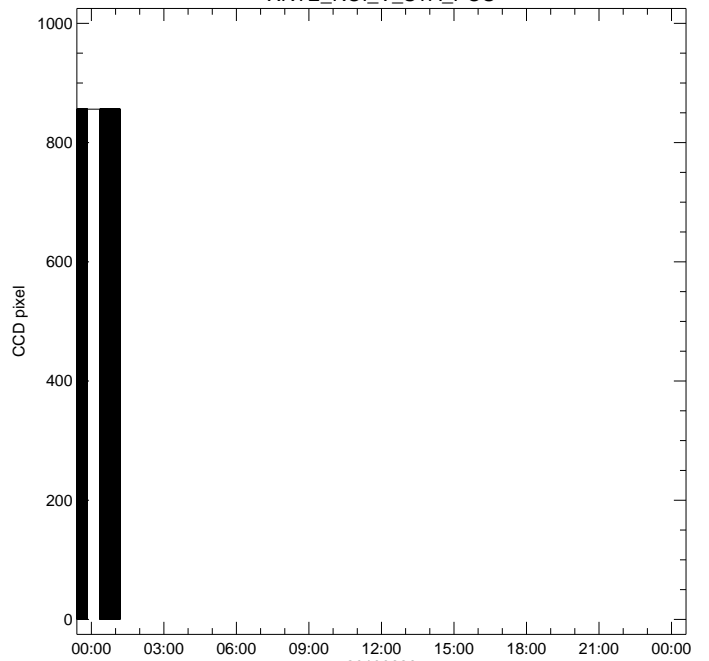
XRTE_CCD_TEMP



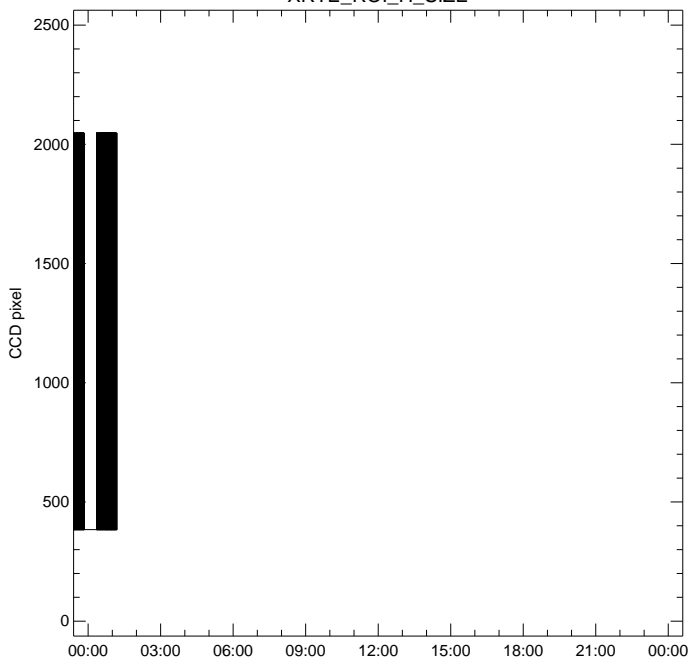
20190626
XRT_TEMP_DL Ver 2008/02/28
XRTE_ROI_H_STA_POS



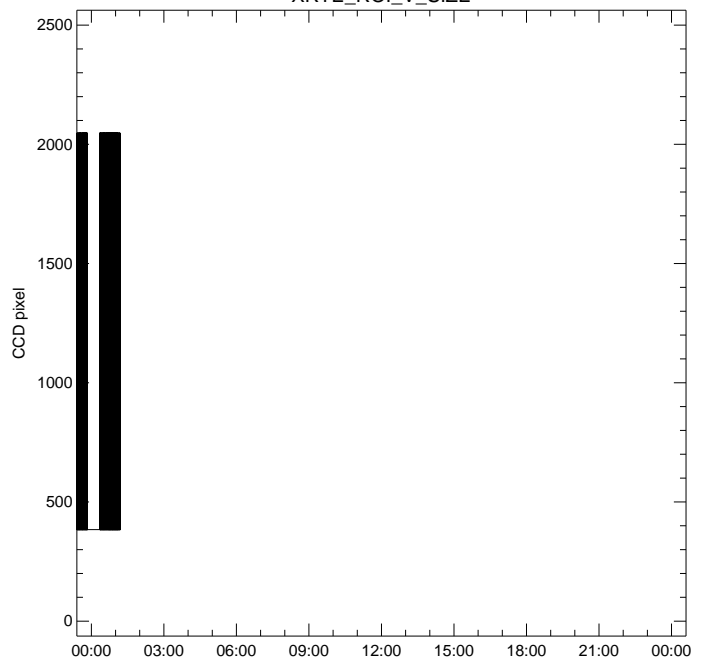
20190626
XRT_TEMP_DL Ver 2008/02/28
XRTE_ROI_V_STA_POS



20190626
XRT_TEMP_DL Ver 2008/02/28
XRTE_ROI_H_SIZE



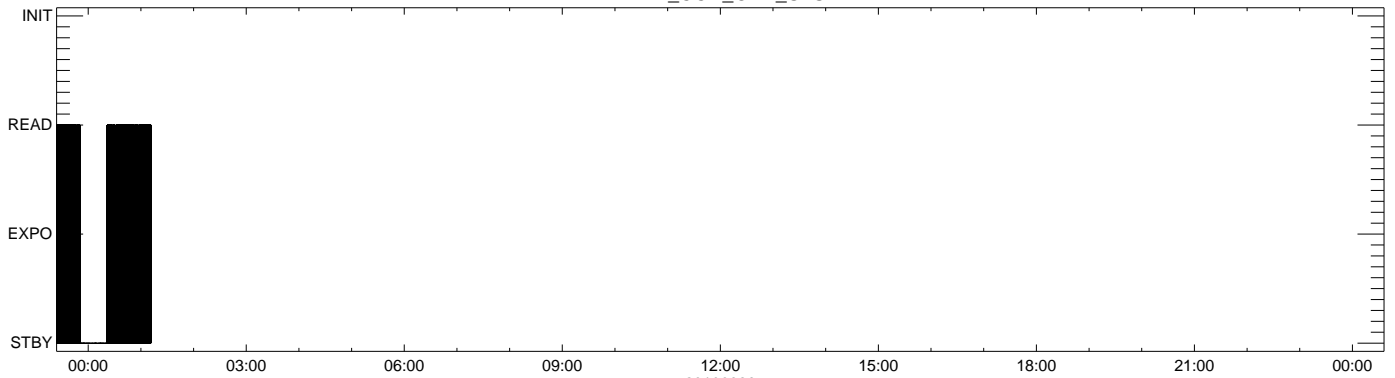
20190626
XRT_TEMP_DL Ver 2008/02/28
XRTE_ROI_V_SIZE



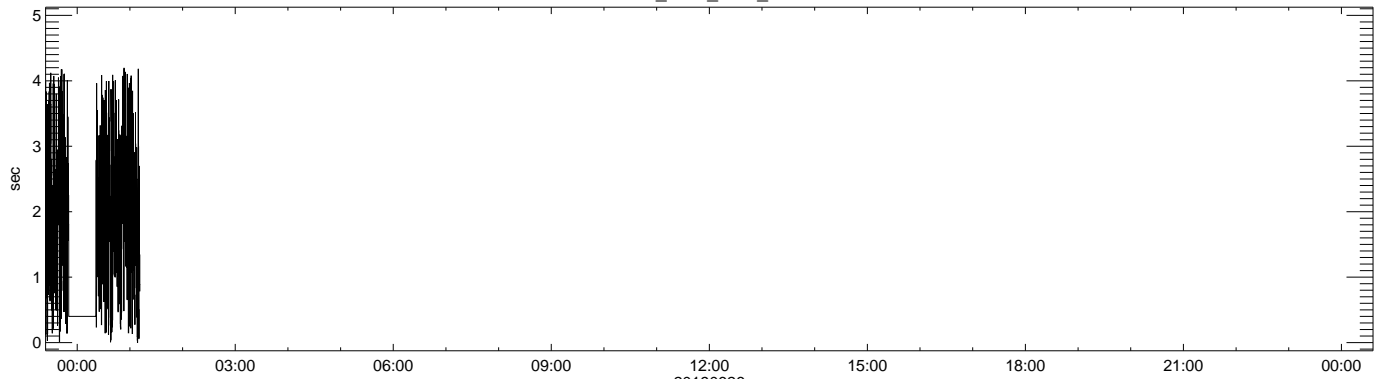
20190626
XRT_TEMP_DL Ver 2008/02/28

20190626
XRT_TEMP_DL Ver 2008/02/28

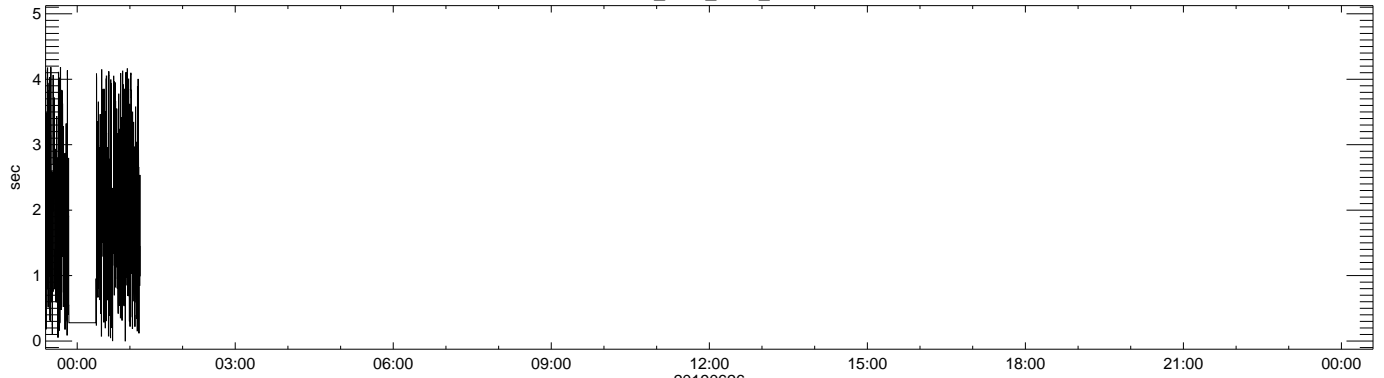
XRTE_CCD_OPE_STS



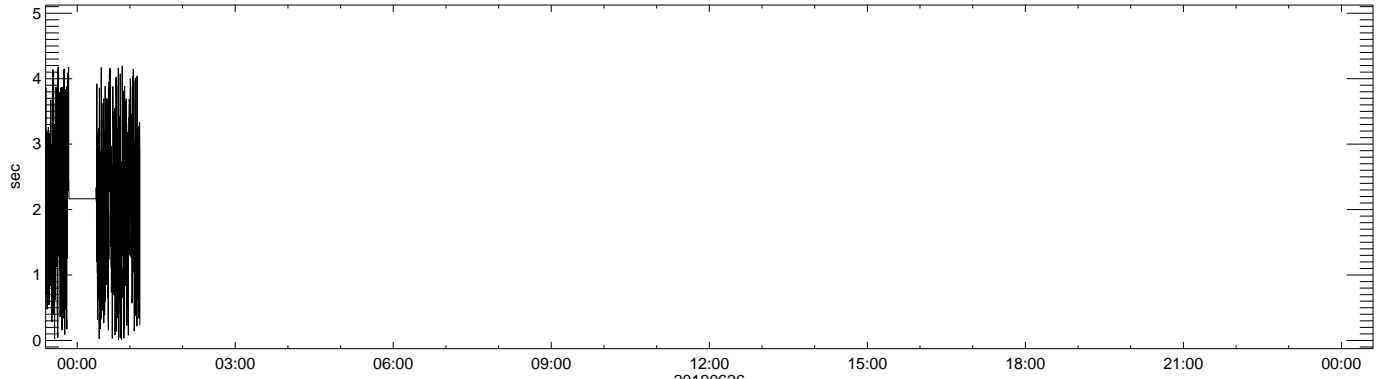
12:00
20190626
XRT_TEMP_DL Ver 2008/02/28
XRTE_EXP_STA_TIME



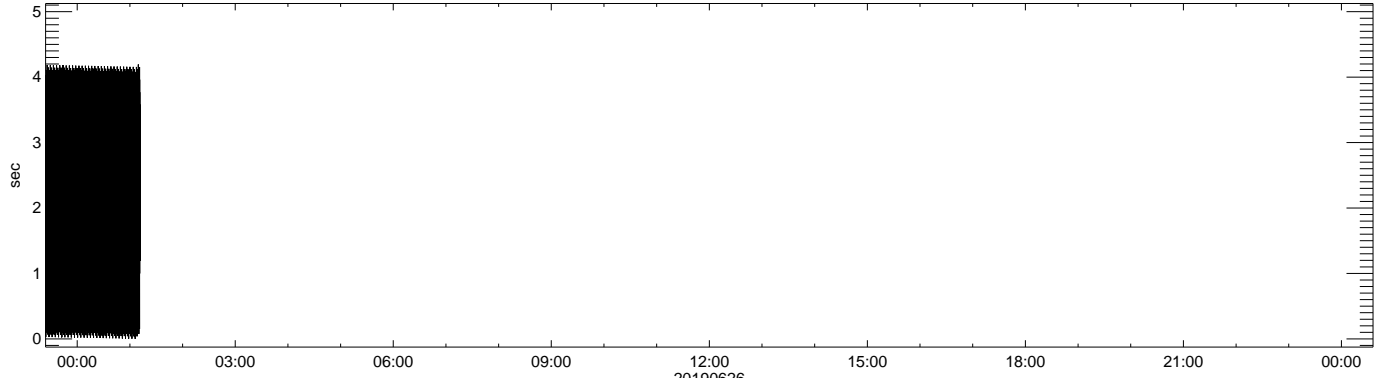
12:00
20190626
XRT_TEMP_DL Ver 2008/02/28
XRTE_EXP_END_TIME



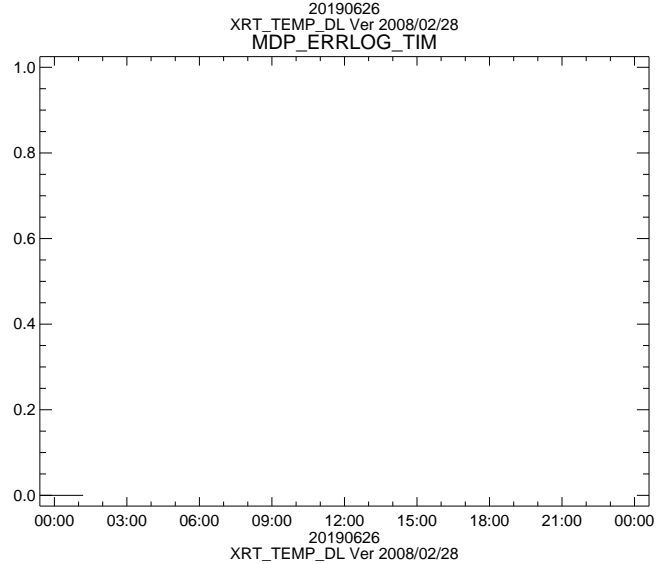
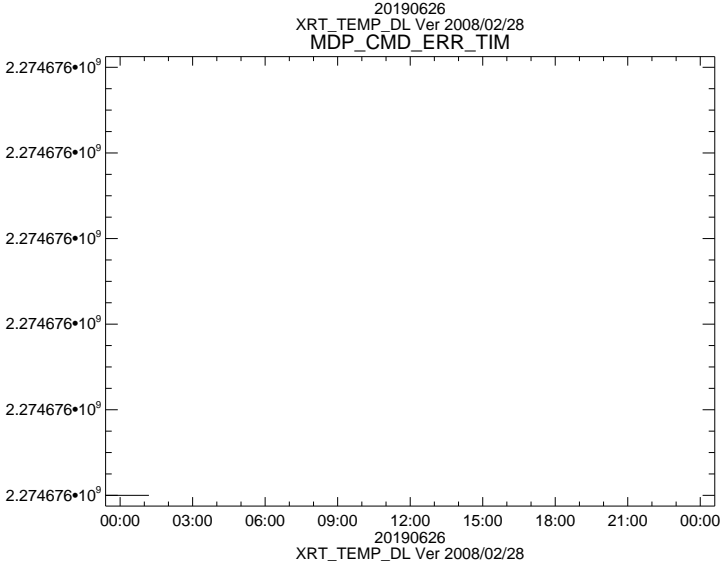
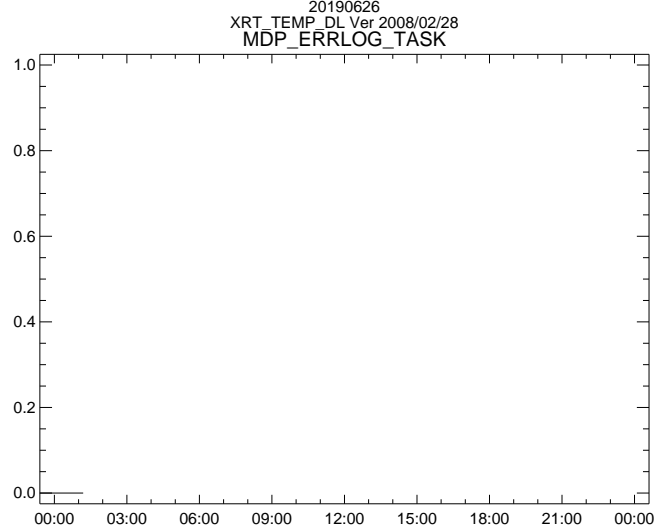
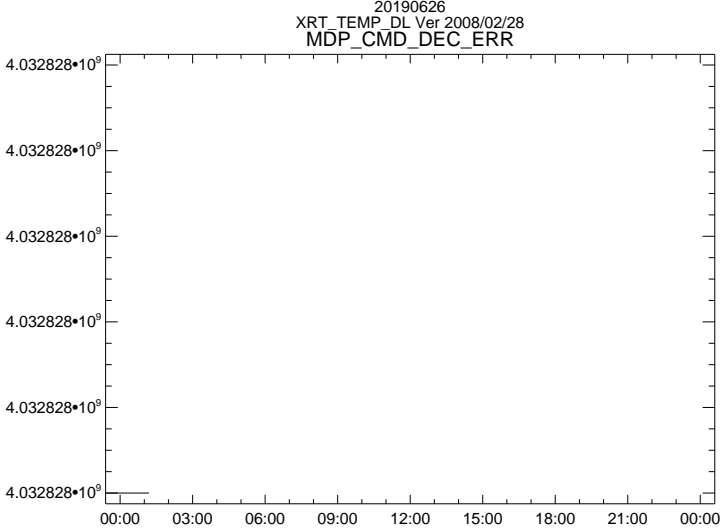
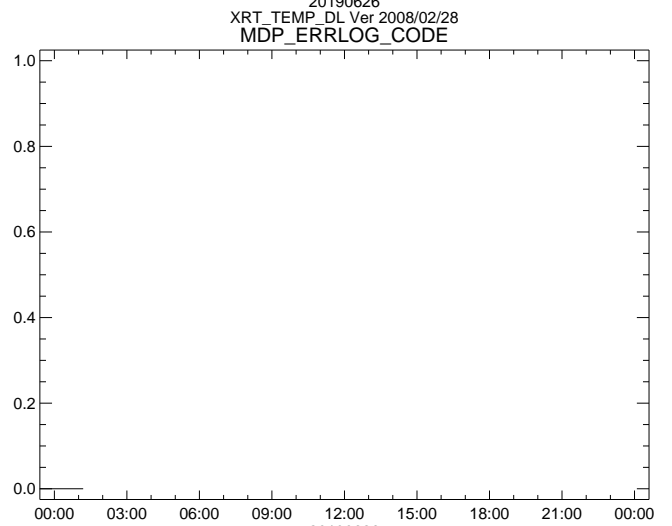
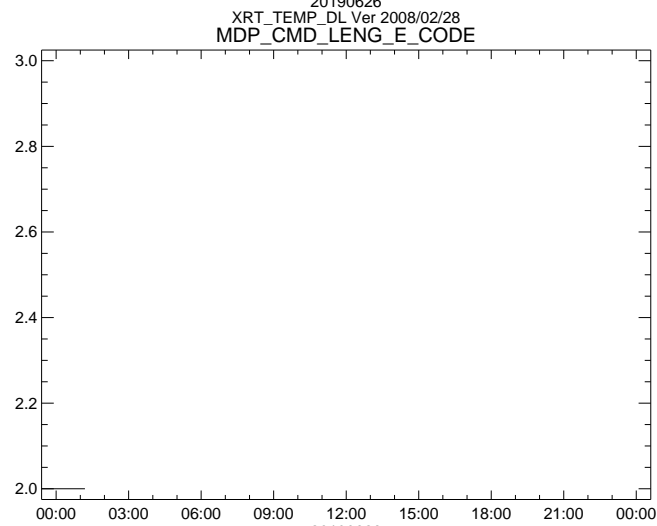
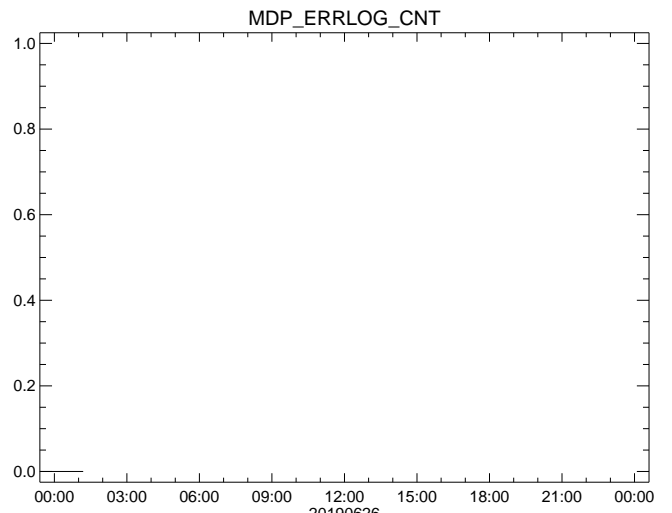
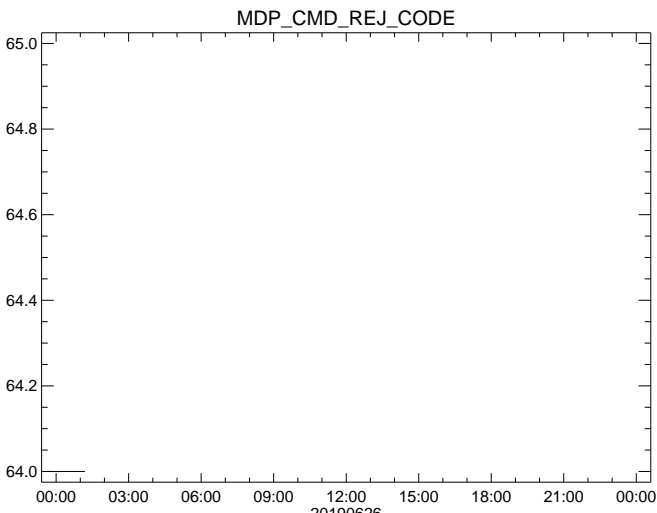
12:00
20190626
XRT_TEMP_DL Ver 2008/02/28
XRTE_READ_END_TIME



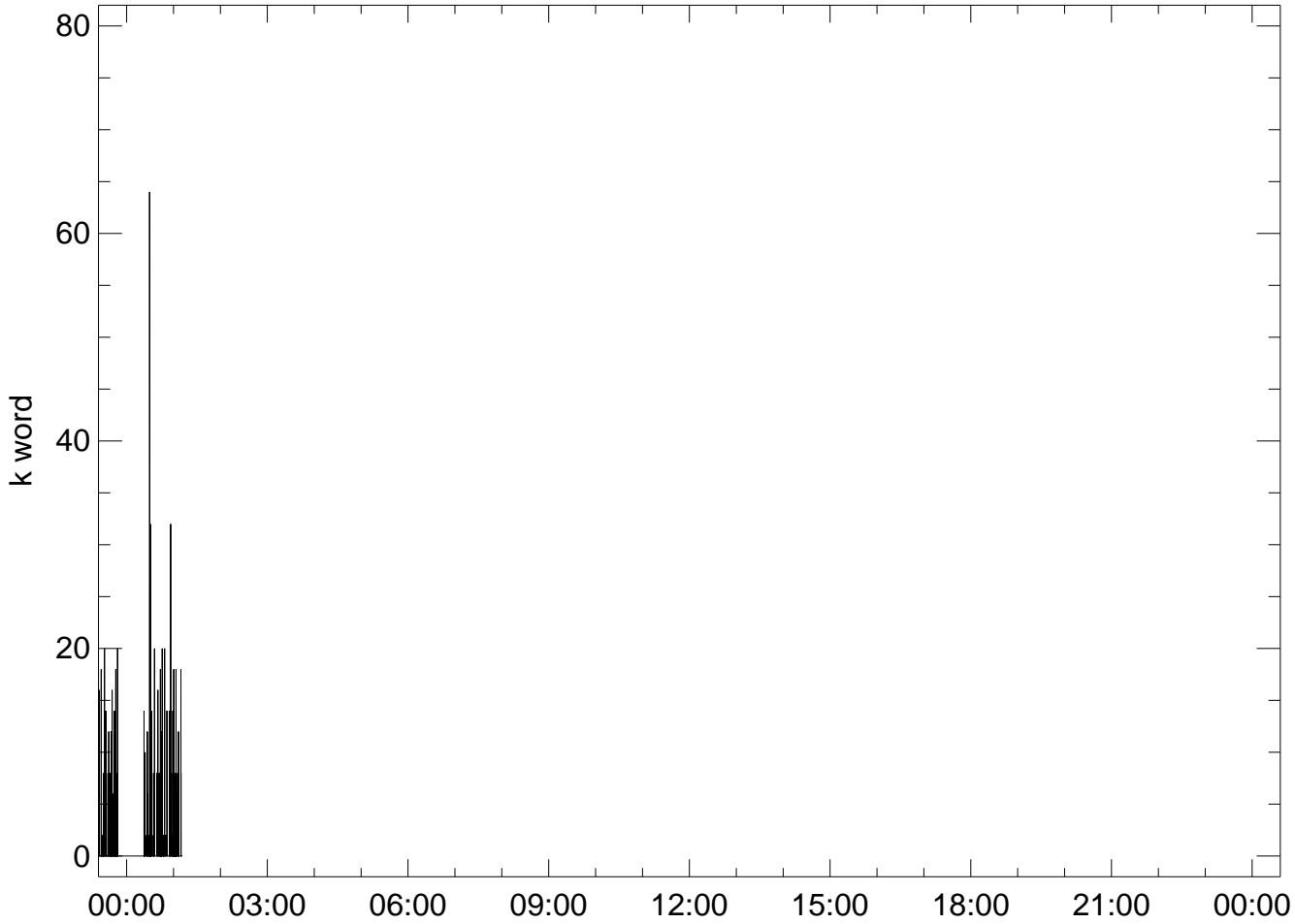
12:00
20190626
XRT_TEMP_DL Ver 2008/02/28
XRTE_TIMER_COUNTER



12:00
20190626
XRT_TEMP_DL Ver 2008/02/28

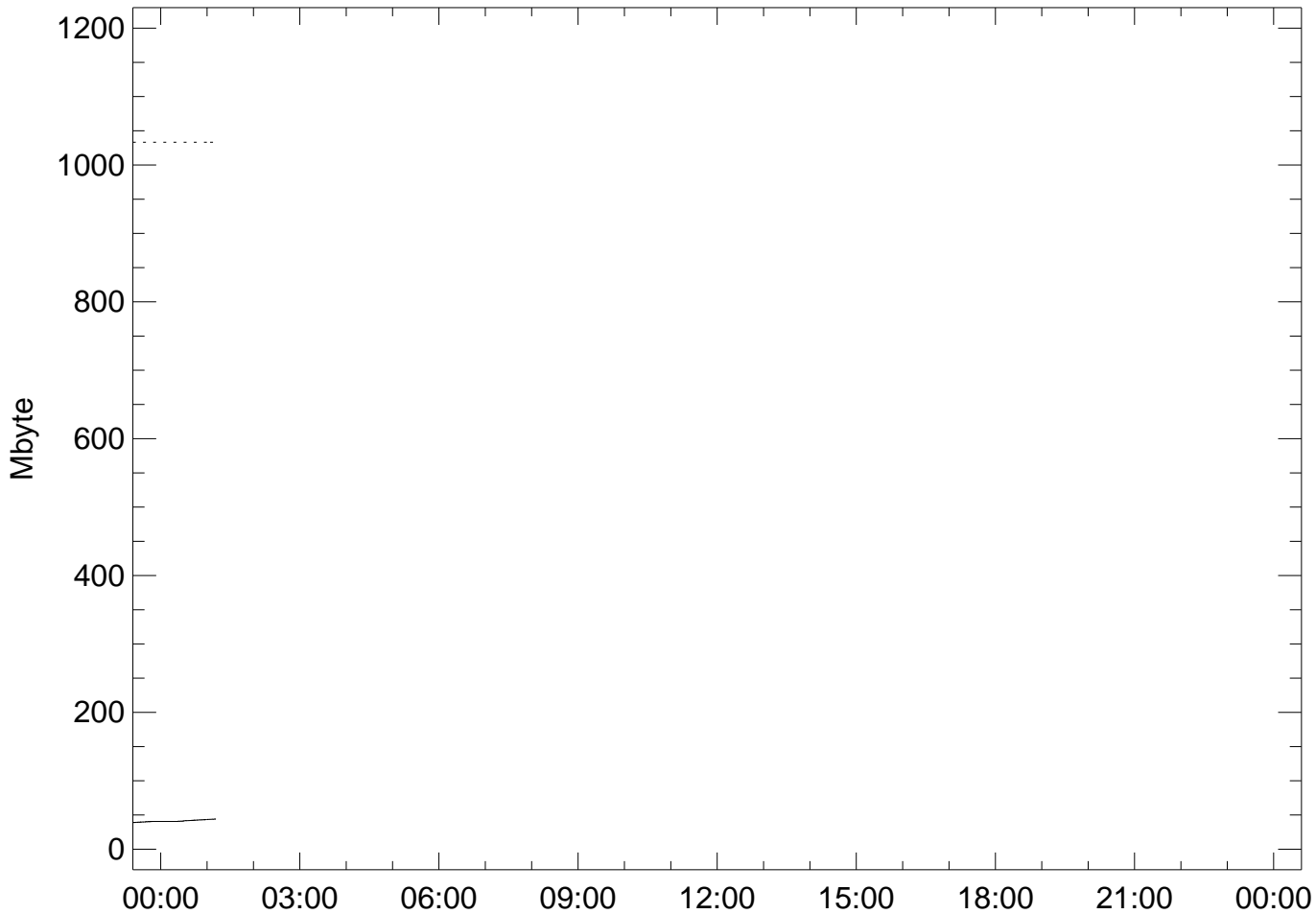


MDP_PST_CMP_XRT_ACM



20190626

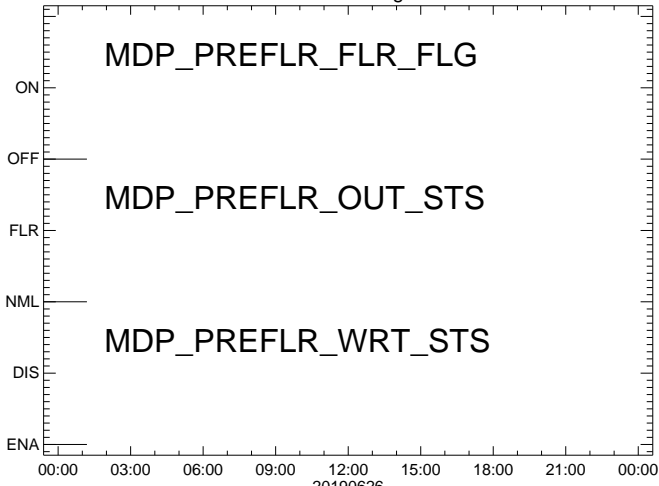
XRT_TEMP_DL Ver 2008/02/28 MDP_XRT_TLM_LIM & CNT



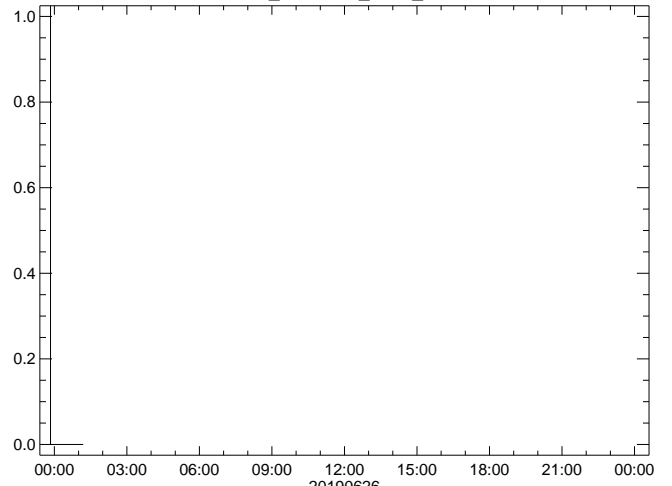
20190626

XRT_TEMP_DL Ver 2008/02/28

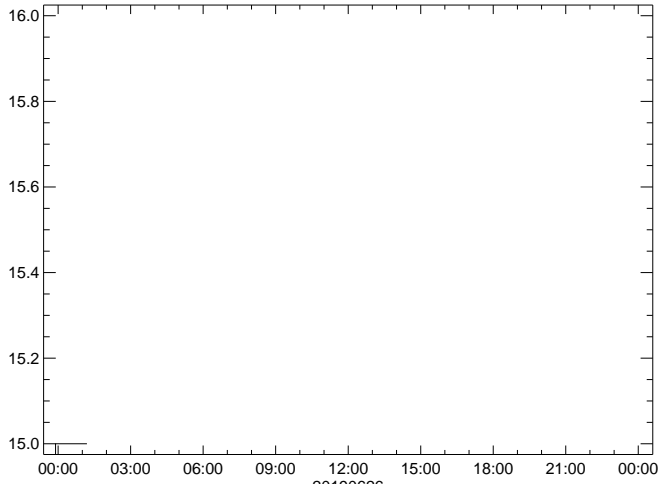
PreFlare Flags



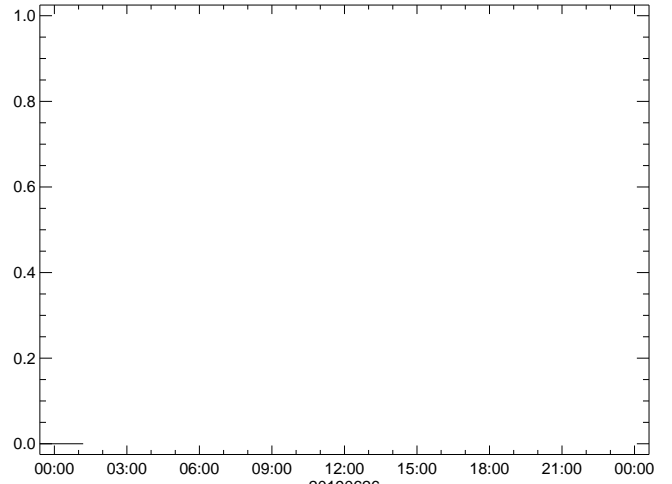
MDP_PREFLR_OUT_CNT



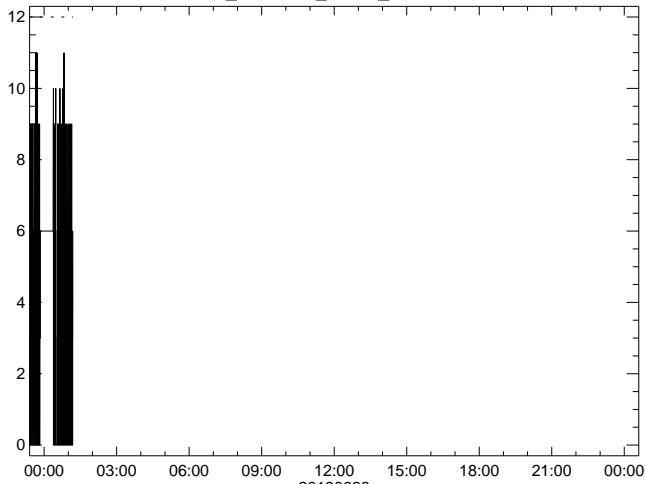
MDP_PREFLR_STP_CNT



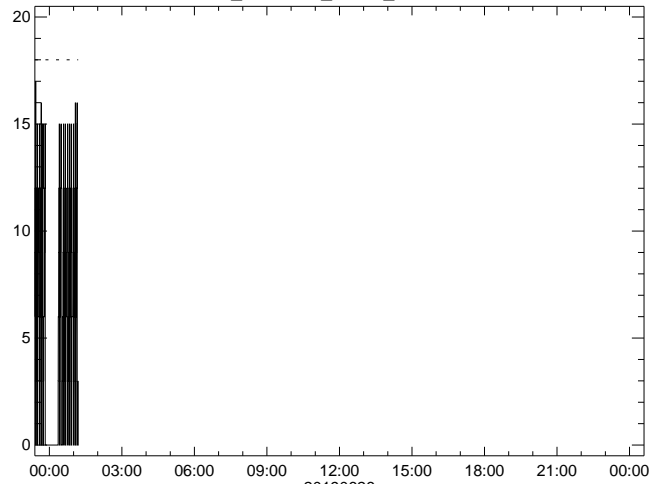
MDP_PREFLR_RST_CNT



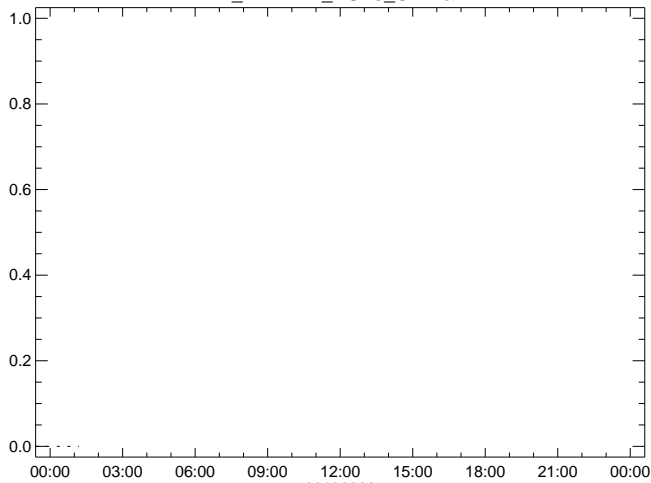
MDP_PREFLR_BUF1_CNT & WP



MDP_PREFLR_BUF2_CNT & WP



MDP_PREFLR_BUF3_CNT & WP



XRT_TEMP_DL Ver 2008/02/28

