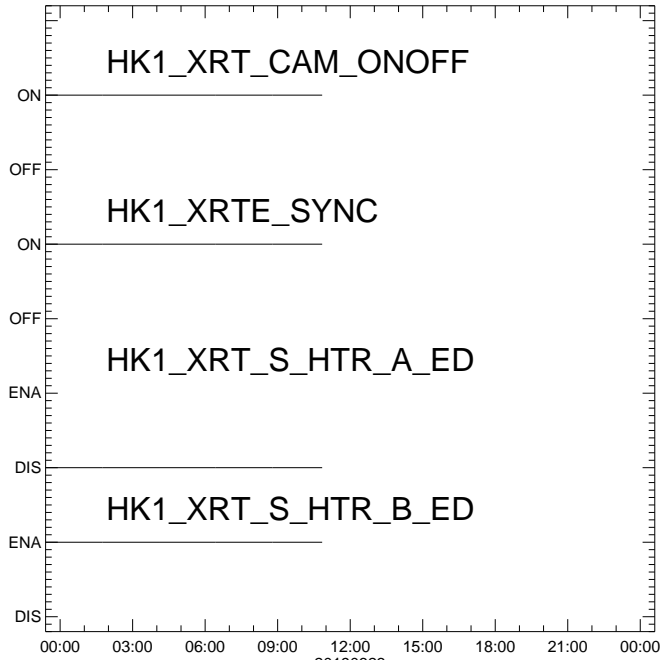
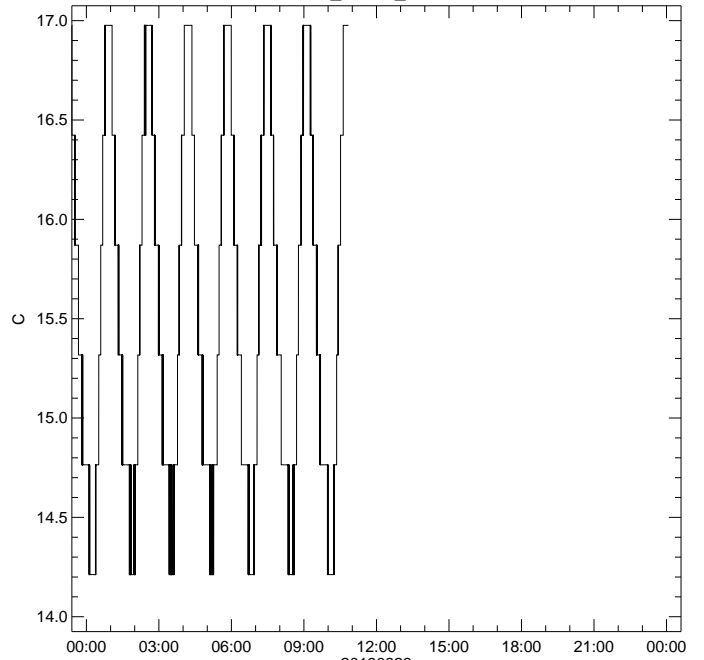


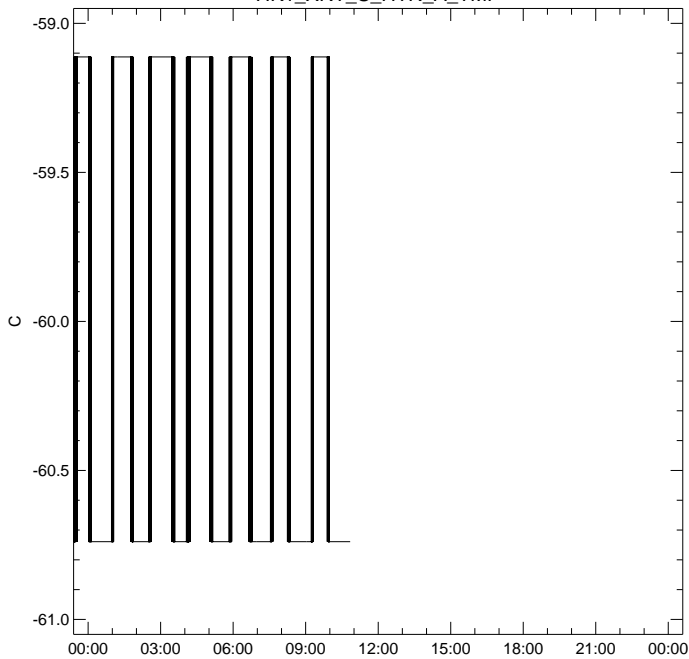
ON/OFF Statuses



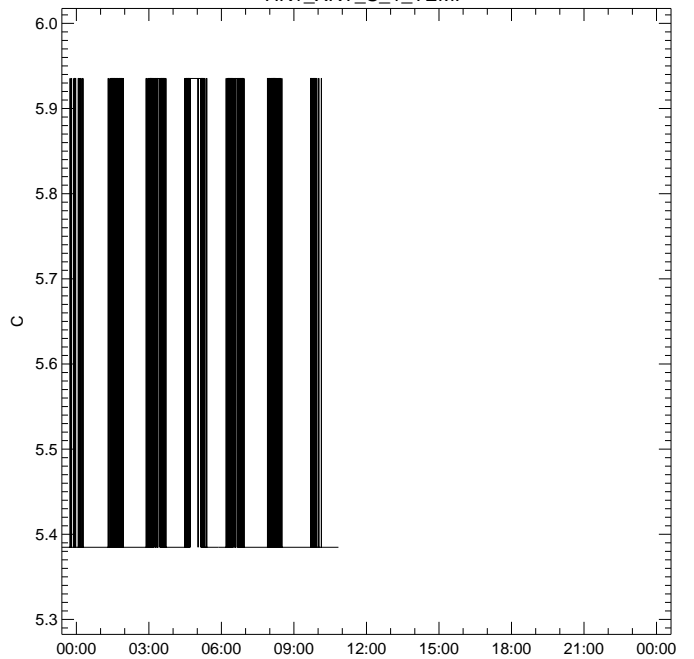
HK1_XRTE_TEMP



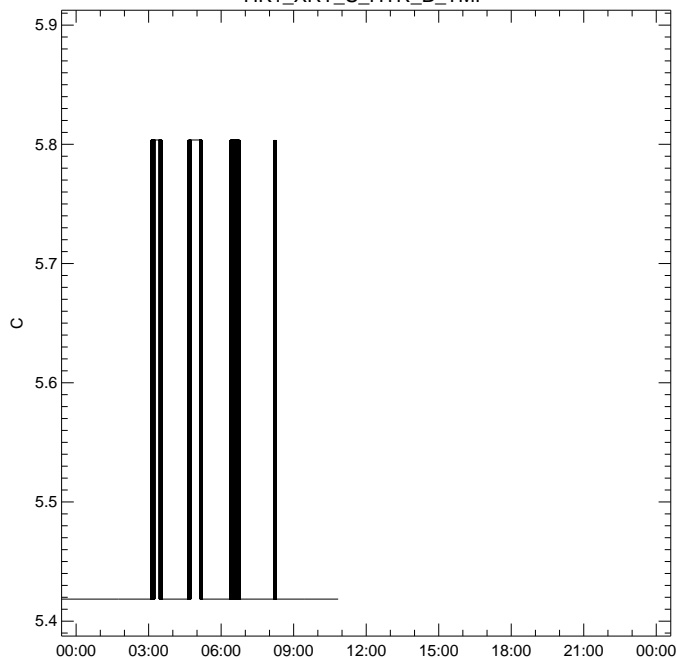
20190323
XRT_TEMP_DL Ver 2008/02/28
HK1_XRT_S_HTR_A_TEMP



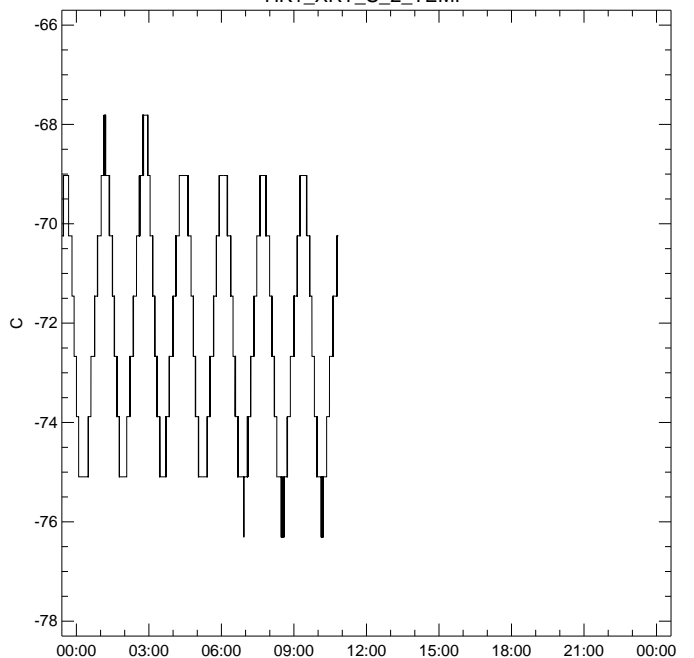
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XRT_TEMP_DL Ver 2008/02/28
HK1_XRT_S_1_TEMP

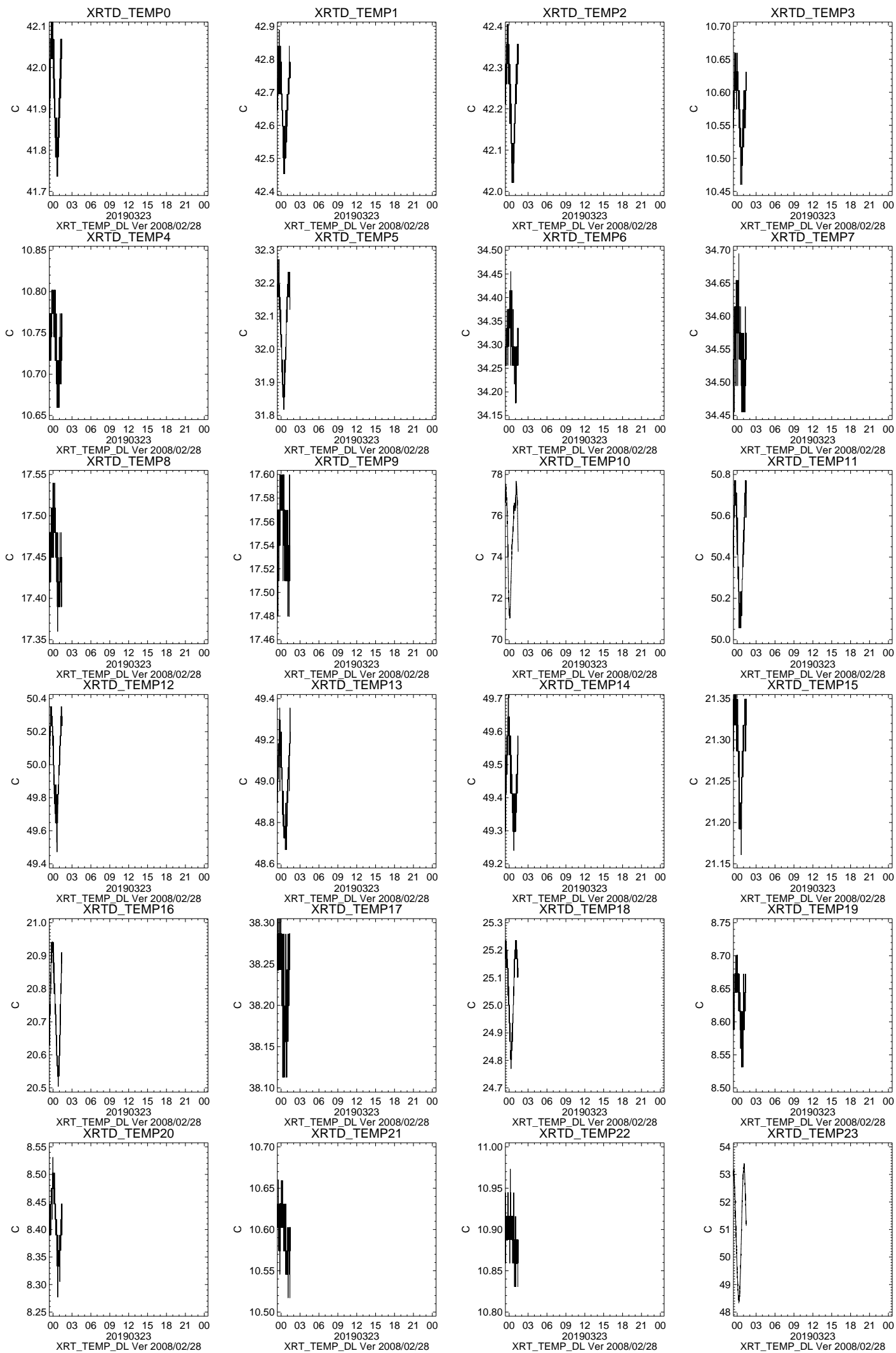


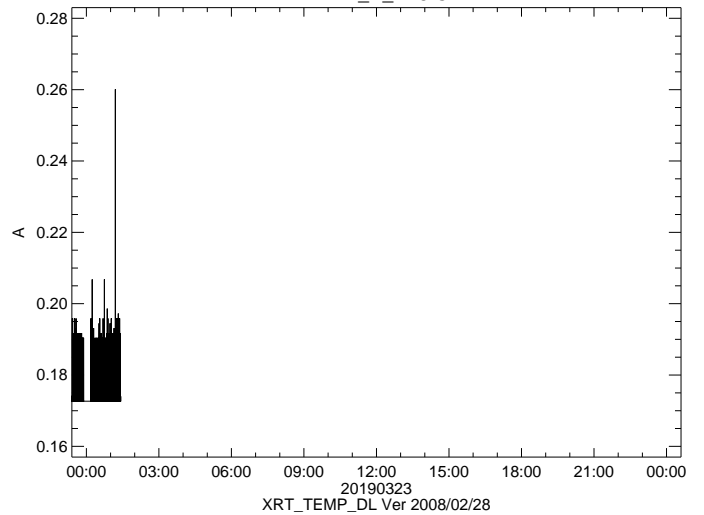
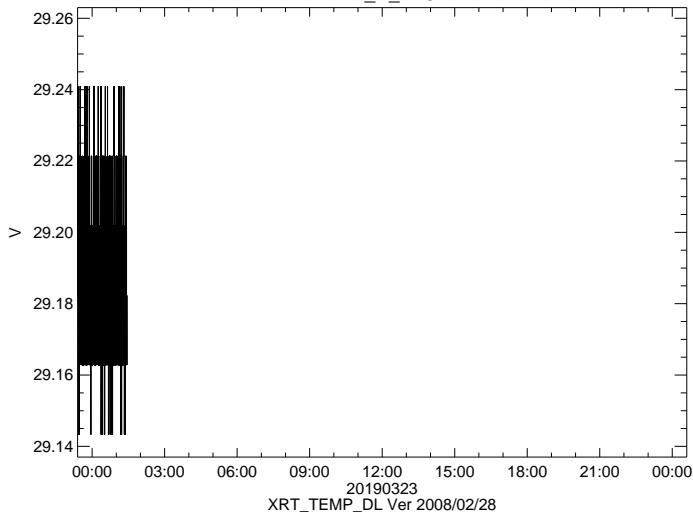
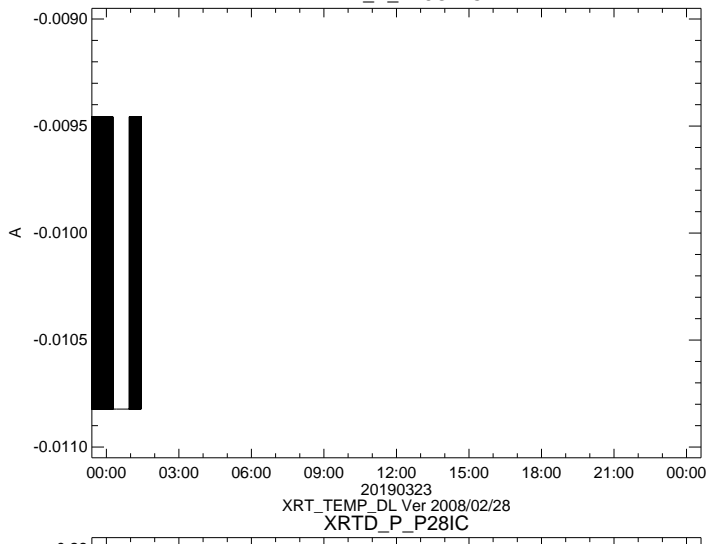
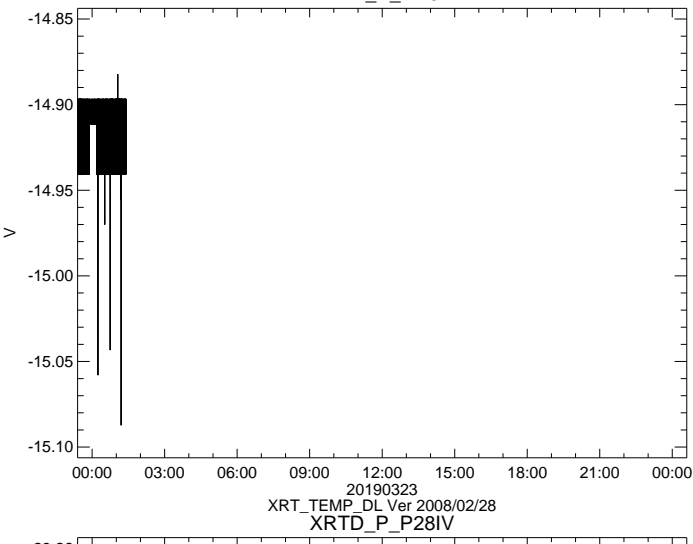
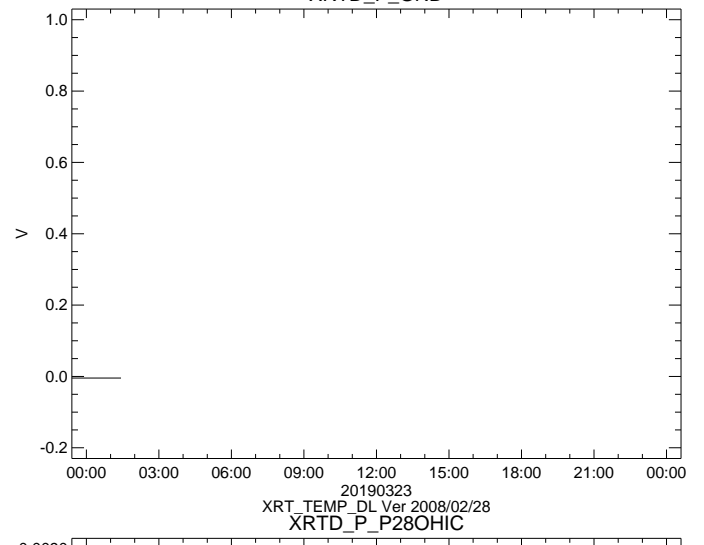
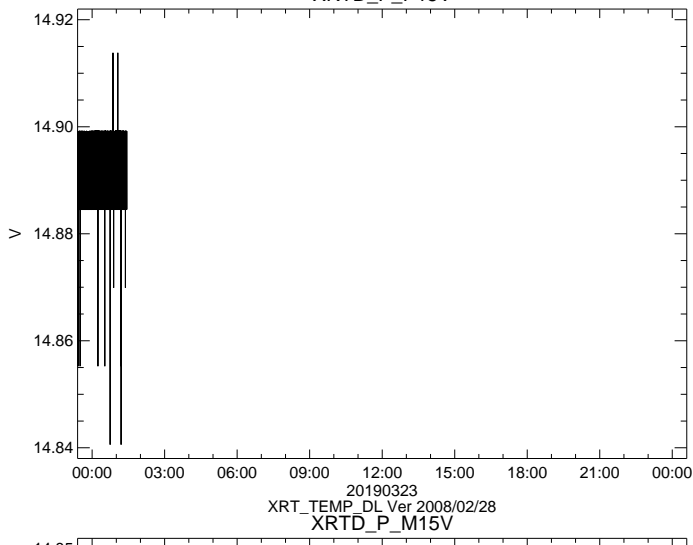
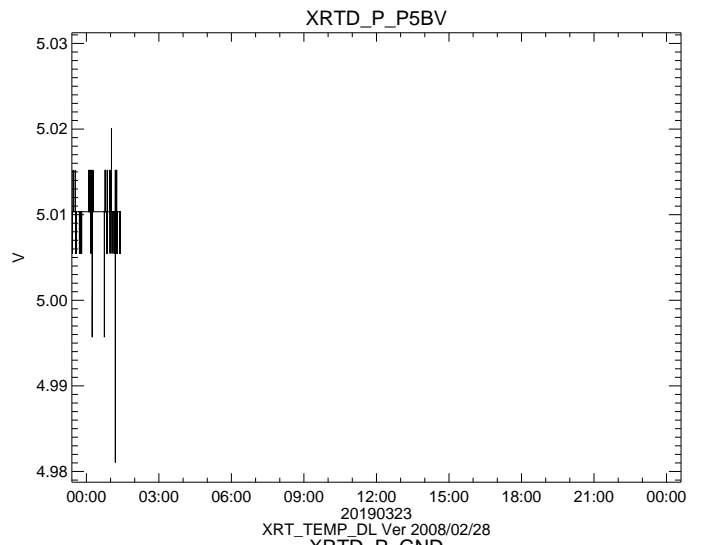
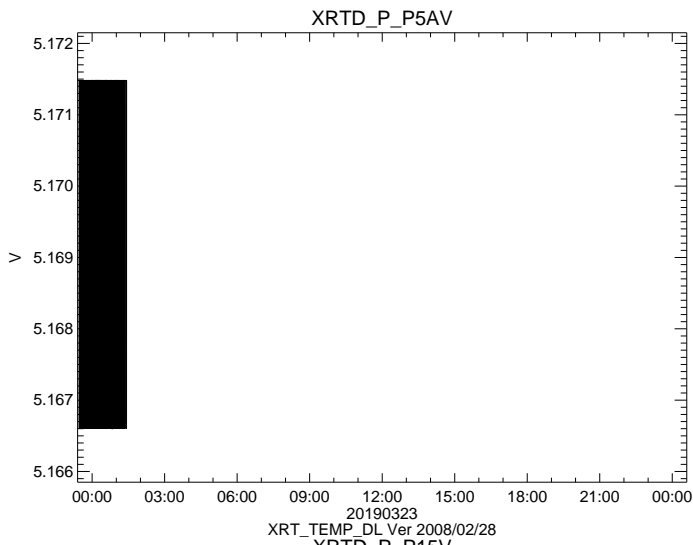
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HK1_XRT_S_HTR_B_TEMP

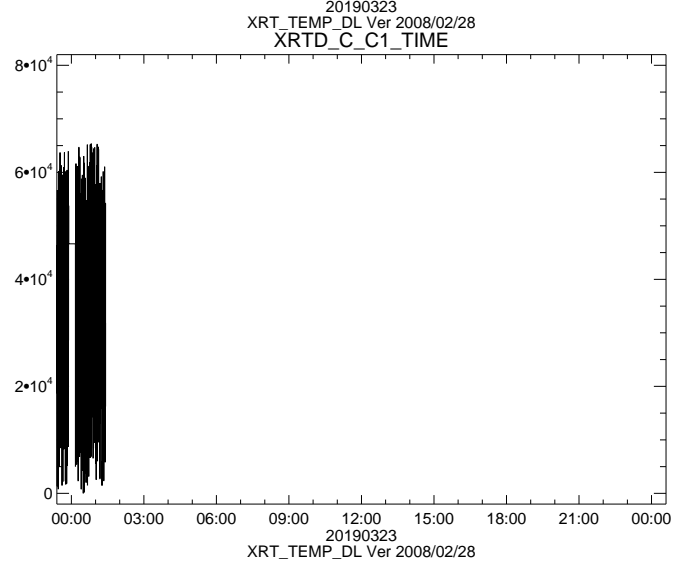
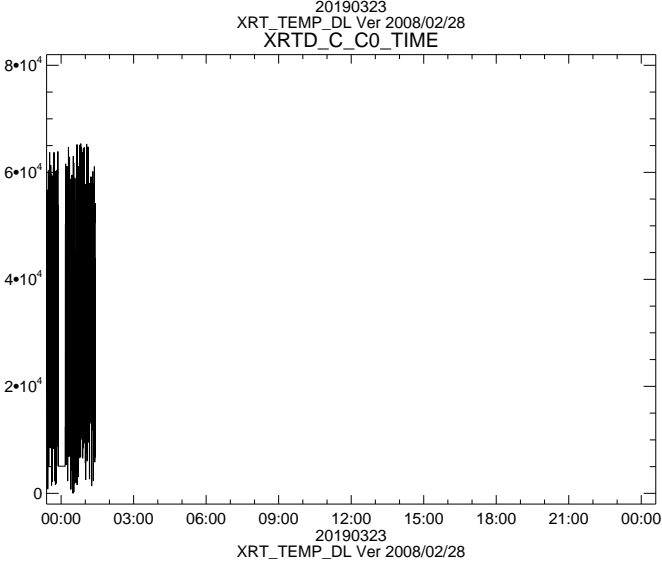
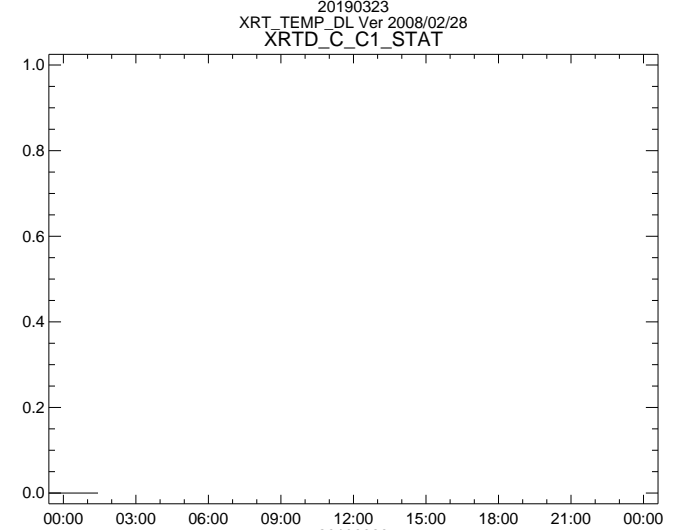
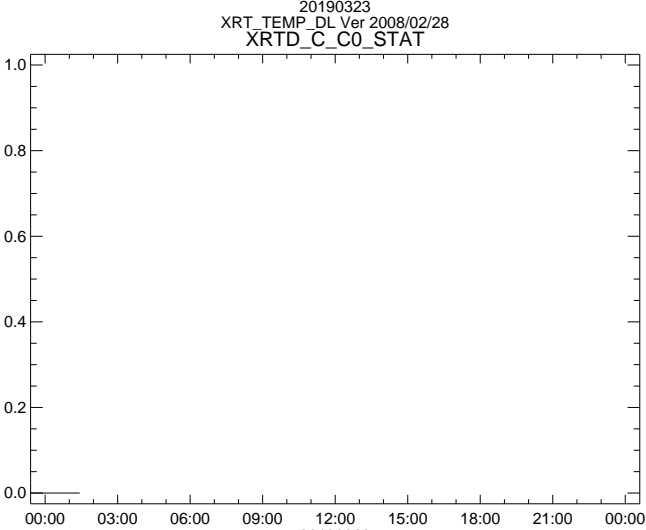
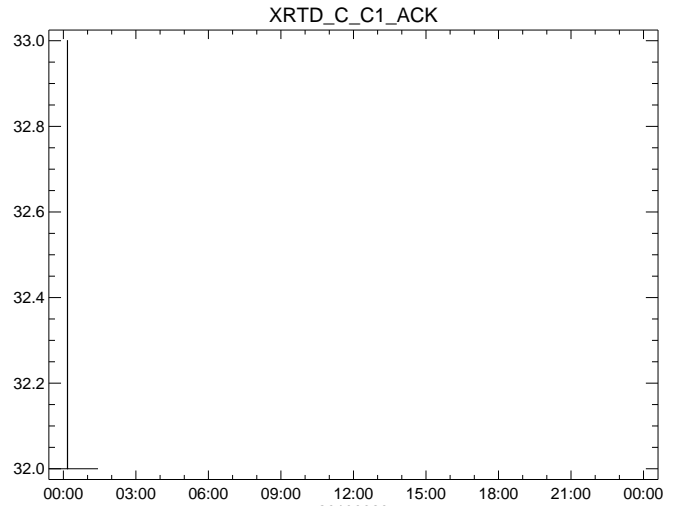
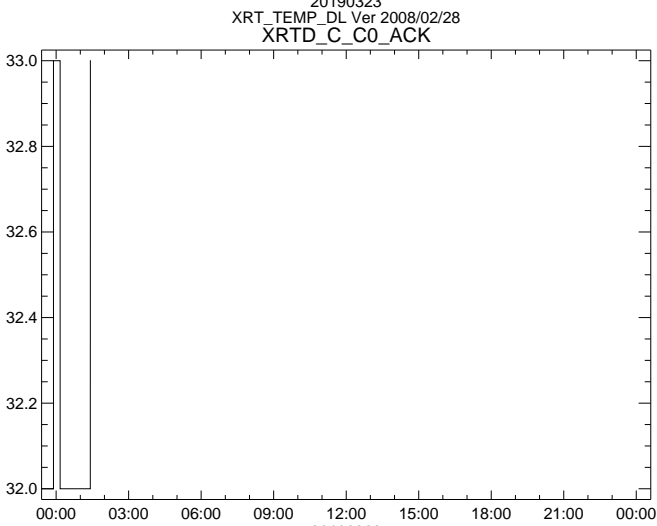
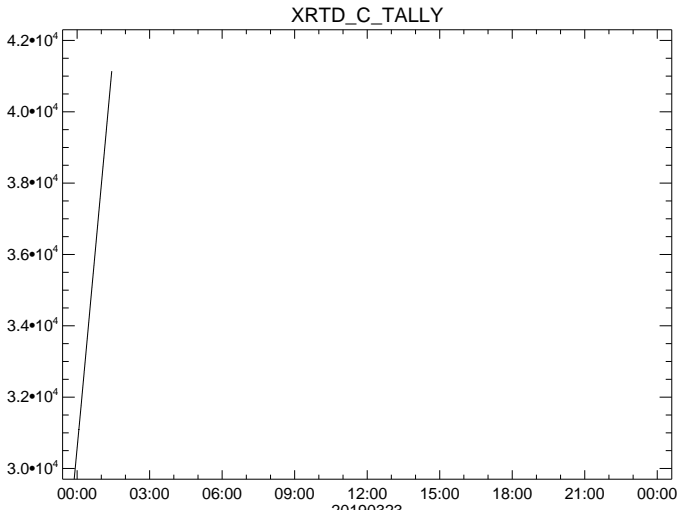


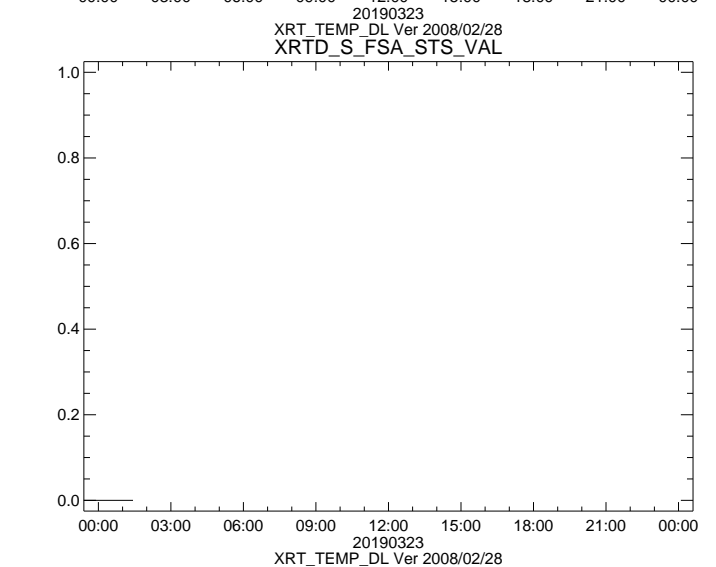
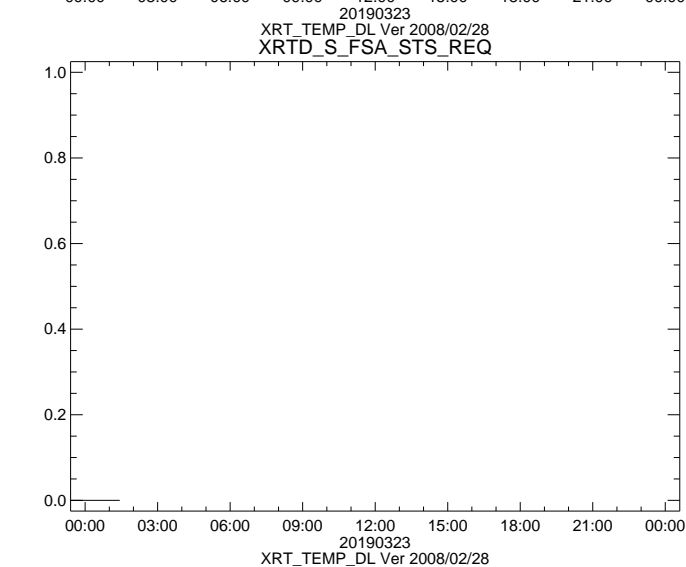
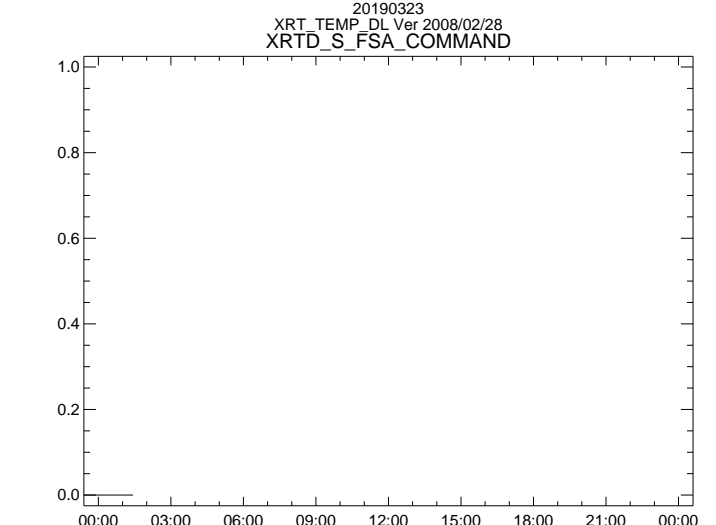
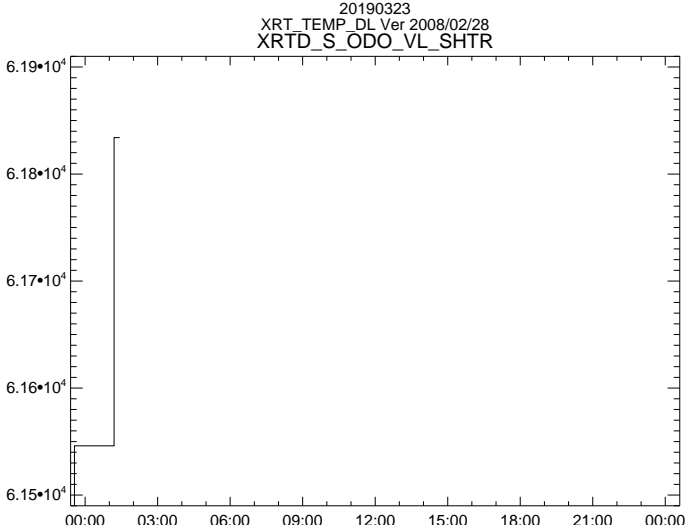
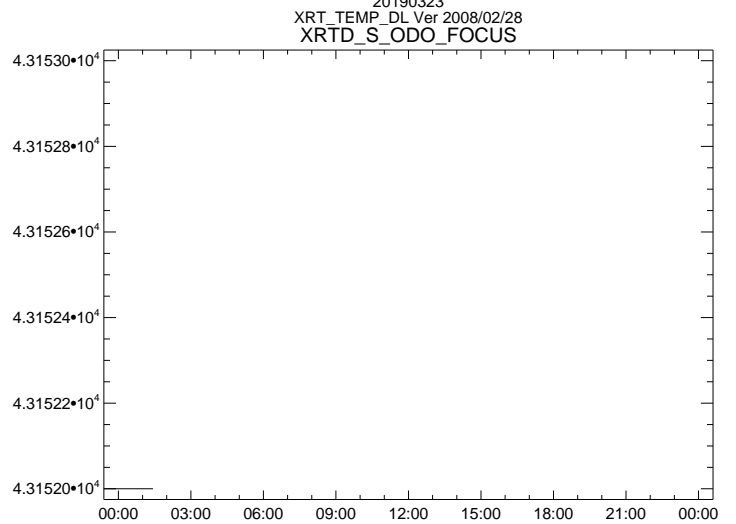
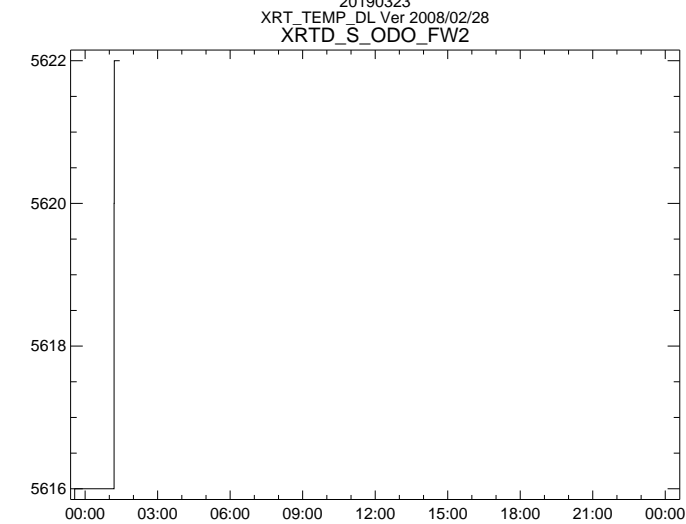
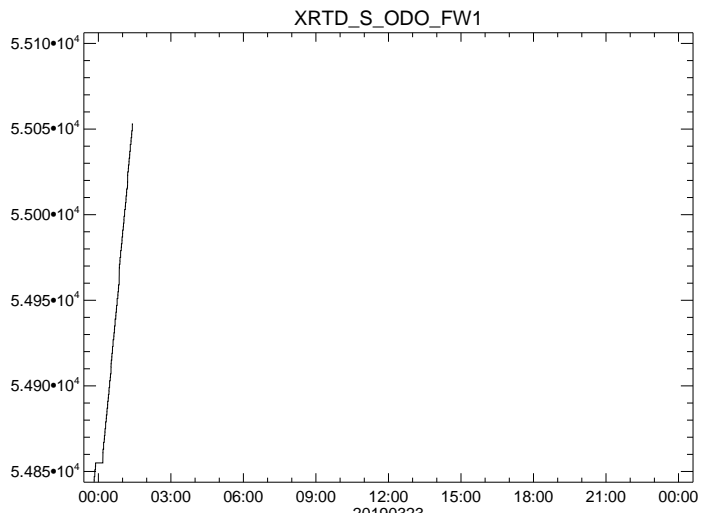
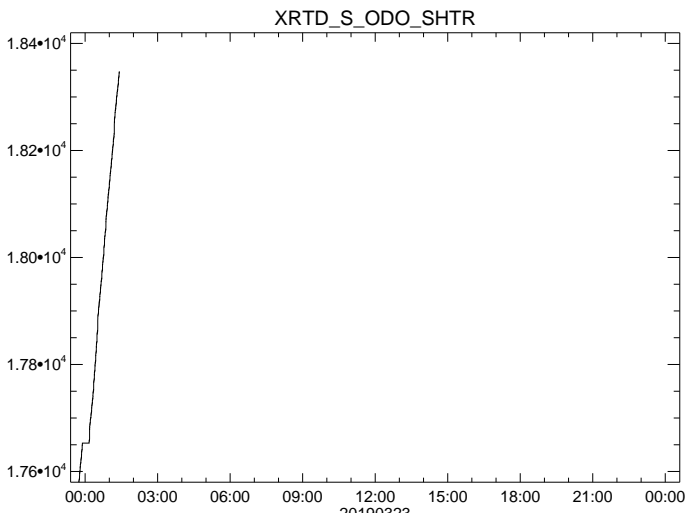
20190323
XRT_TEMP_DL Ver 2008/02/28
HK1_XRT_S_2_TEMP

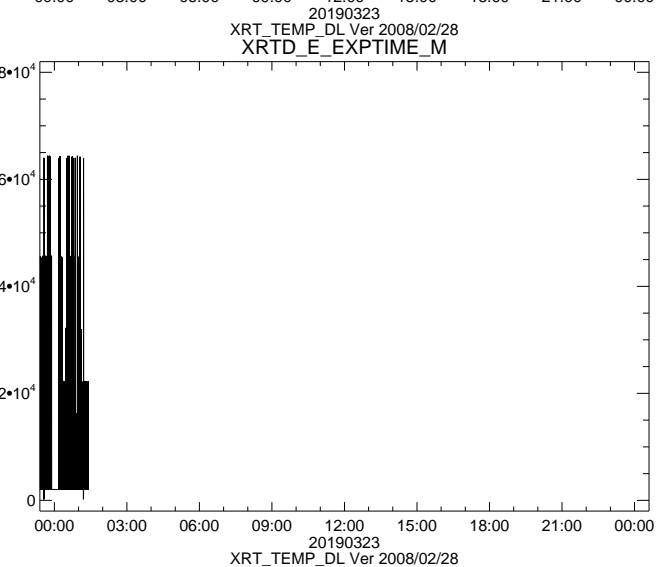
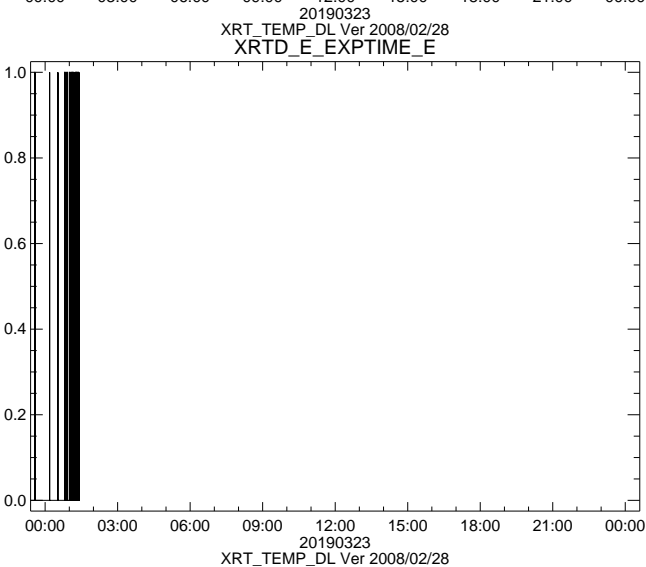
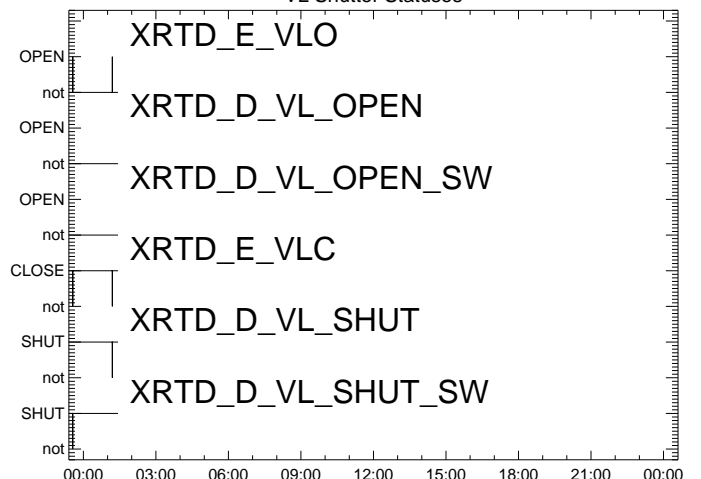
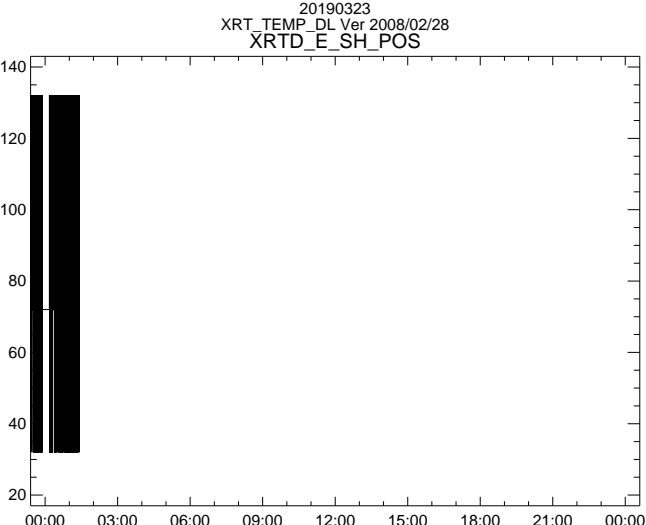
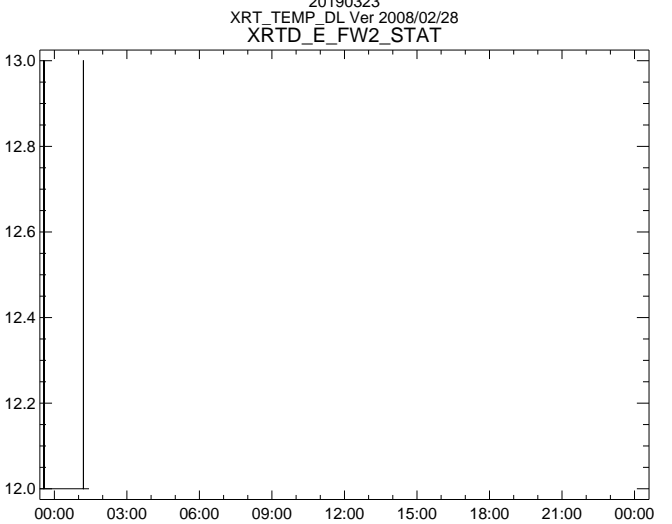
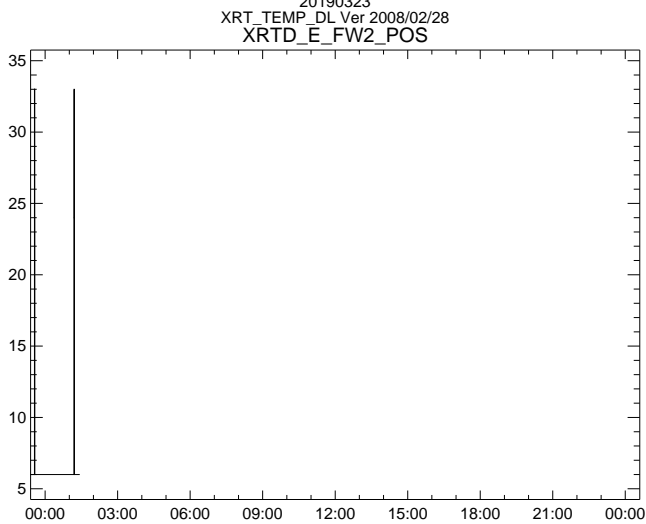
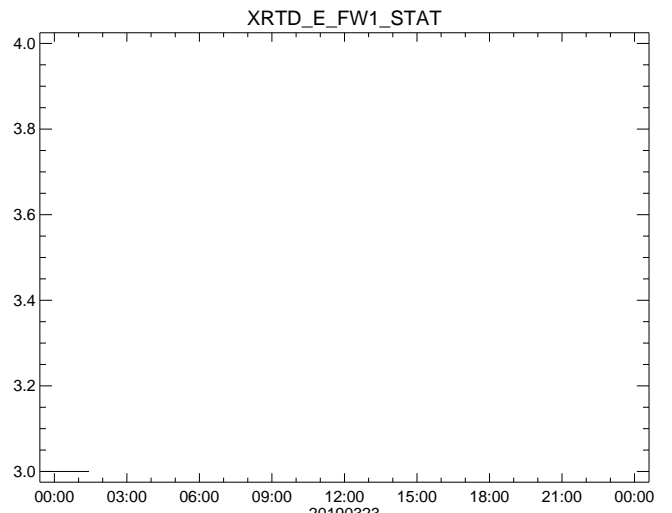
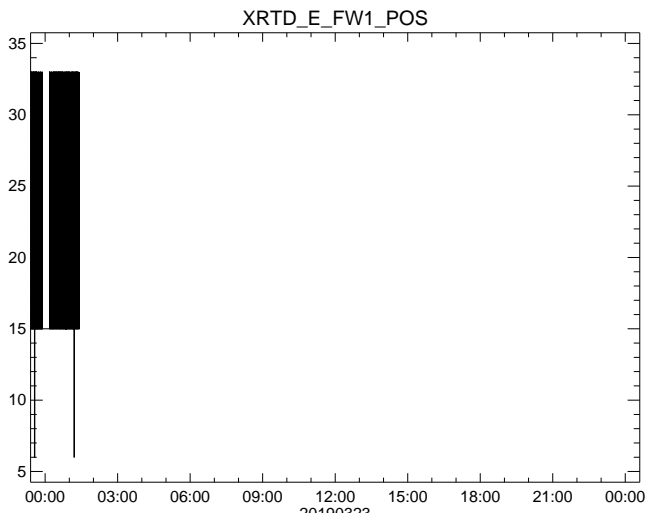




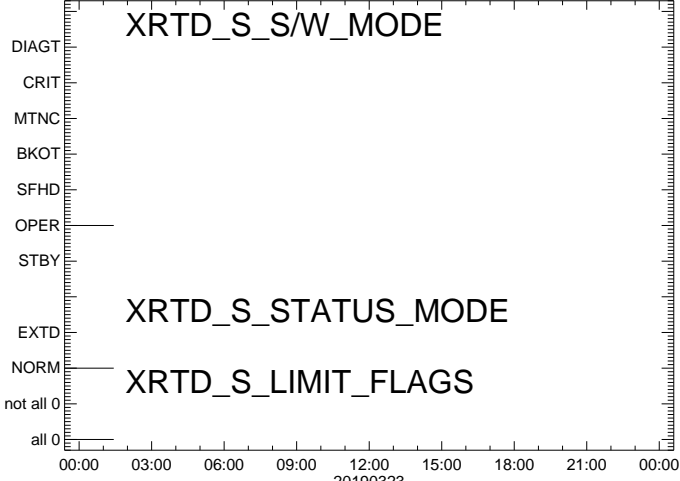




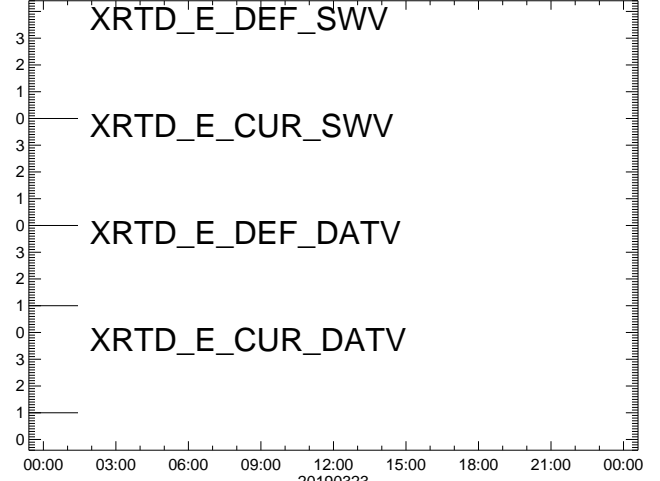




Software Statuses

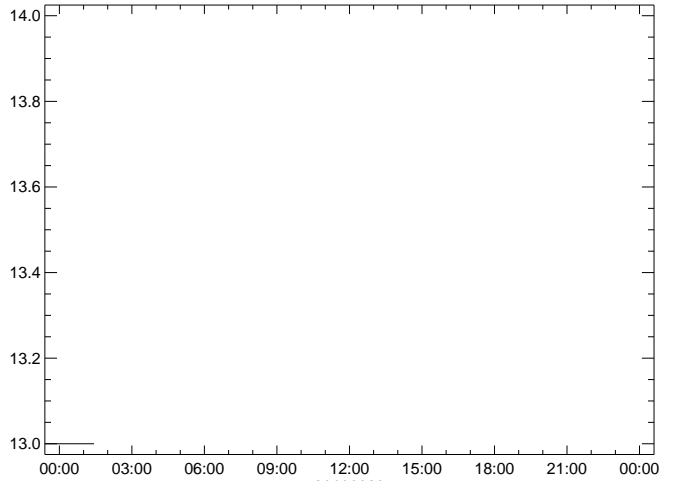
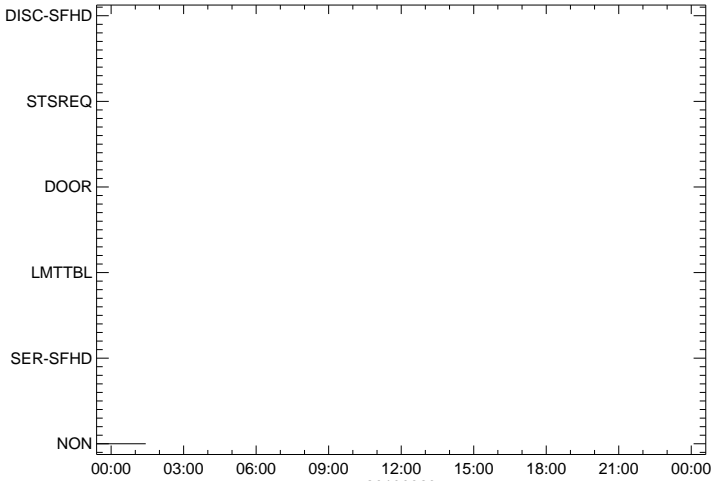


SW/DATA Version



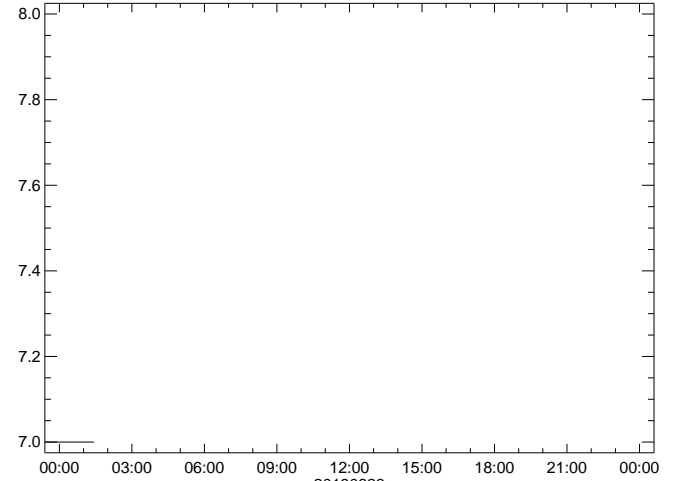
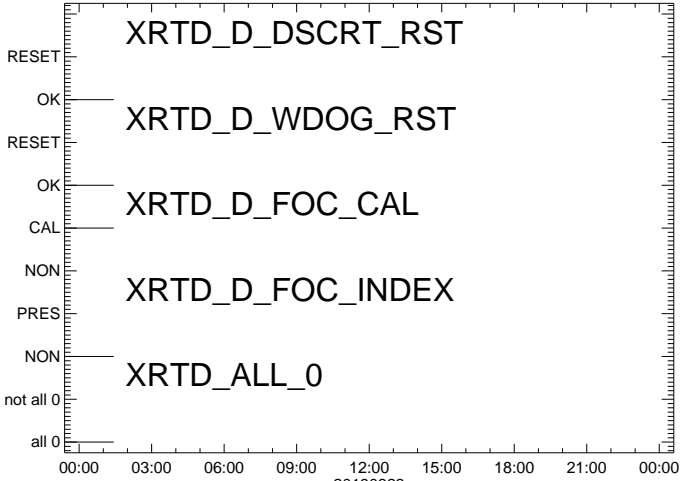
20190323
XRT_TEMP_DL Ver 2008/02/28
XRTD_S_SFHD_CAUSE

20190323
XRT_TEMP_DL Ver 2008/02/28
XRTD_E_DSCRT_RST



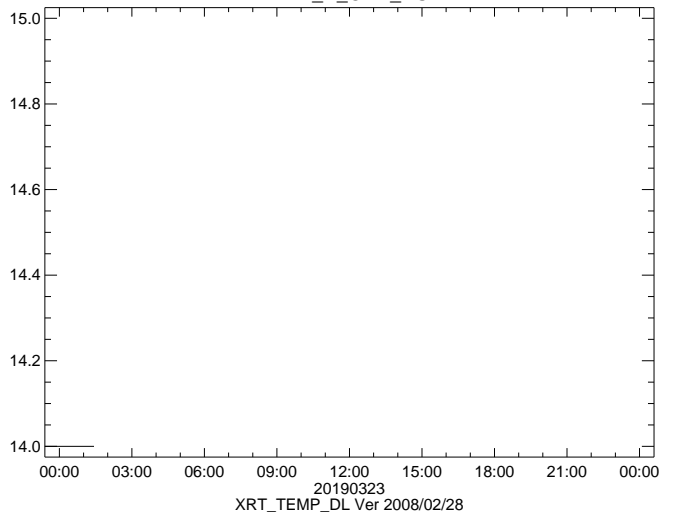
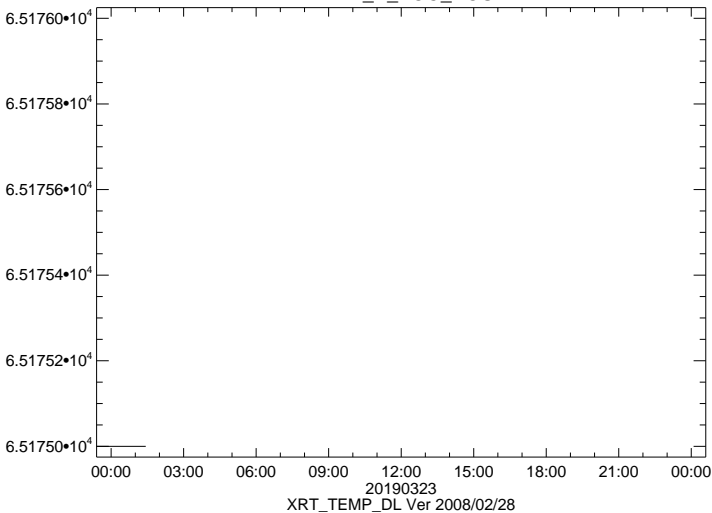
20190323
XRT_TEMP_DL Ver 2008/02/28
Digital Statuses

20190323
XRT_TEMP_DL Ver 2008/02/28
XRTD_E_WDOG_RST



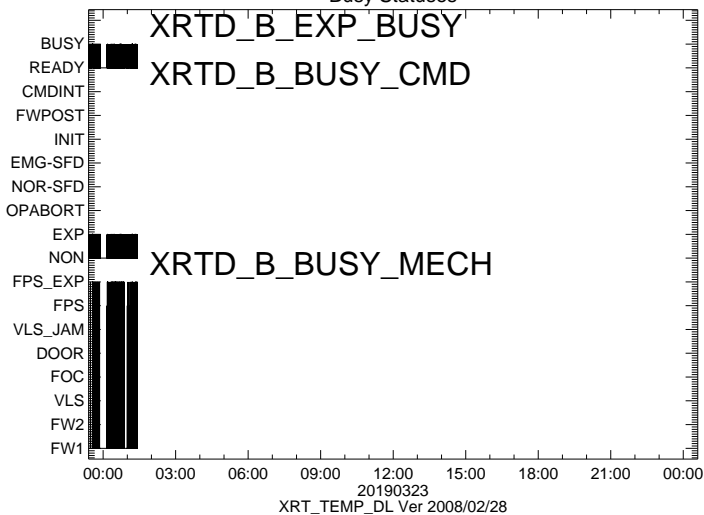
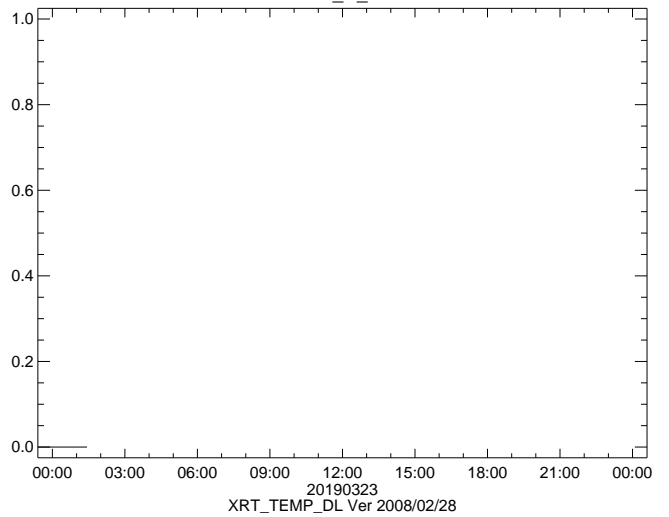
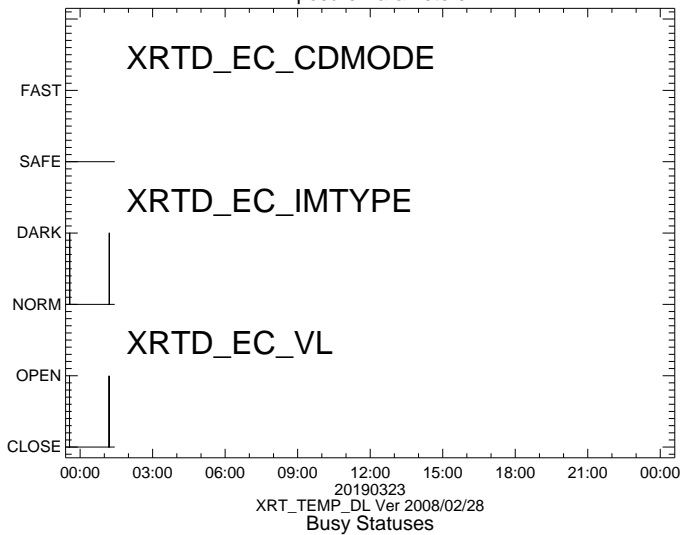
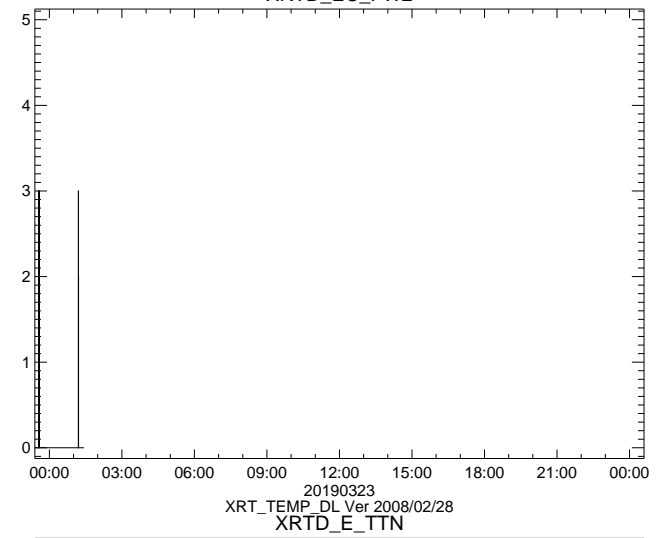
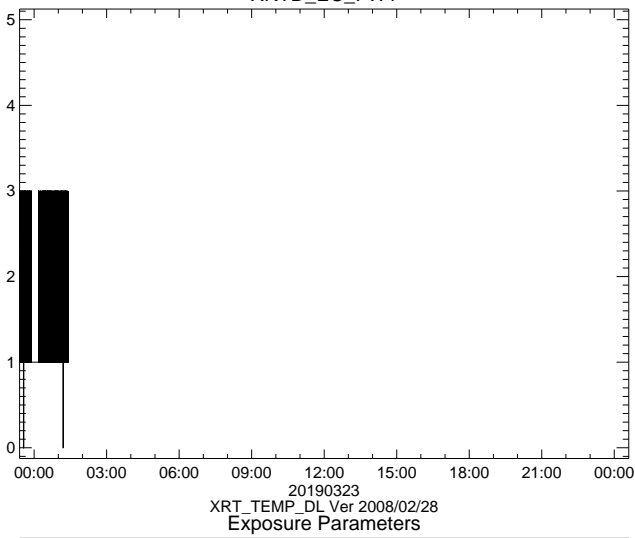
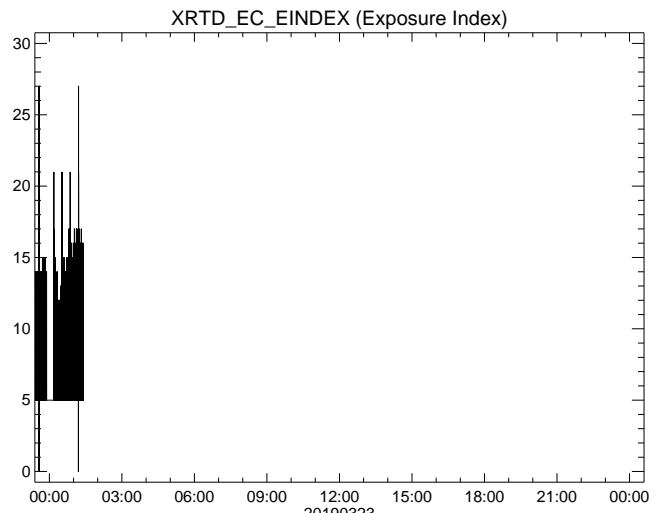
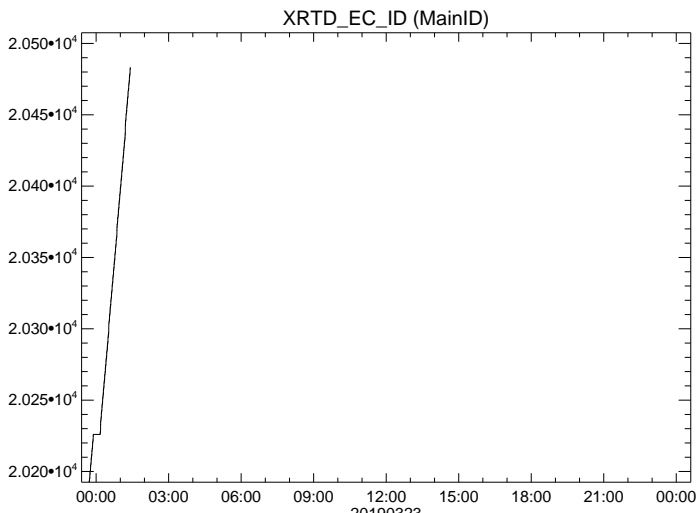
20190323
XRT_TEMP_DL Ver 2008/02/28
XRTD_E_FOC_POS

20190323
XRT_TEMP_DL Ver 2008/02/28
XRTD_E_OTH_RST

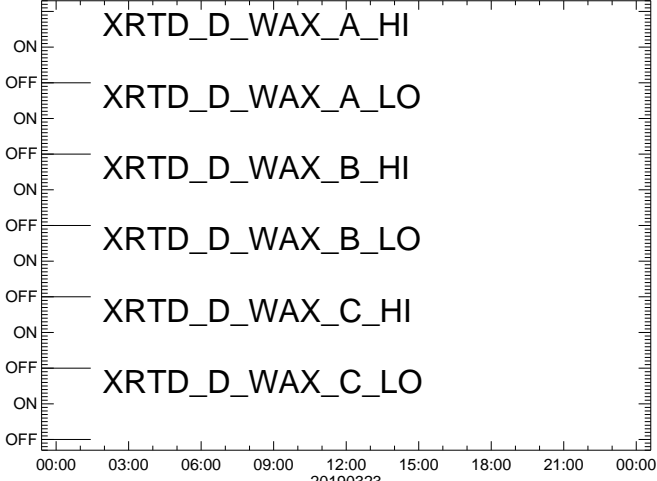


20190323
XRT_TEMP_DL Ver 2008/02/28

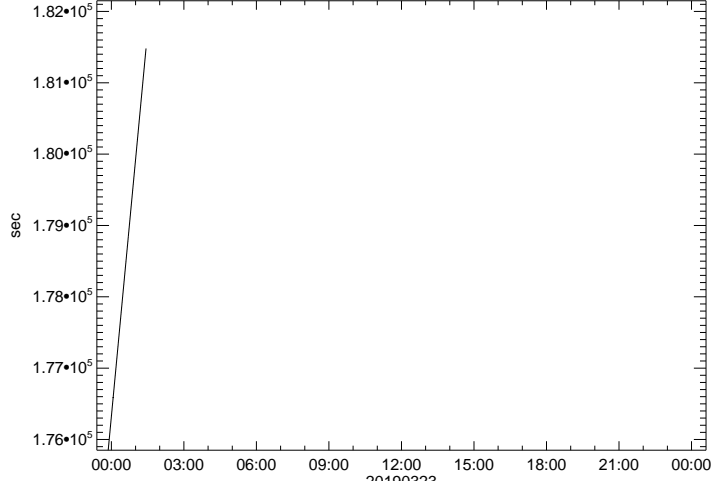
20190323
XRT_TEMP_DL Ver 2008/02/28



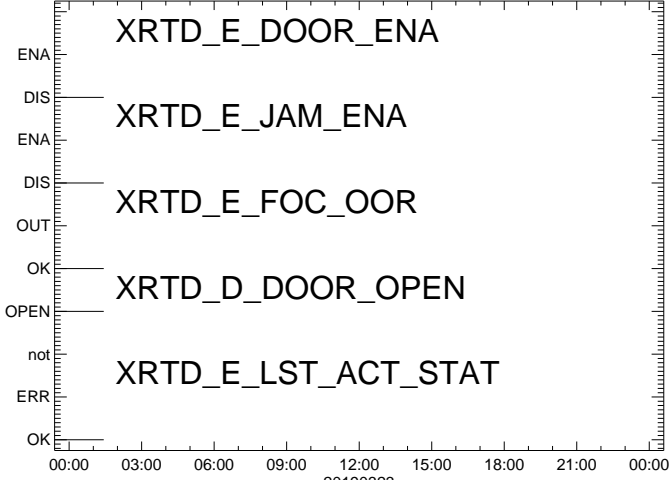
WAX Actuator Statuses



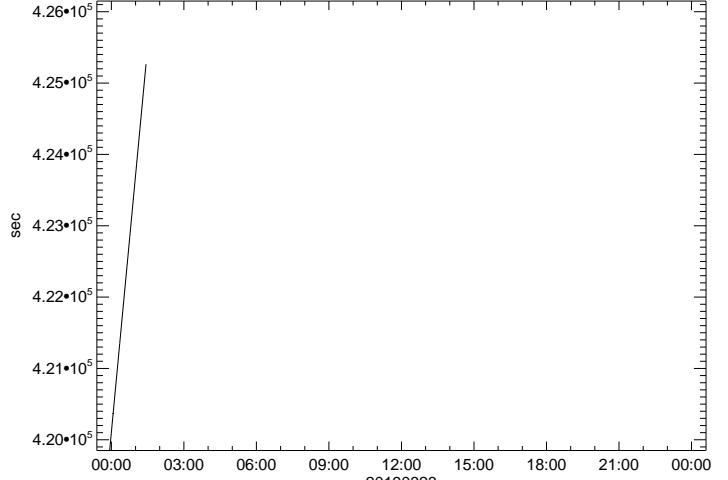
XRTD_T_SC_CLOCK



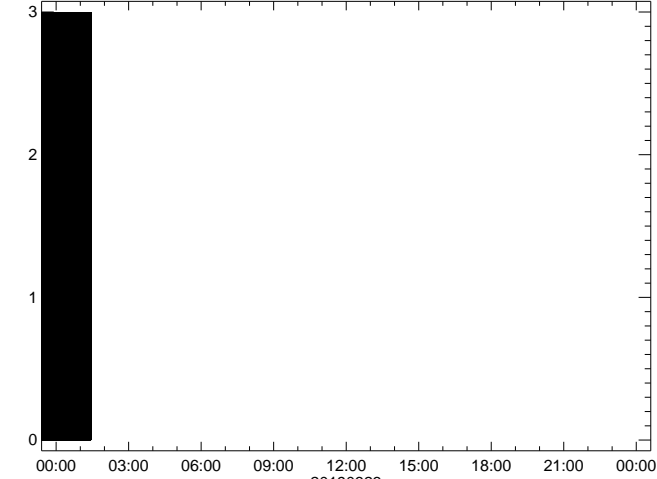
20190323
XRT_TEMP_DL Ver 2008/02/28
E Statuses



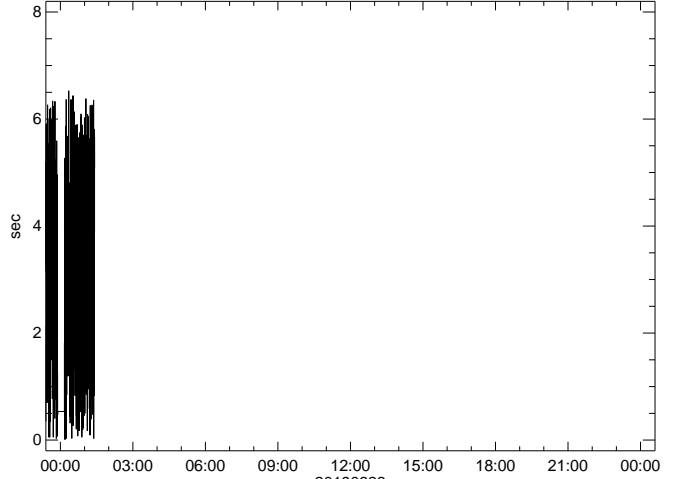
20190323
XRT_TEMP_DL Ver 2008/02/28
XRTD_T_LO_CLOCK



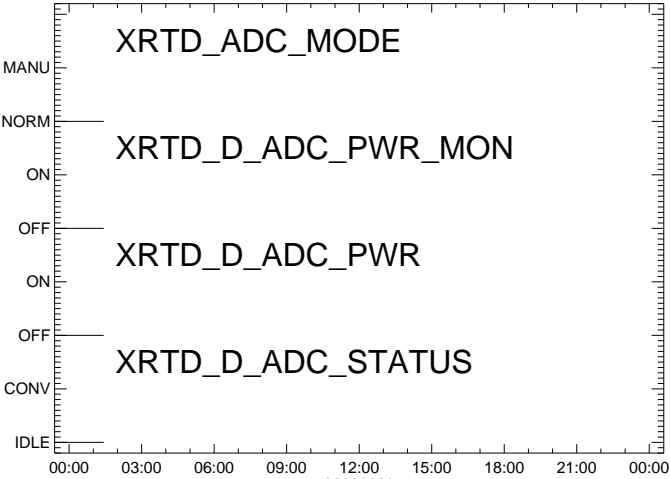
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XRT_TEMP_DL Ver 2008/02/28
XRTD_TEMP_SENS_GROUP



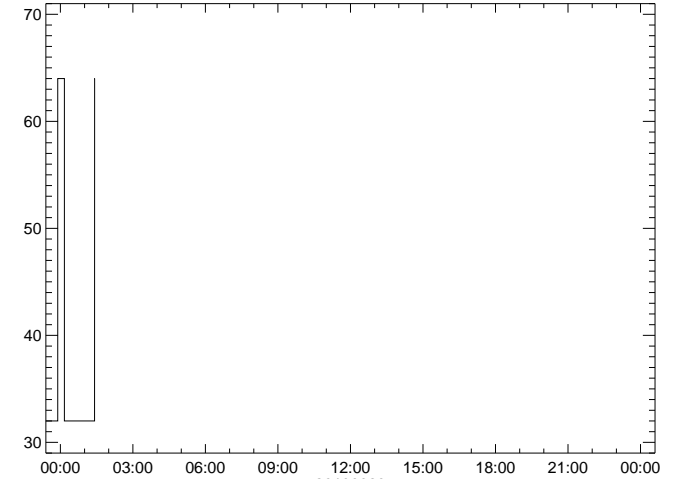
20190323
XRT_TEMP_DL Ver 2008/02/28
XRTD_T_LST_ACT



20190323
XRT_TEMP_DL Ver 2008/02/28
ADC Statuses

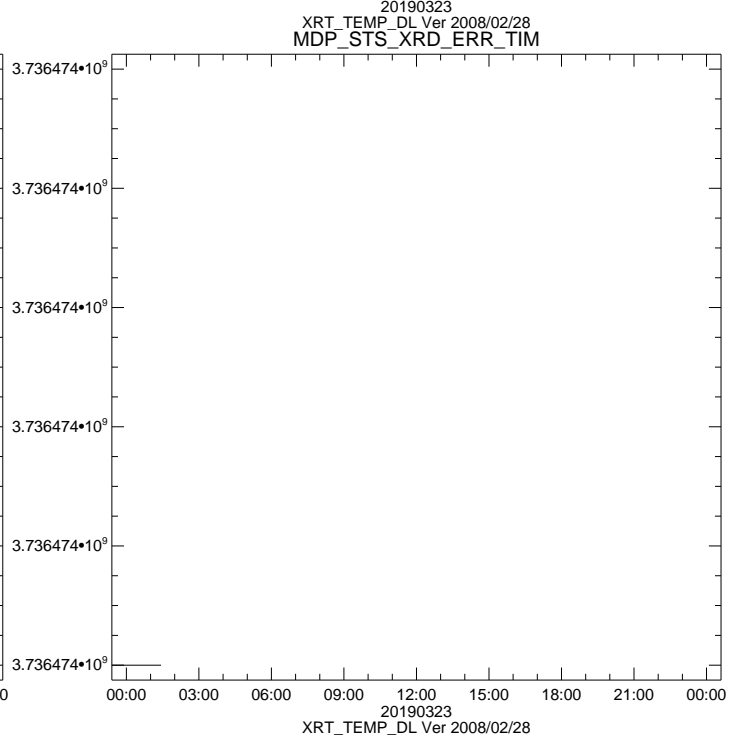
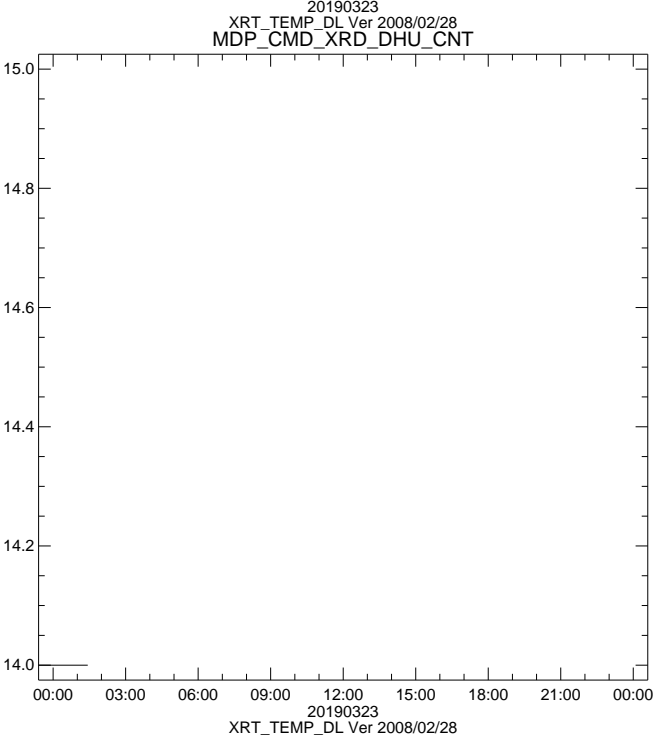
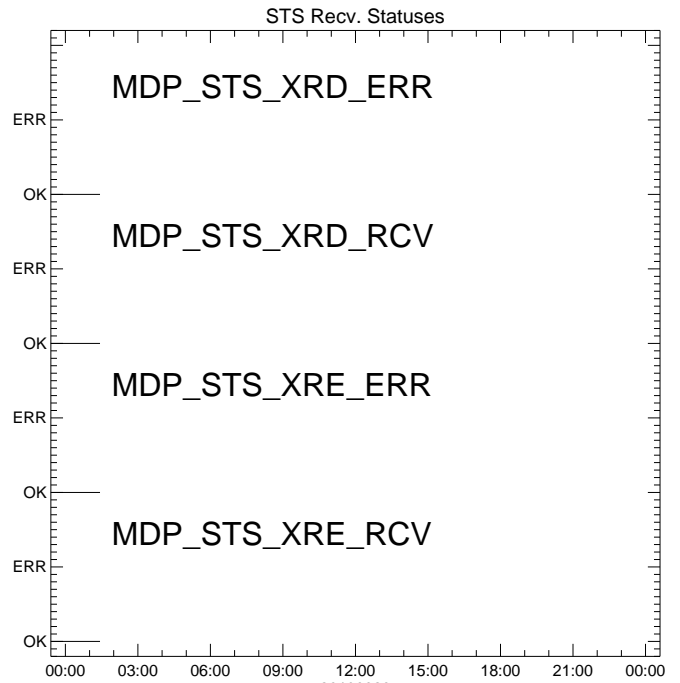
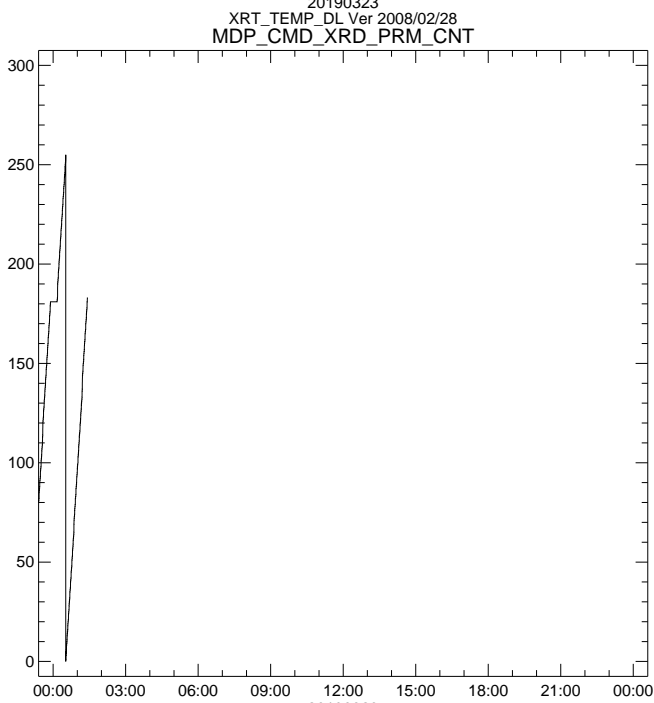
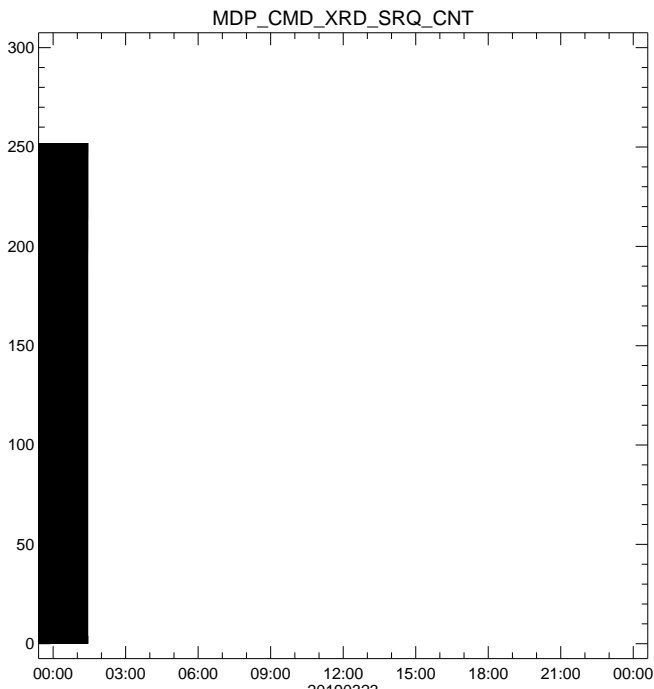


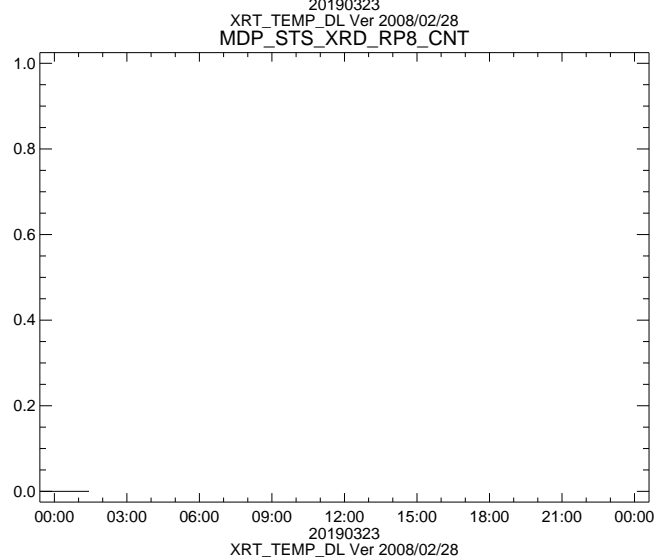
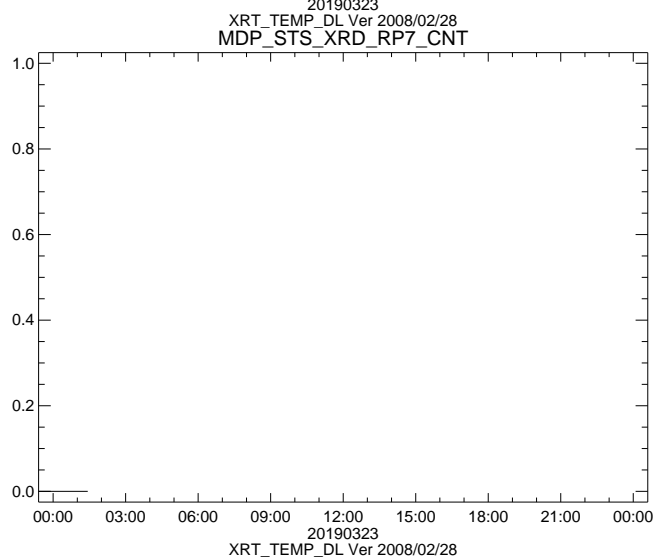
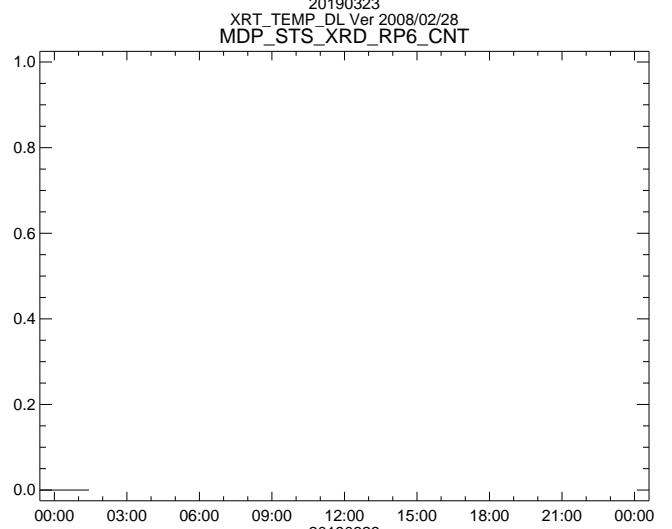
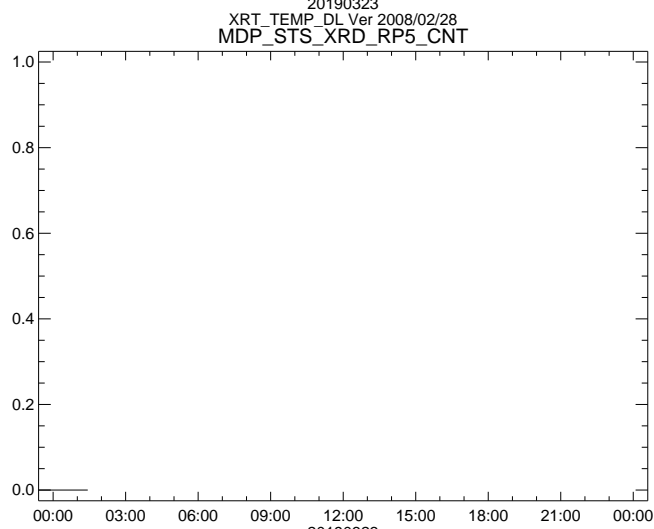
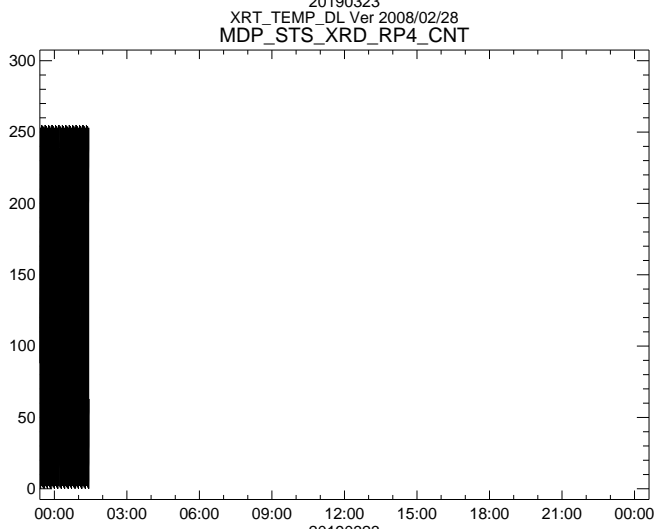
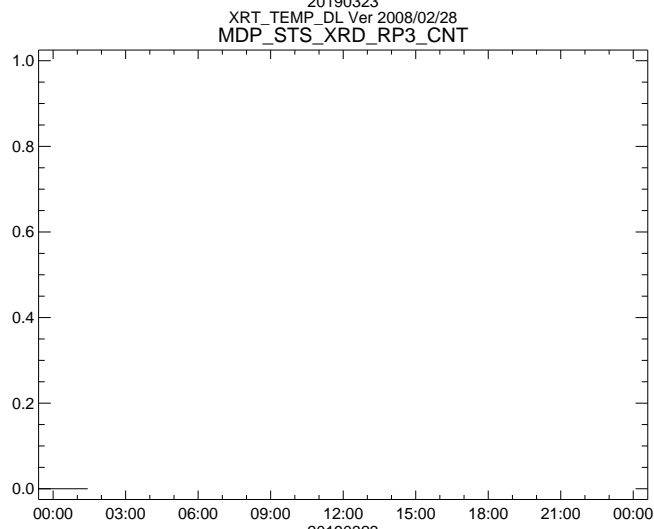
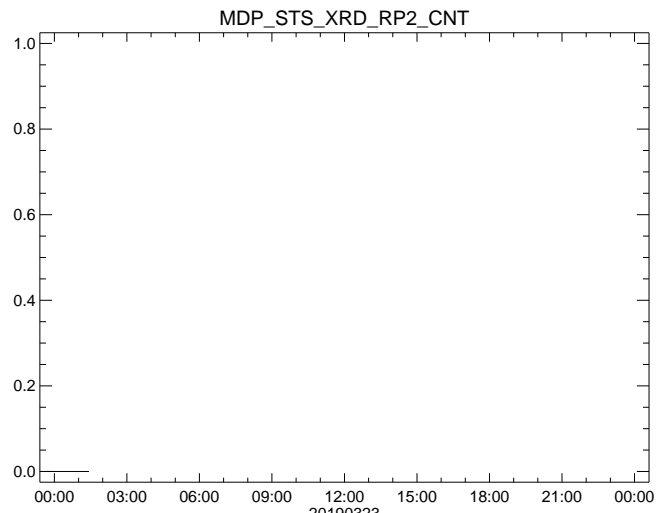
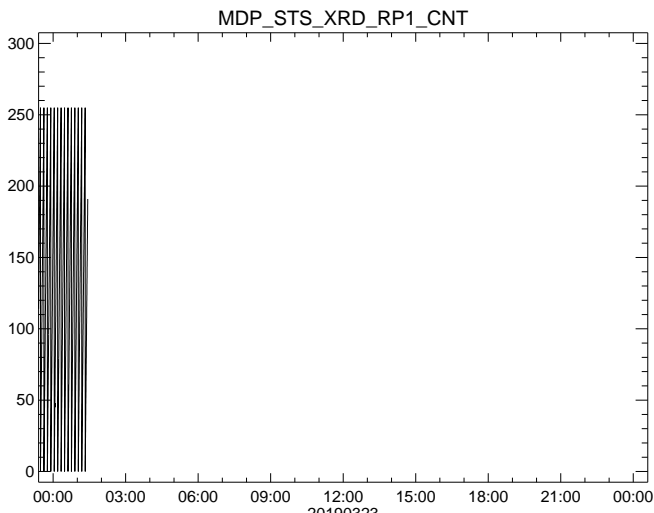
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XRT_TEMP_DL Ver 2008/02/28
XRTD_E_LST_ACT



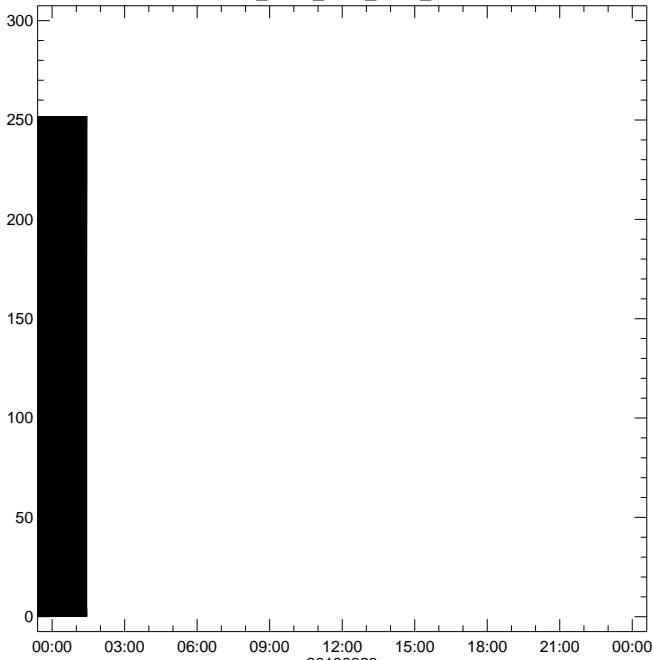
20190323
XRT_TEMP_DL Ver 2008/02/28

20190323
XRT_TEMP_DL Ver 2008/02/28

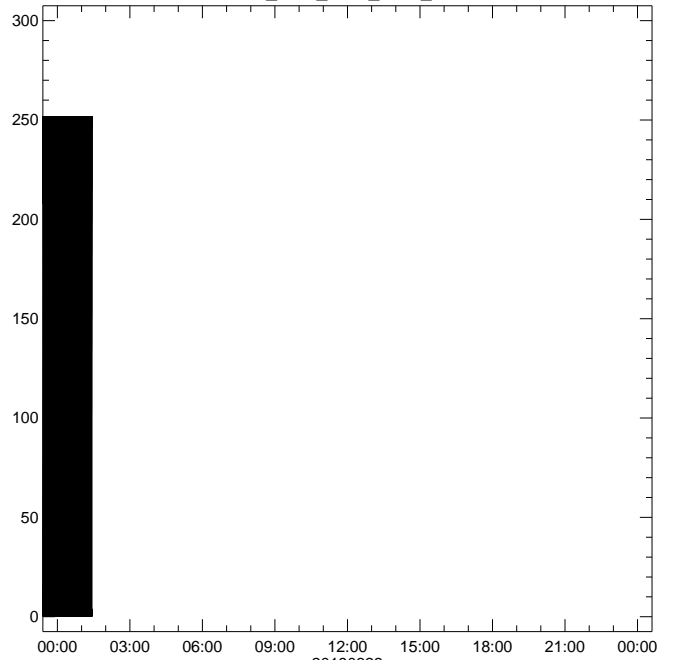




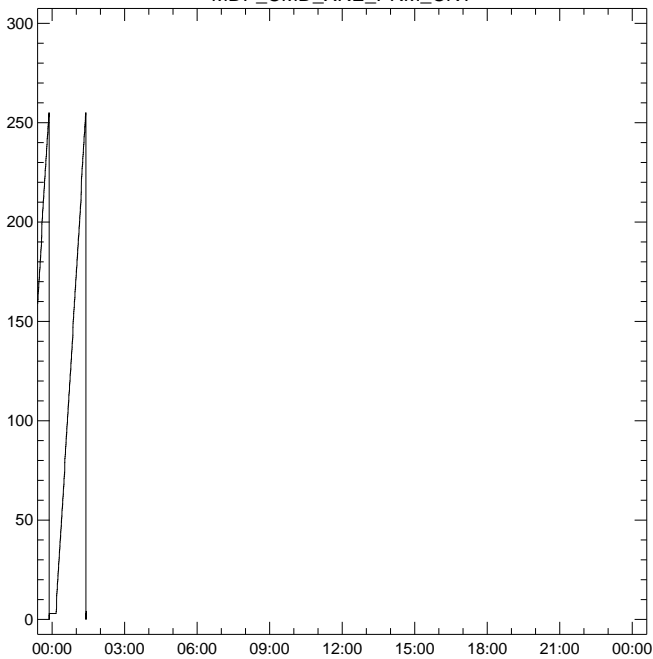
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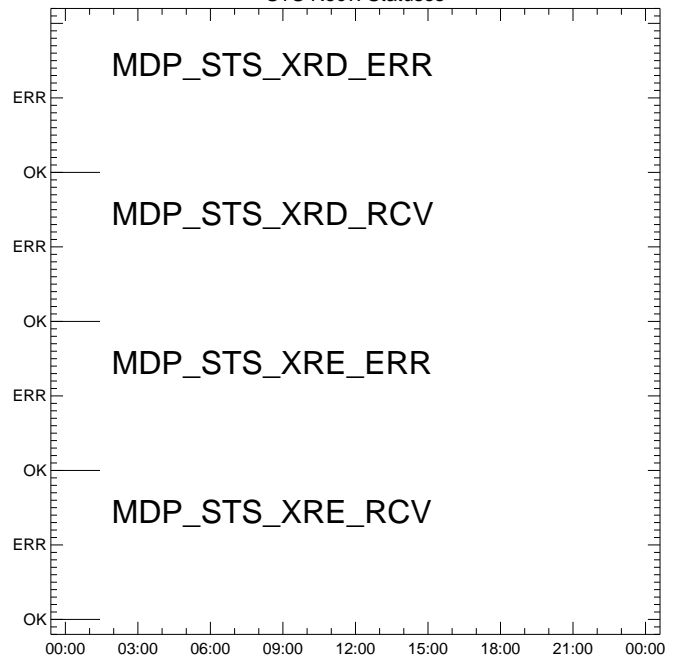
MDP_STS_XRE_SRP_CNT



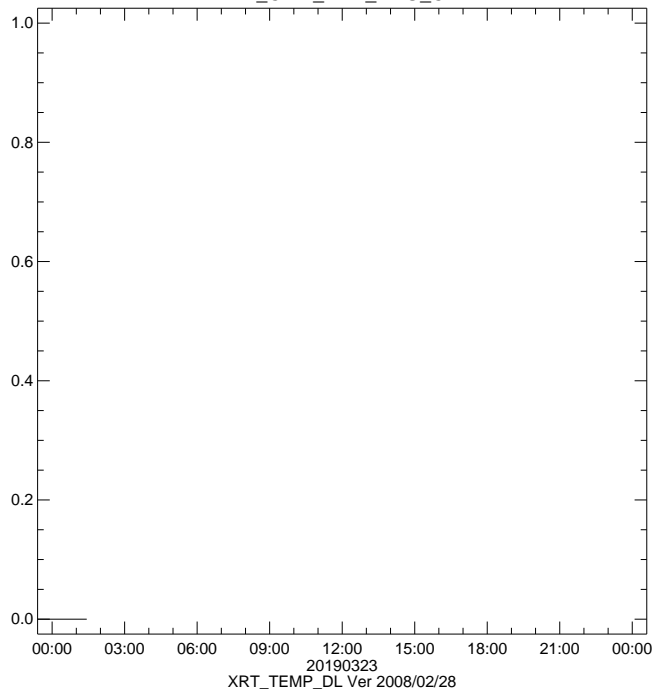
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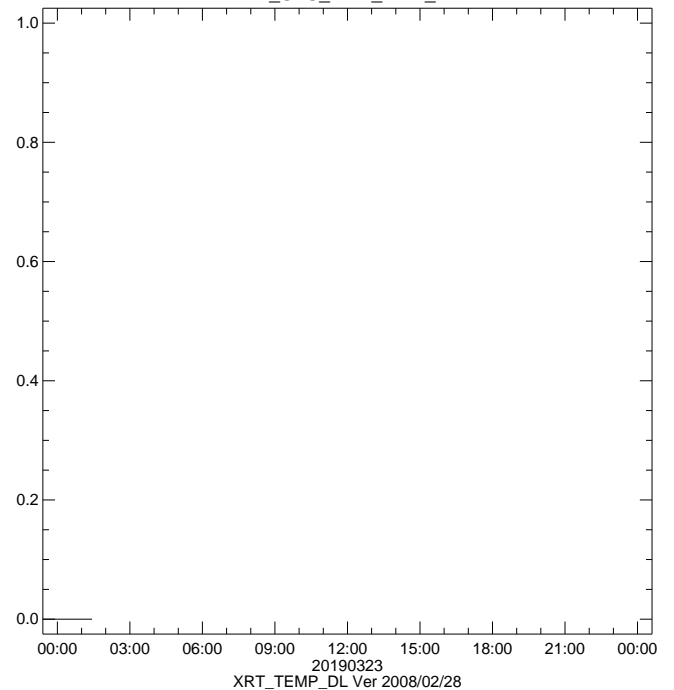
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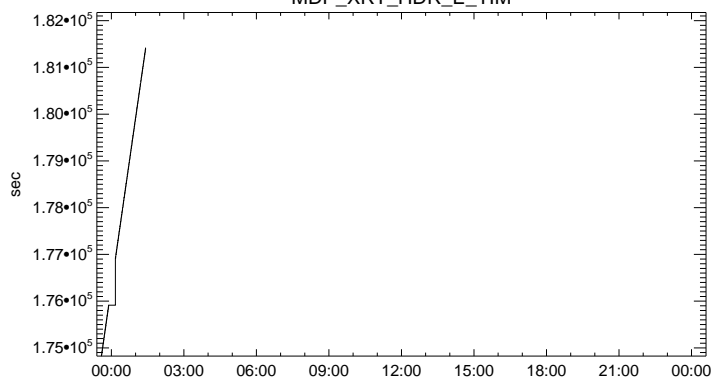
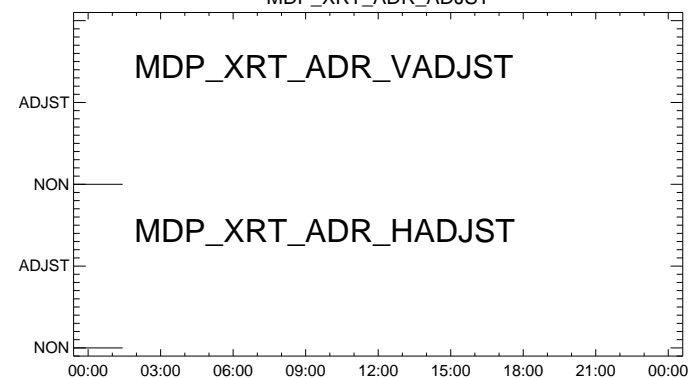
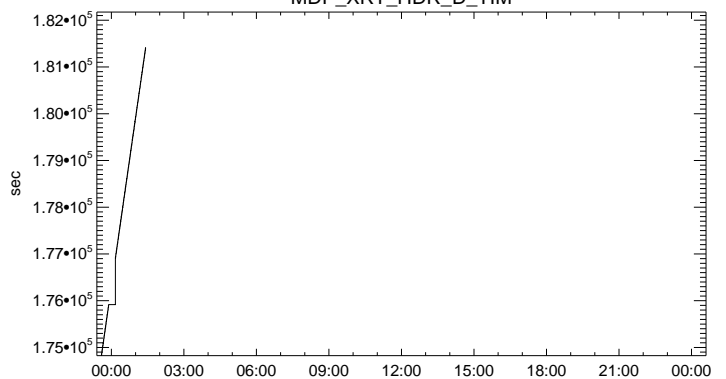
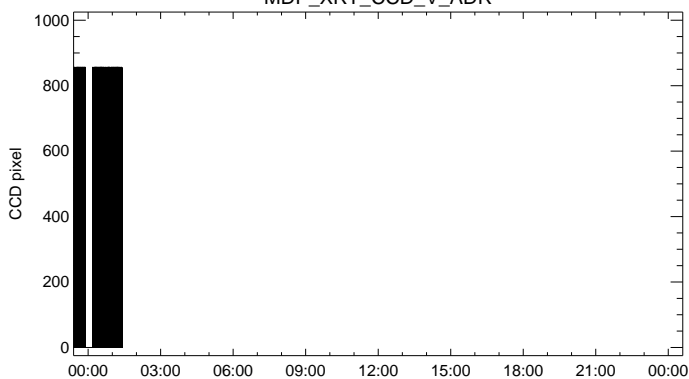
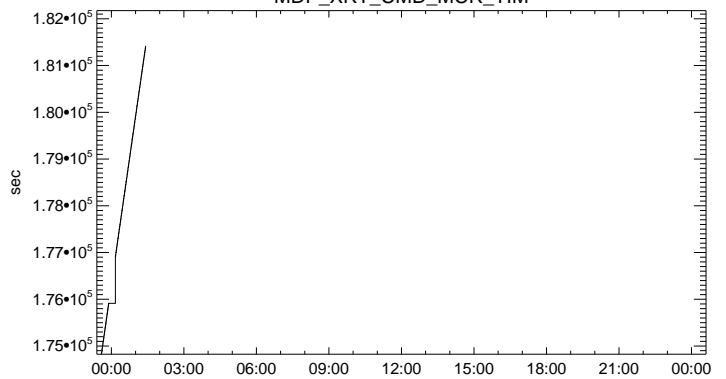
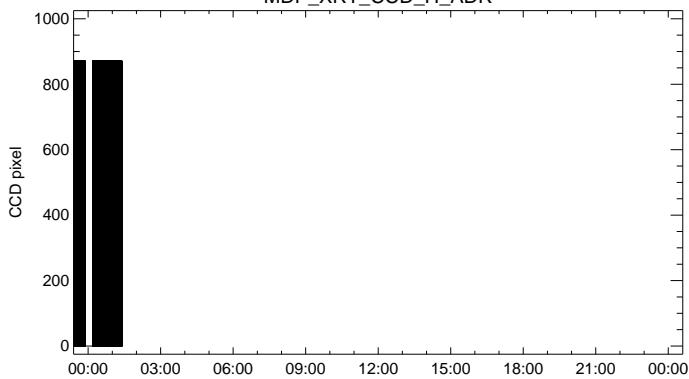
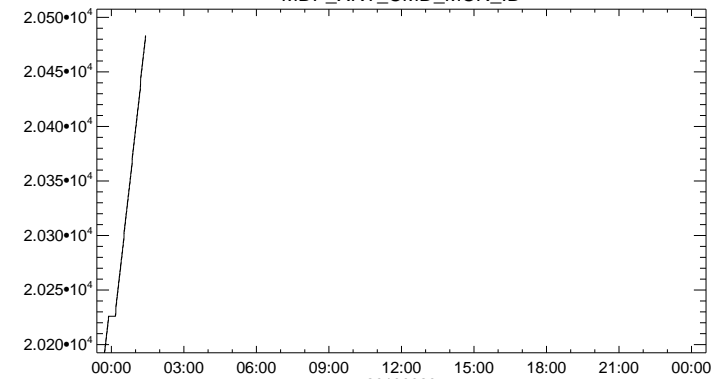
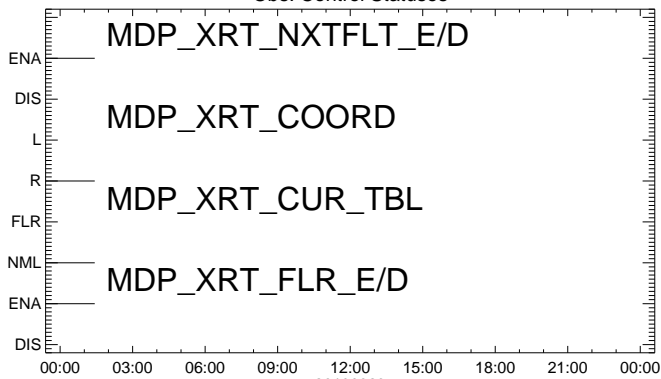
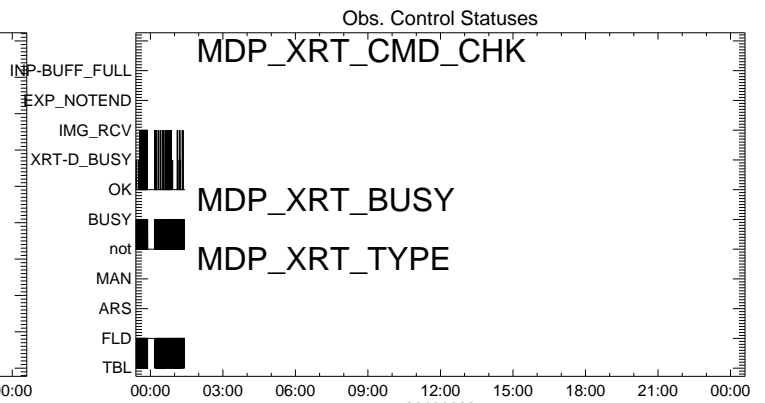
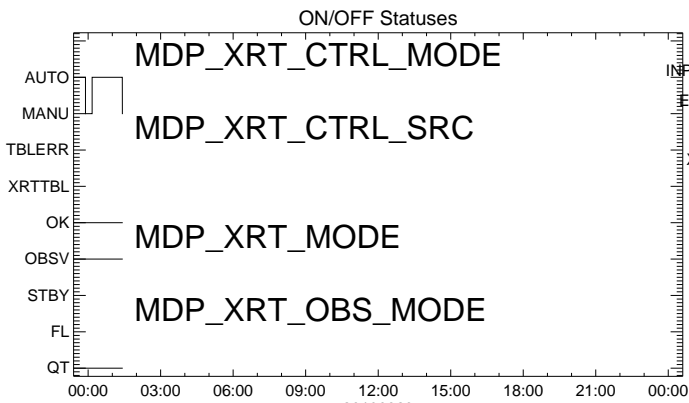


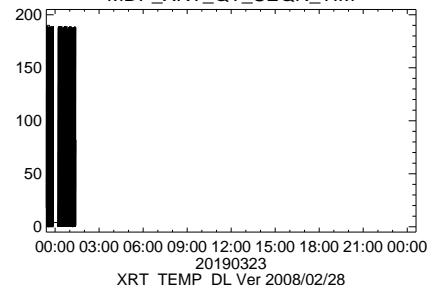
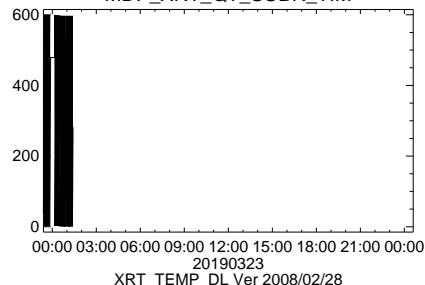
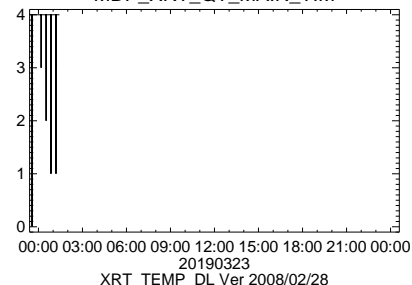
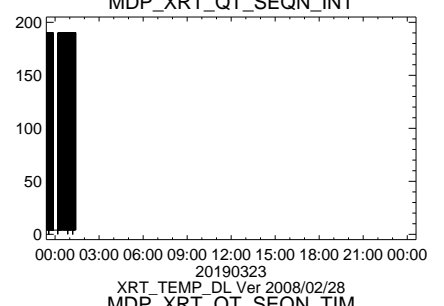
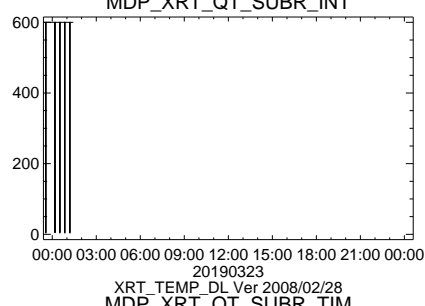
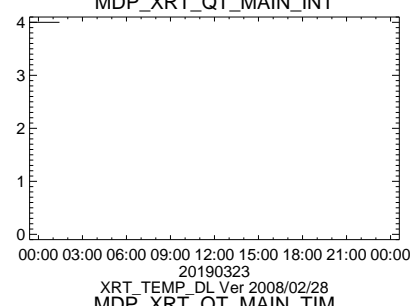
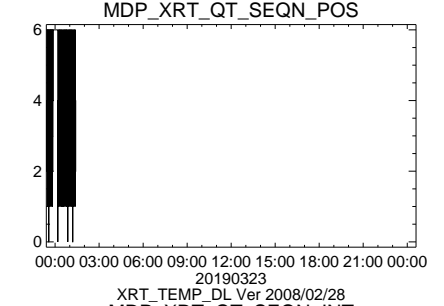
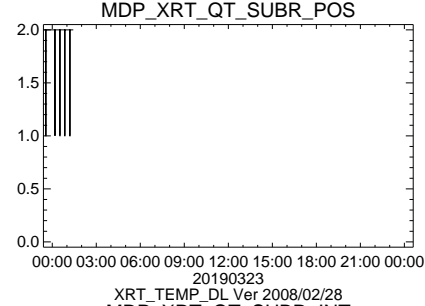
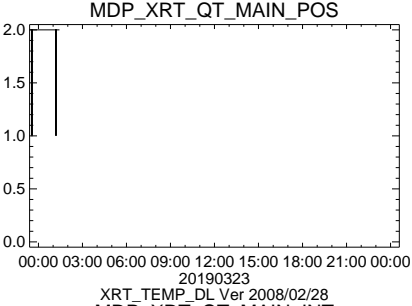
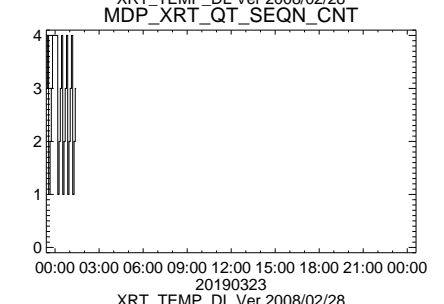
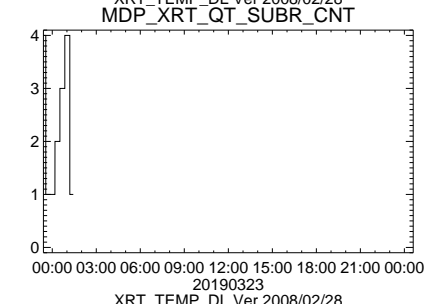
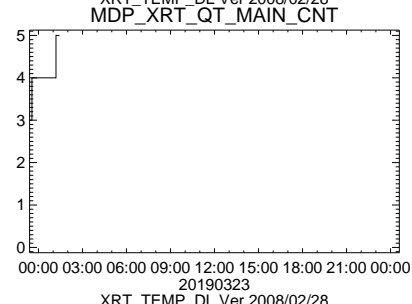
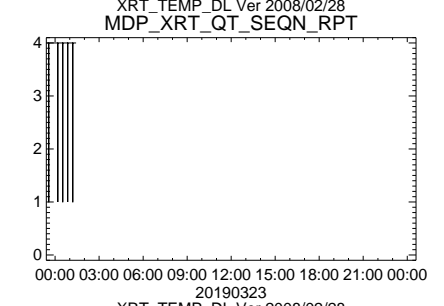
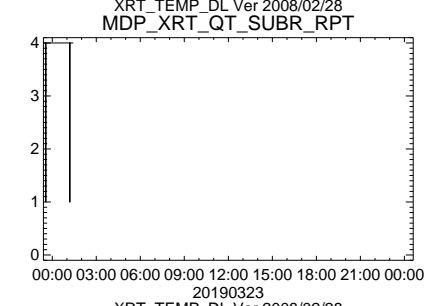
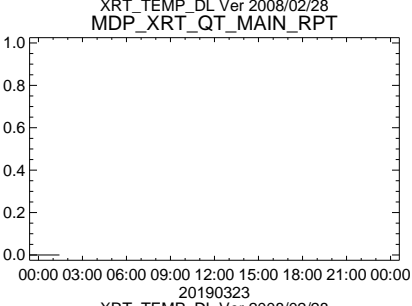
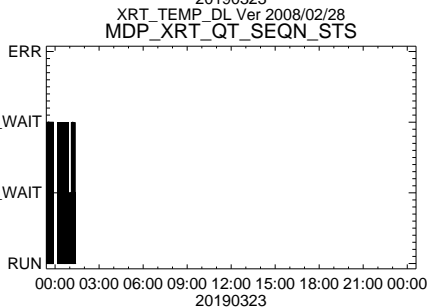
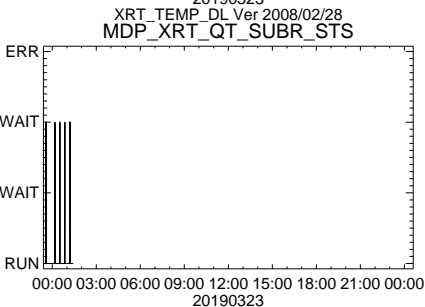
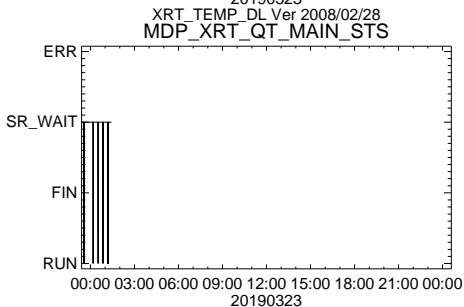
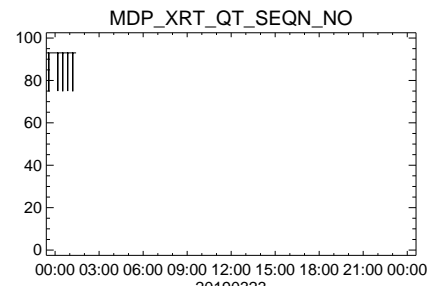
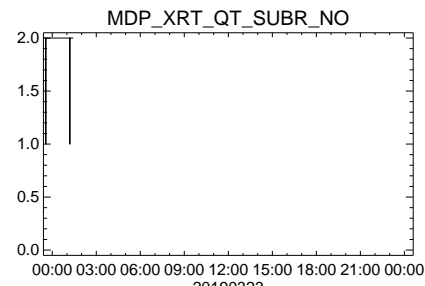
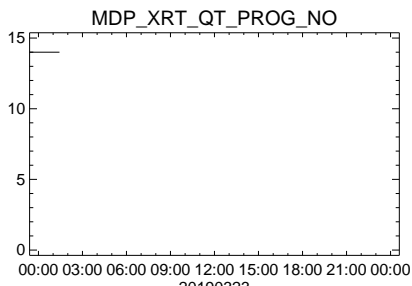
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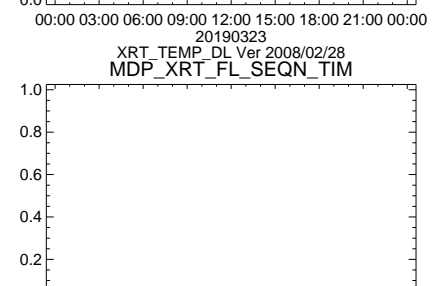
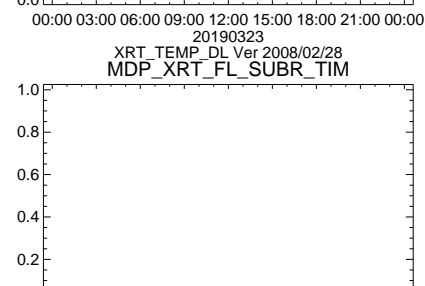
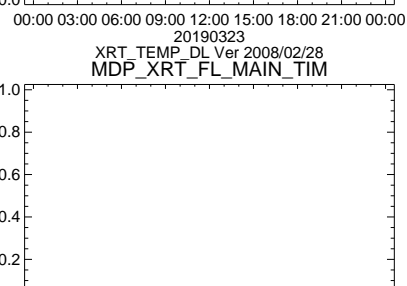
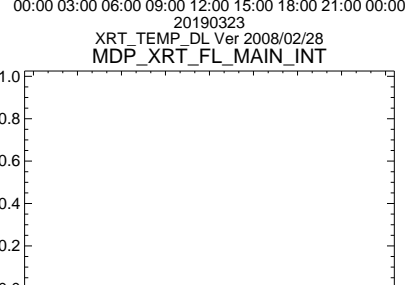
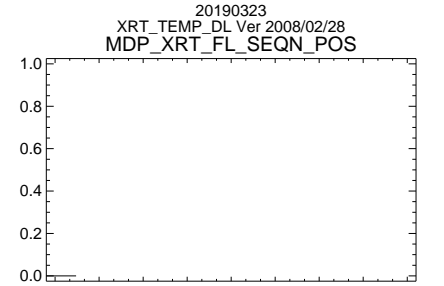
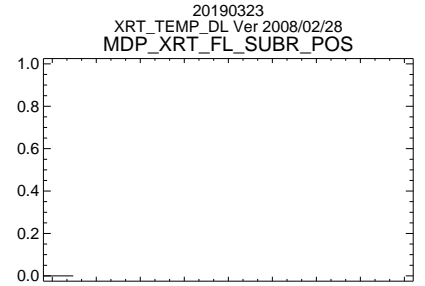
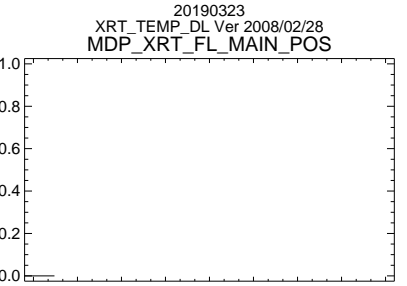
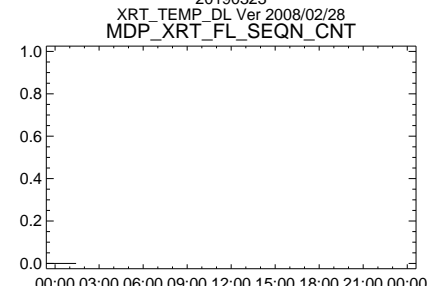
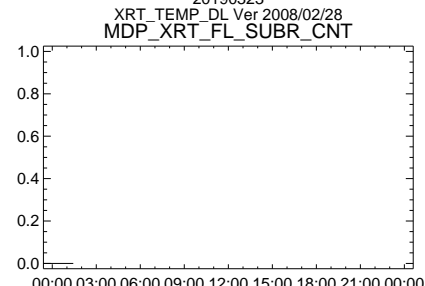
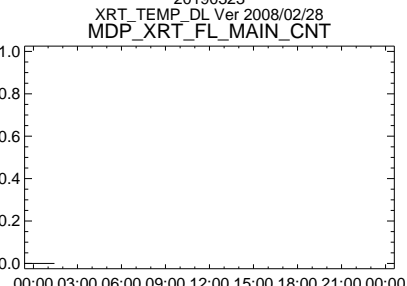
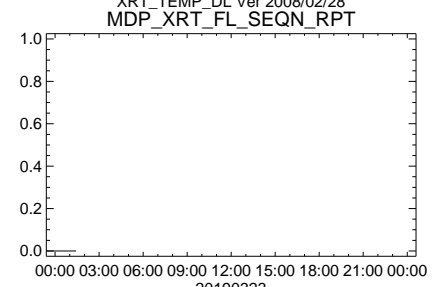
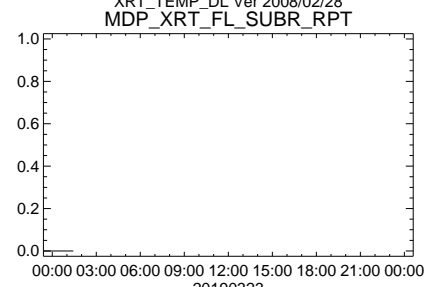
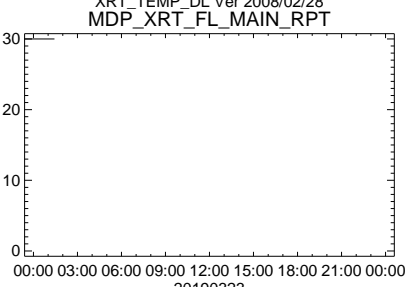
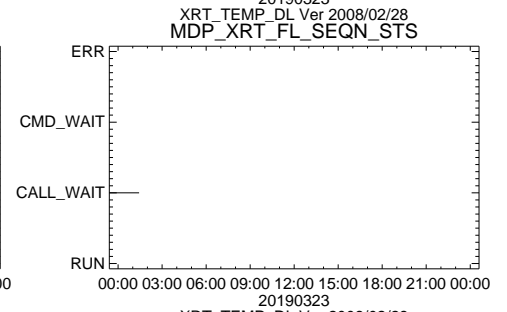
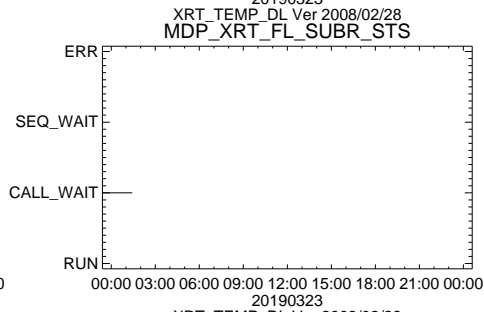
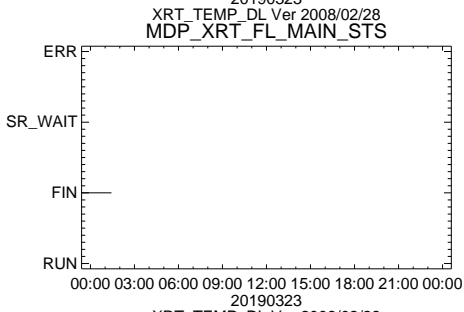
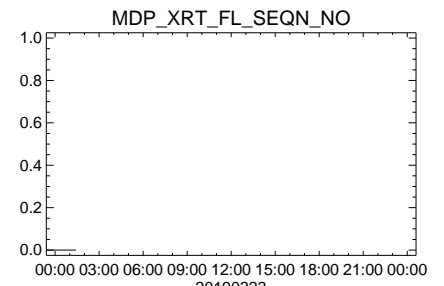
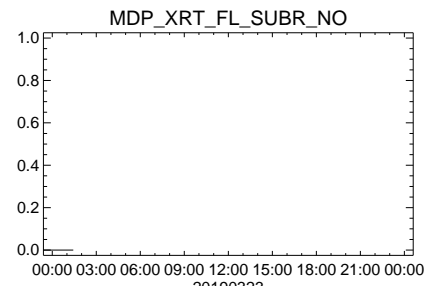
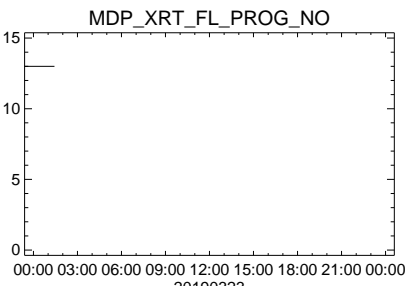


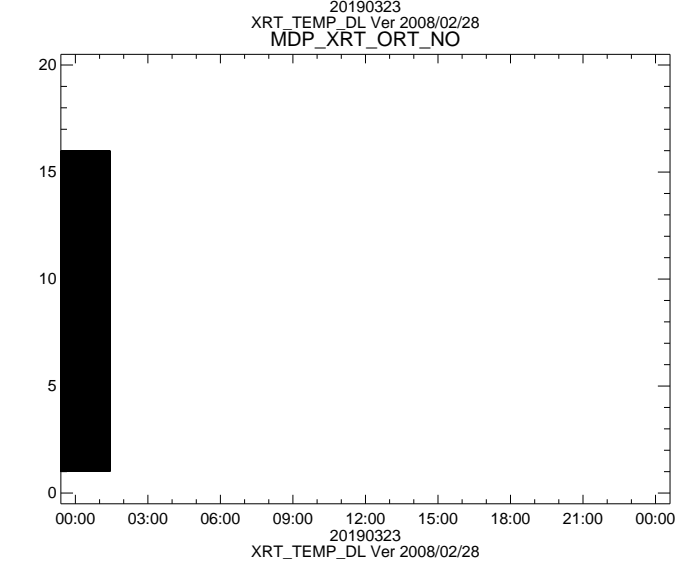
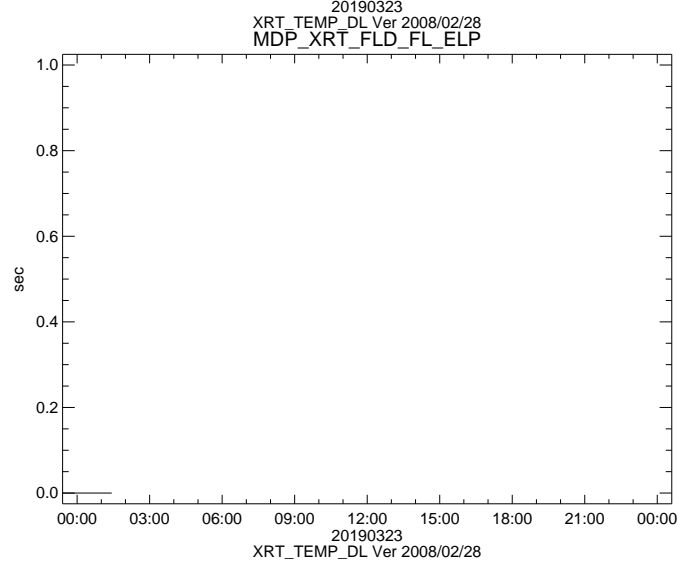
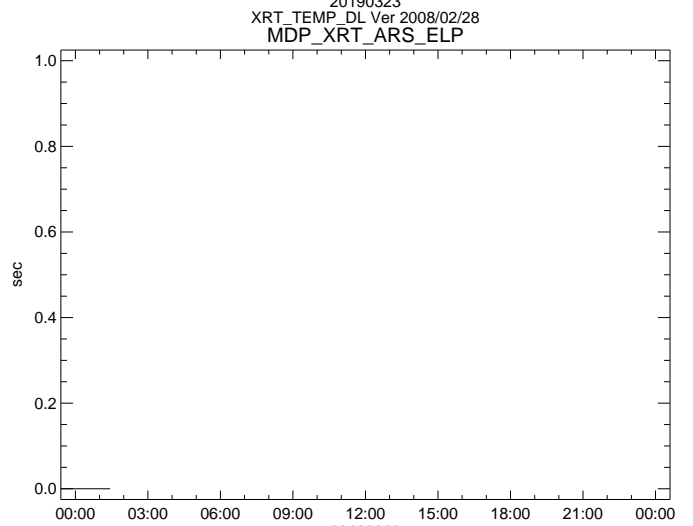
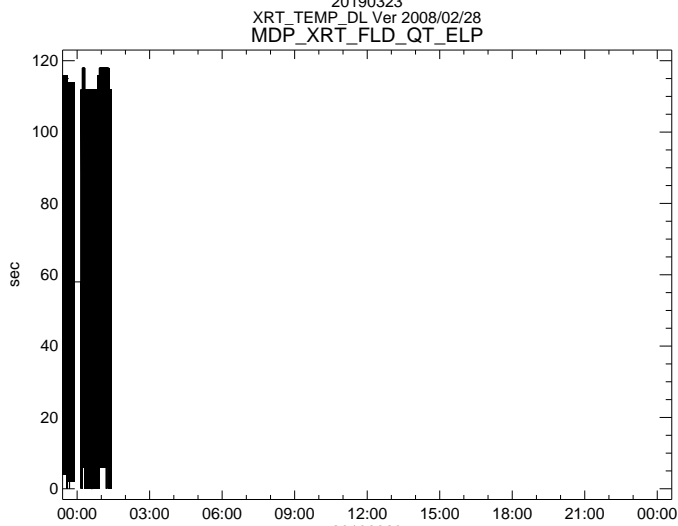
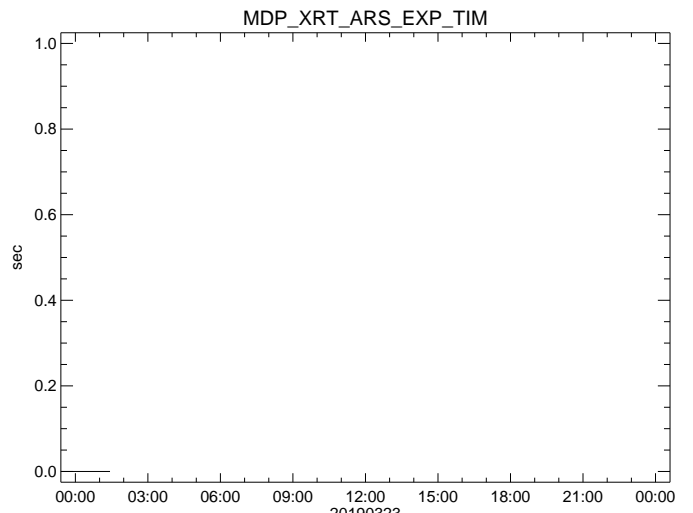
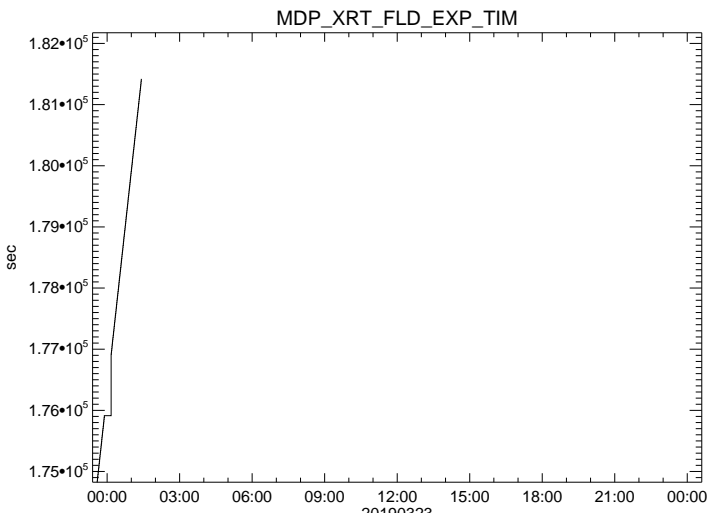
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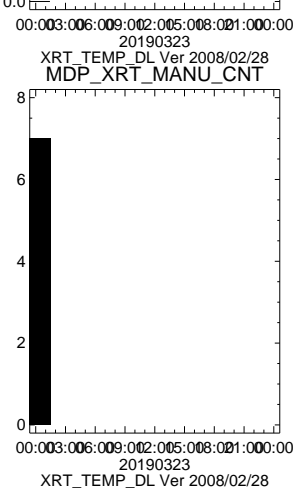
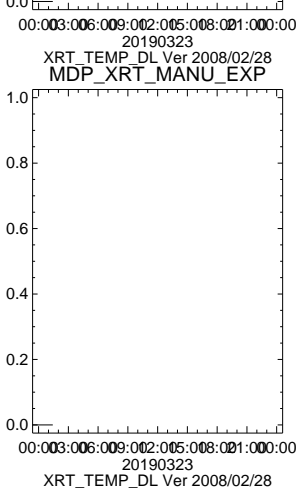
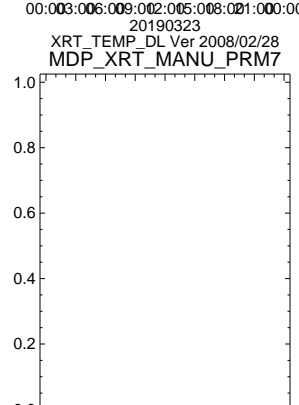
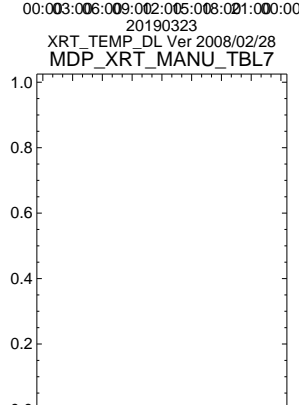
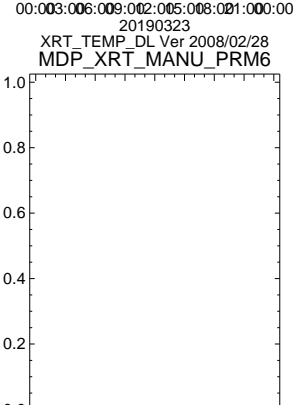
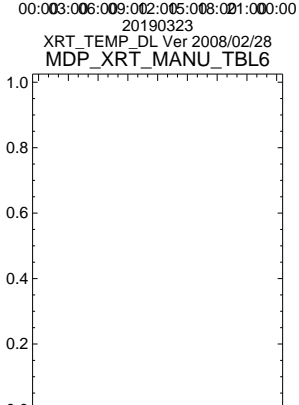
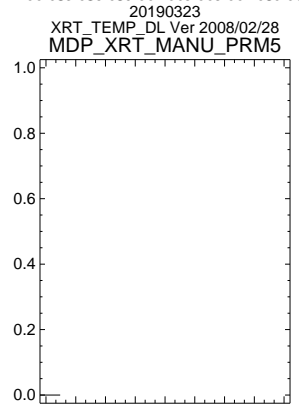
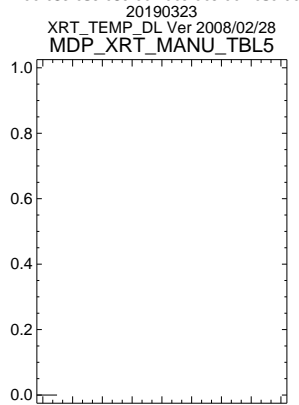
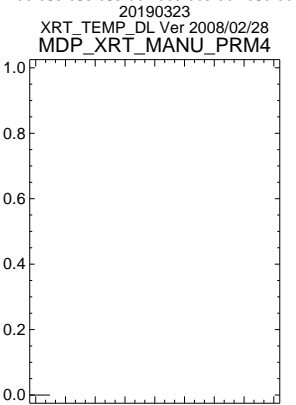
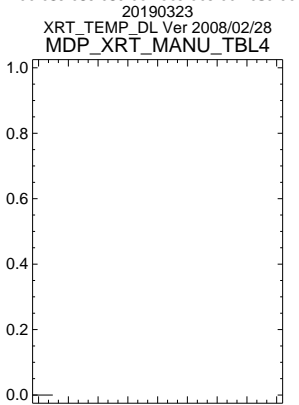
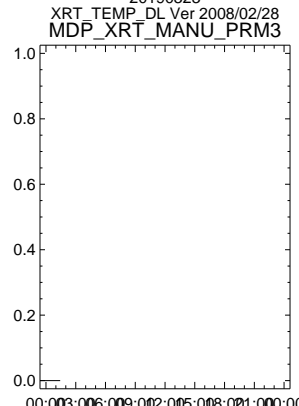
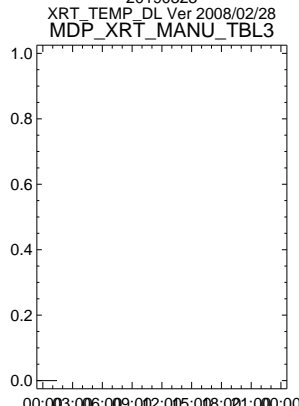
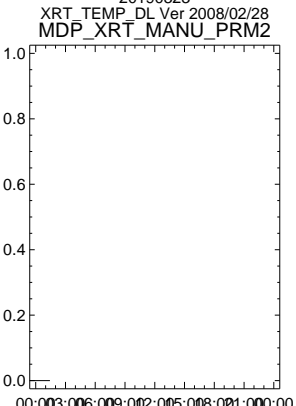
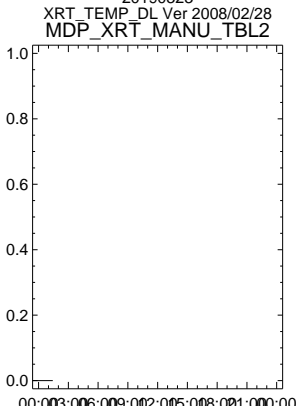
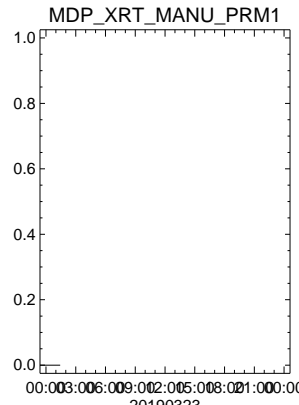
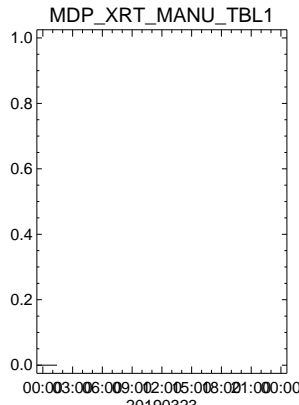
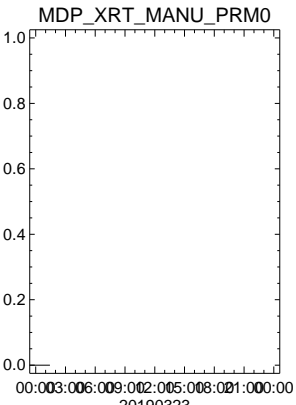
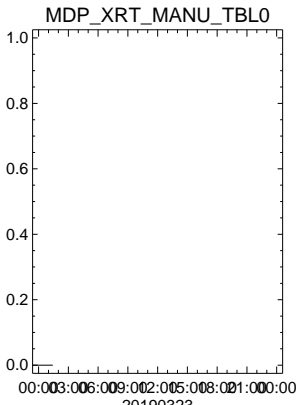
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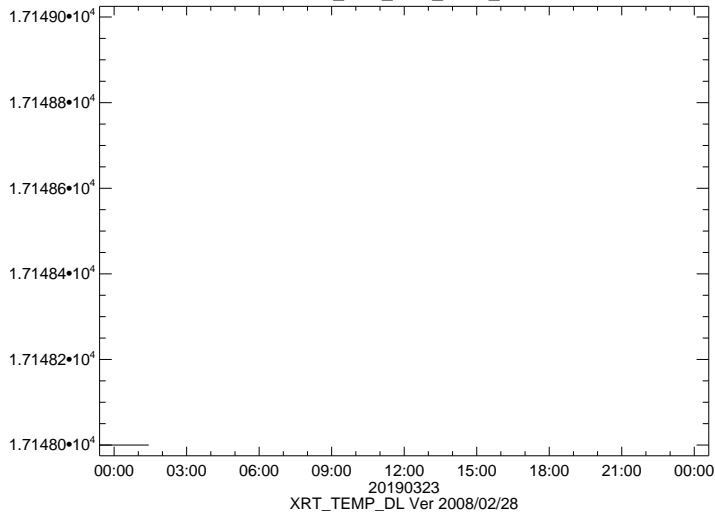
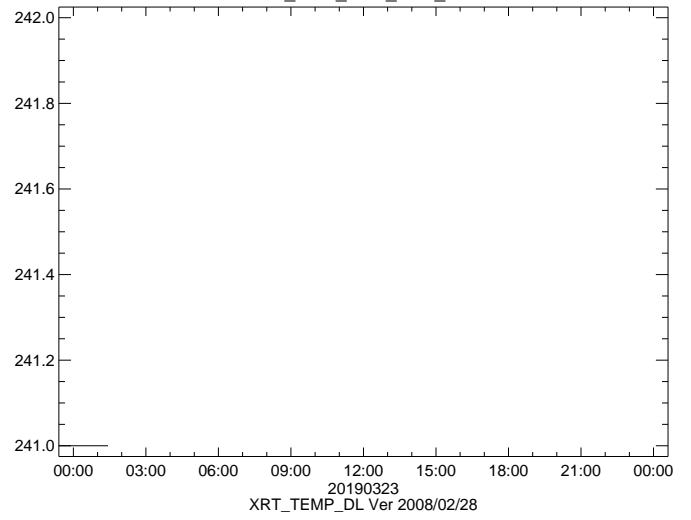
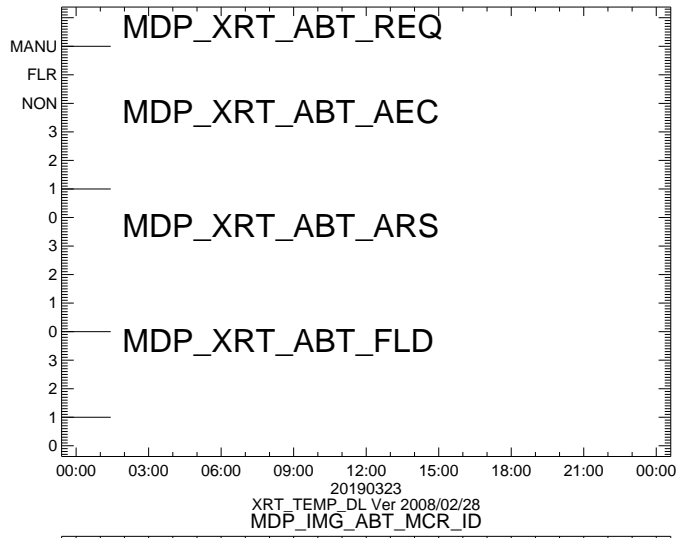
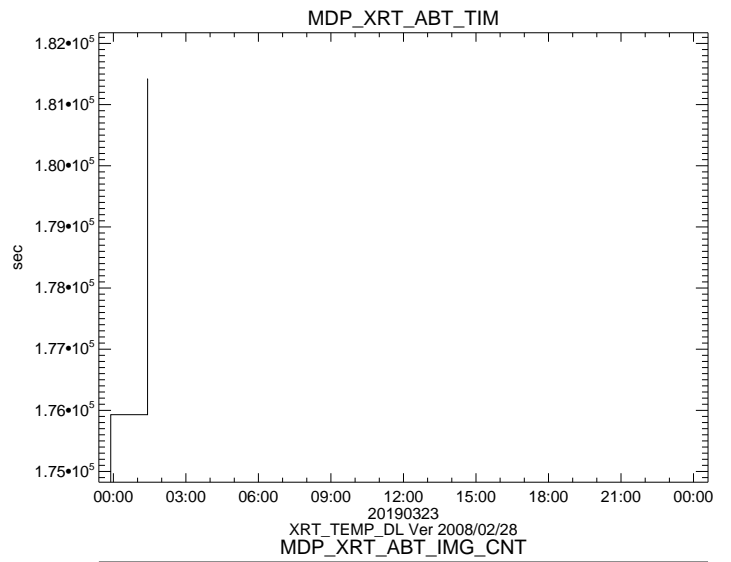
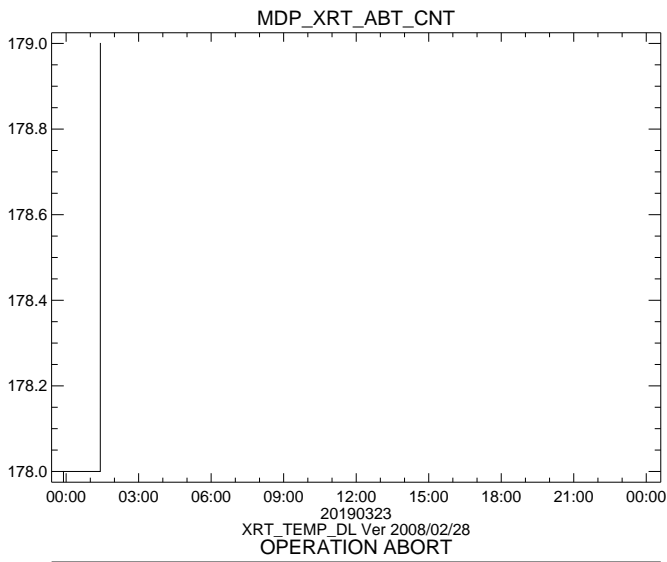
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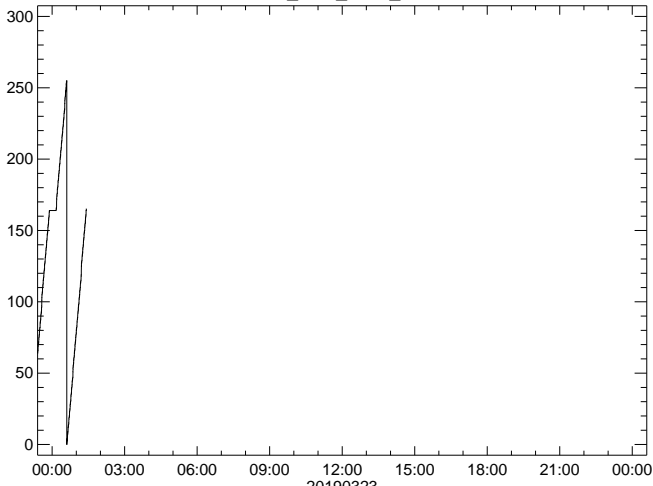
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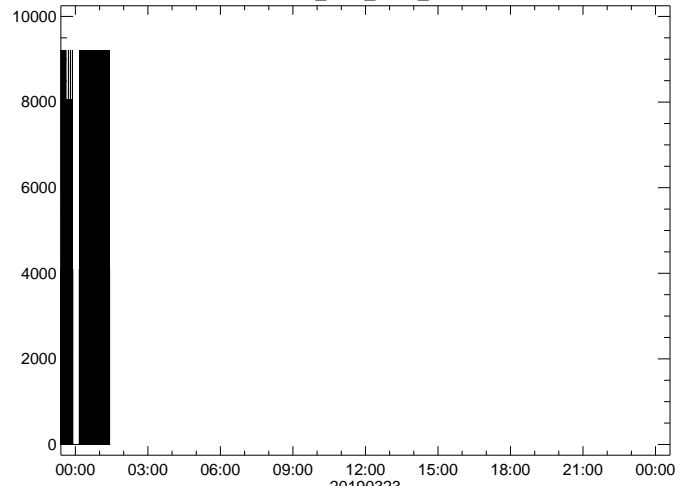




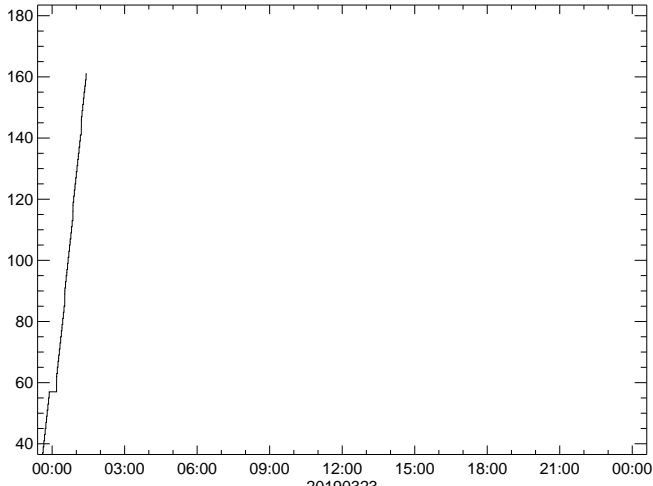
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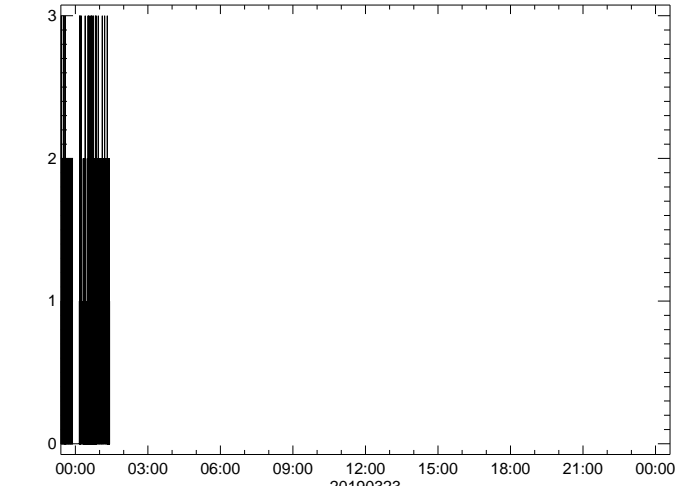
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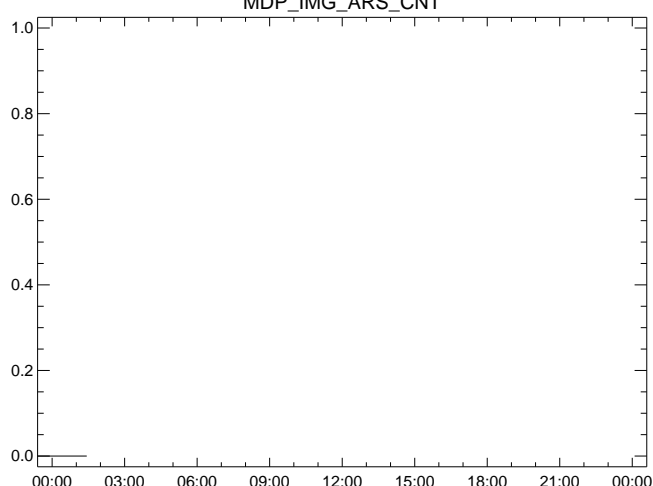
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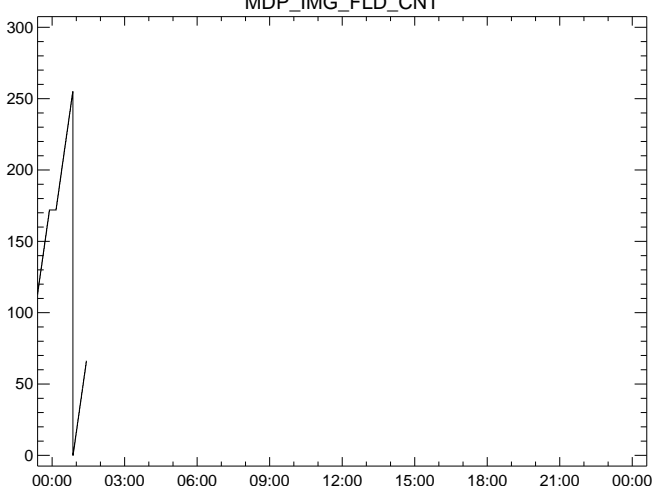
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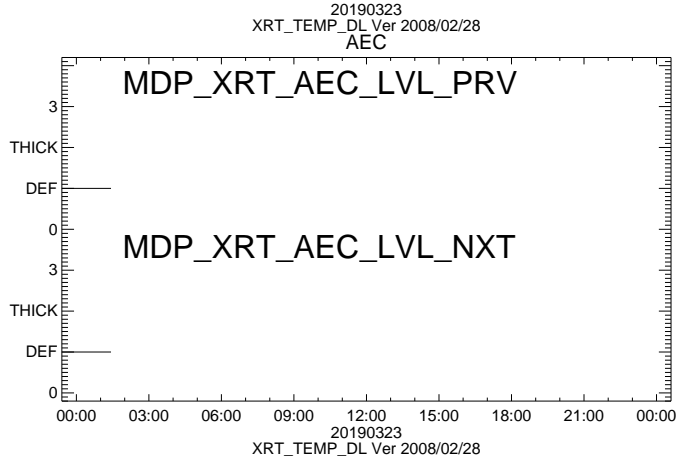
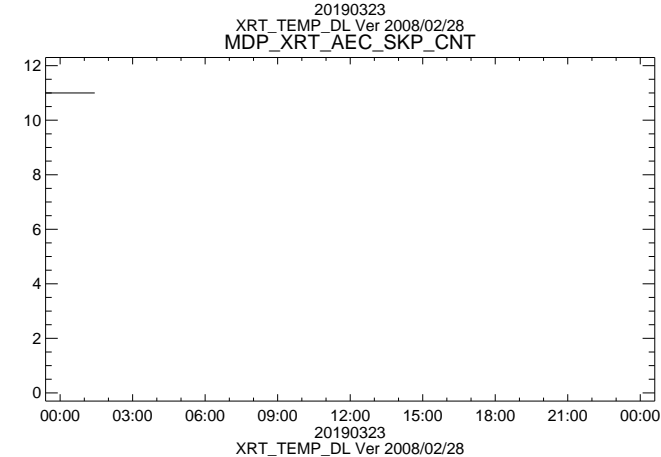
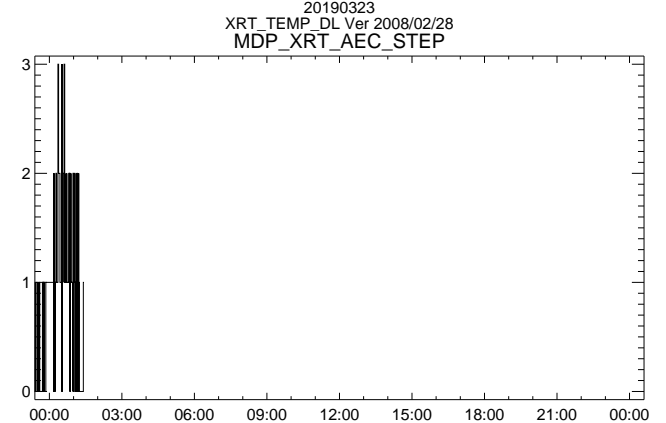
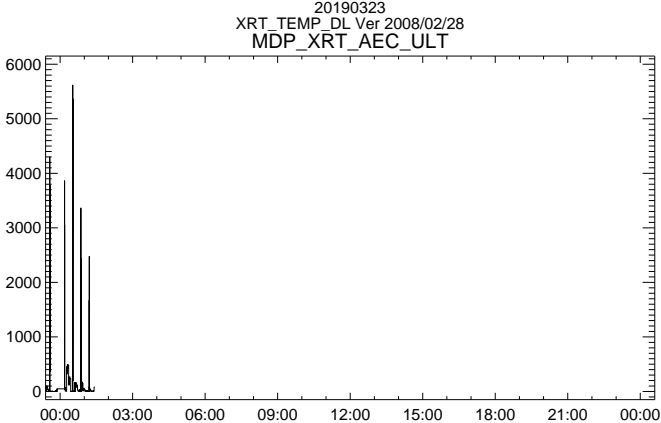
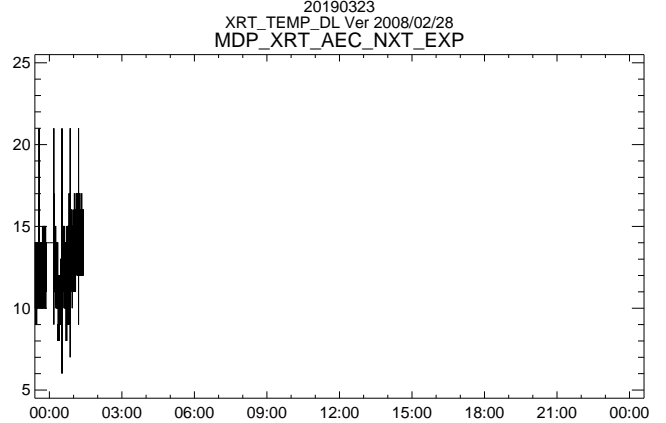
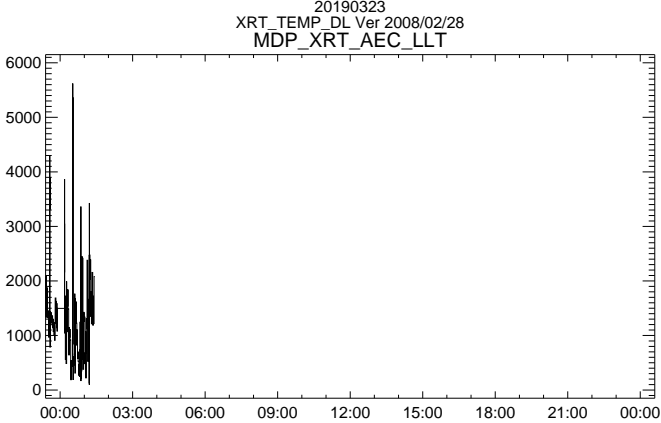
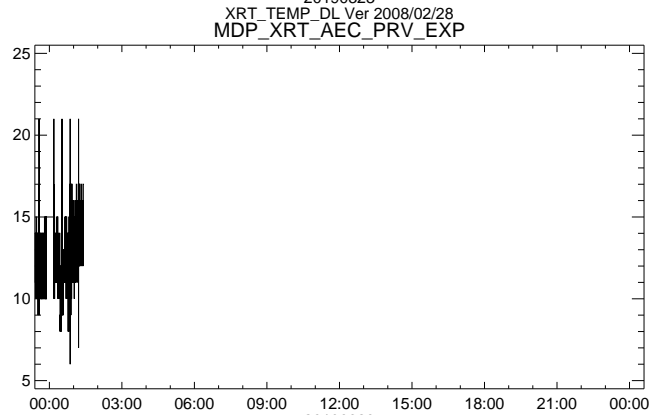
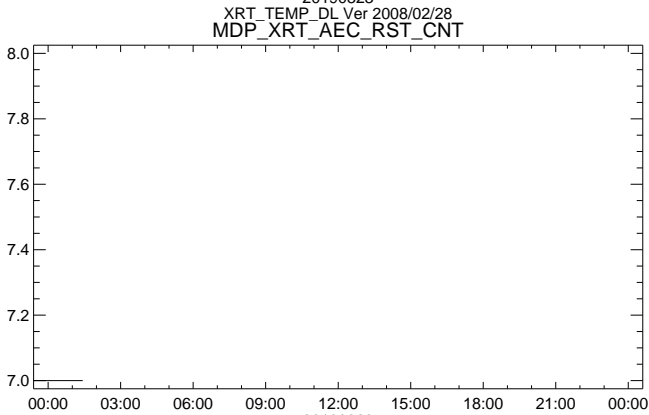
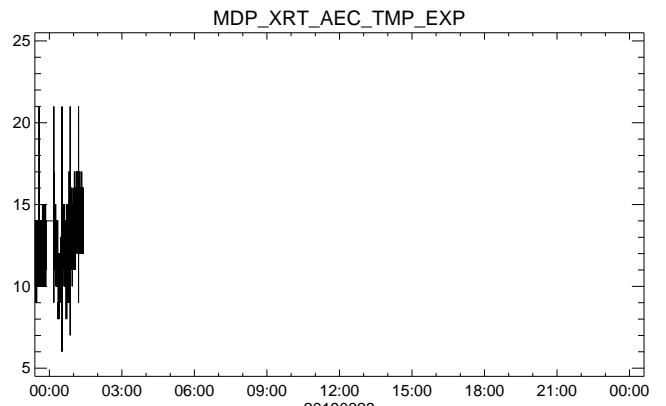
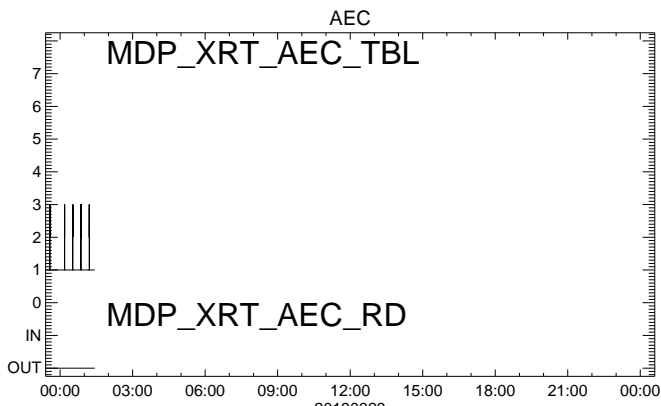


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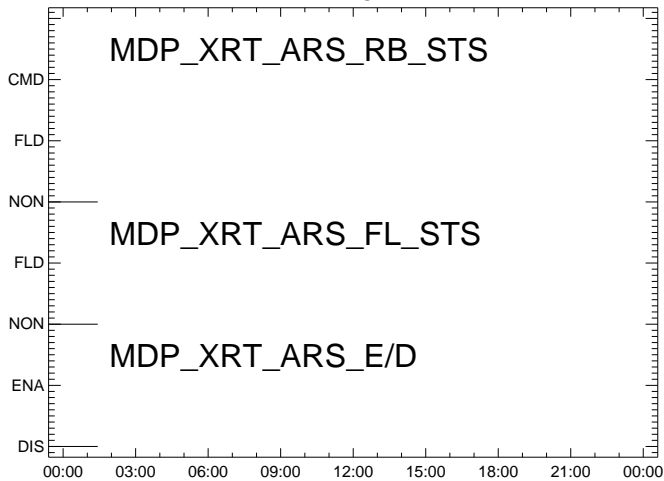


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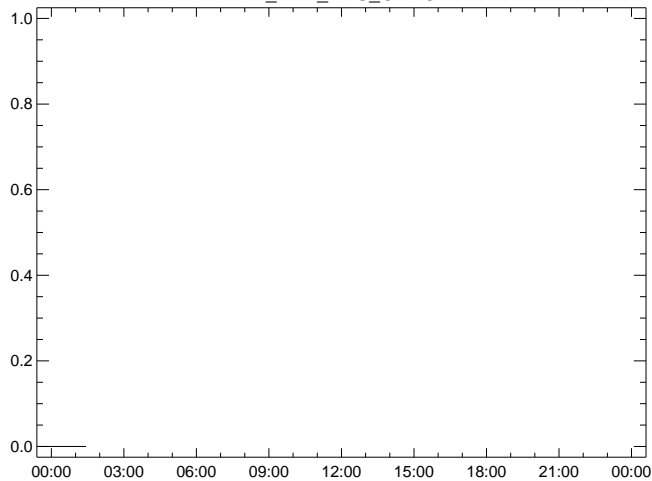




ARS

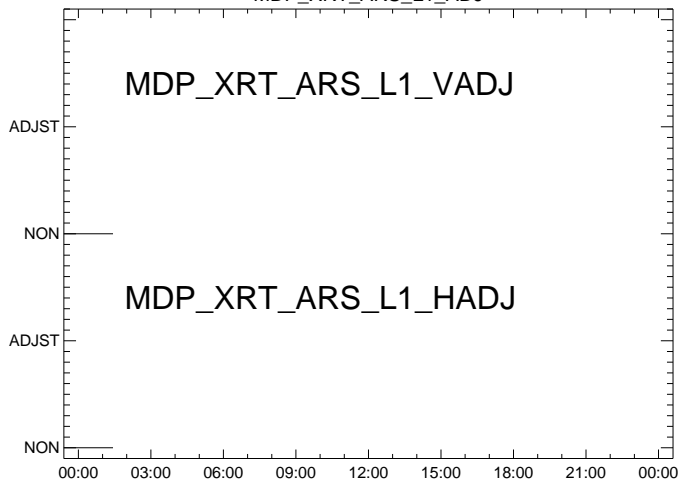
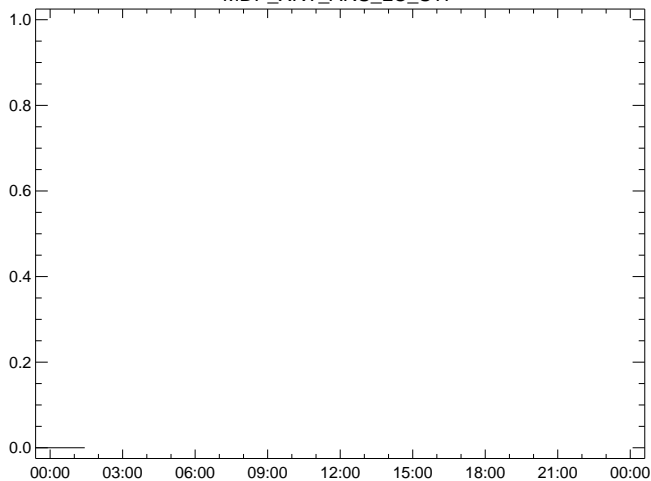


MDP_XRT_ARS_CANCEL



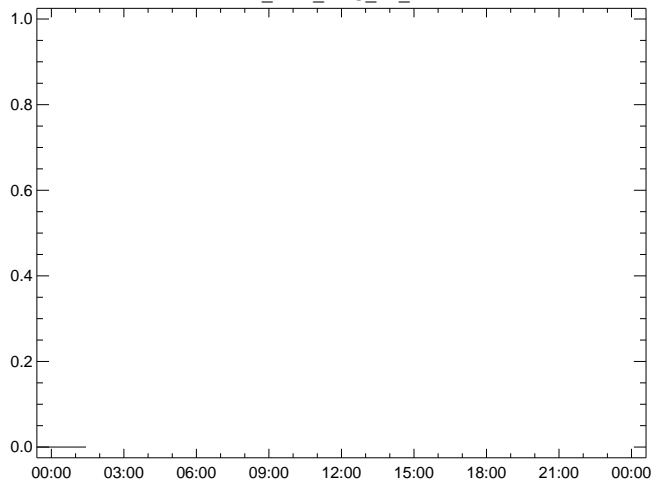
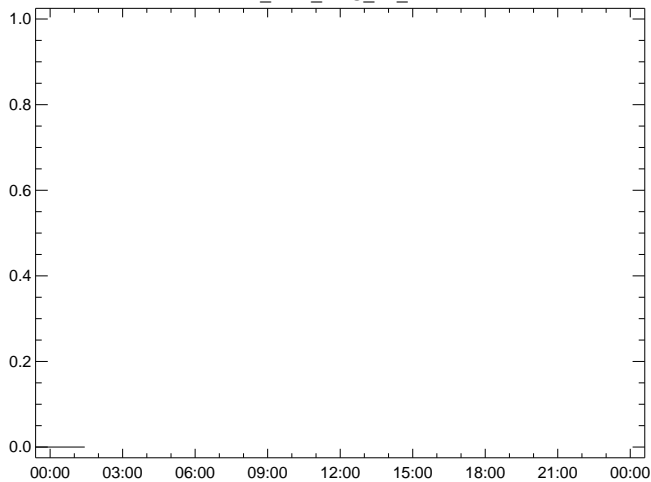
20190323
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_ARS_LC_STP

20190323
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_ARS_L1_ADJ



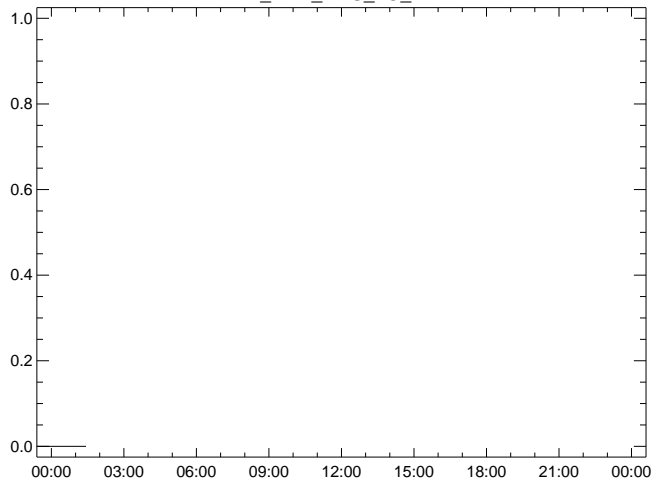
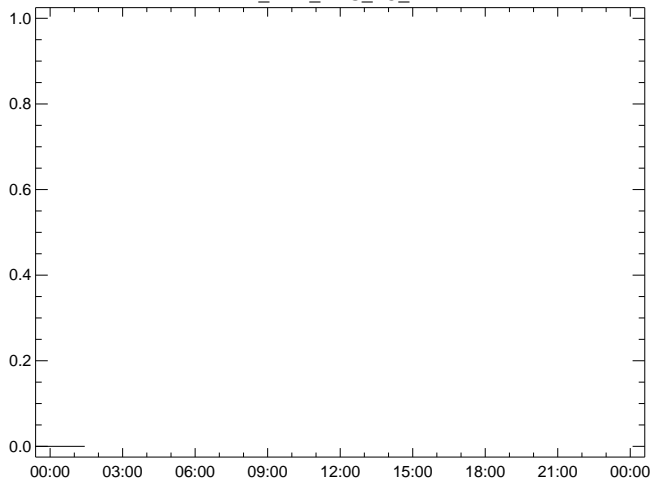
20190323
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_ARS_L2_HADR

20190323
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_ARS_L2_VADR



20190323
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_ARS_LC_HADR

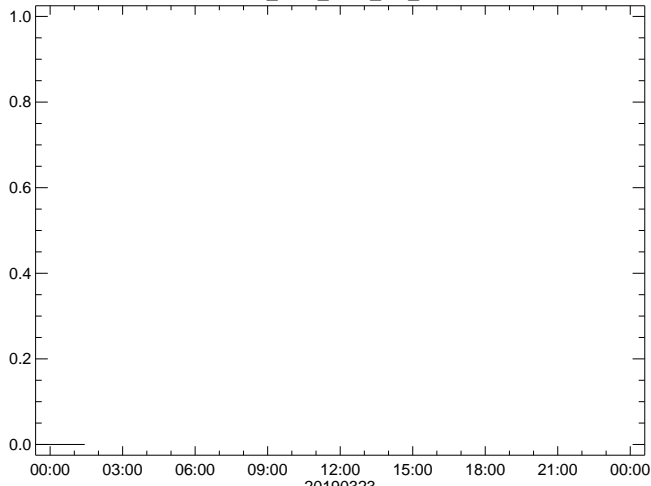
20190323
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_ARS_LC_VADR



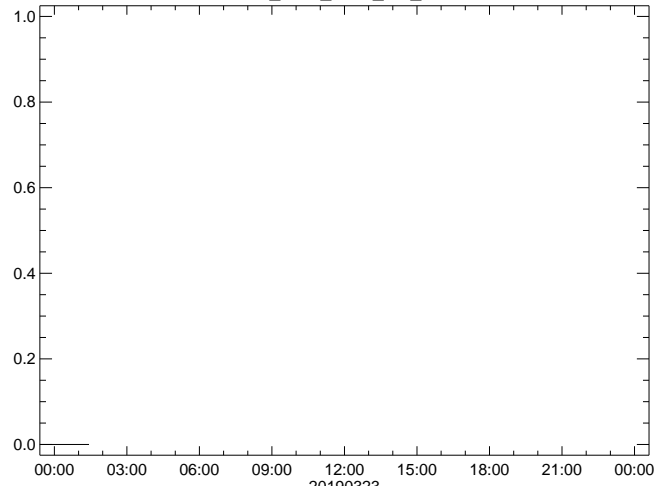
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XRT_TEMP_DL Ver 2008/02/28

20190323
XRT_TEMP_DL Ver 2008/02/28

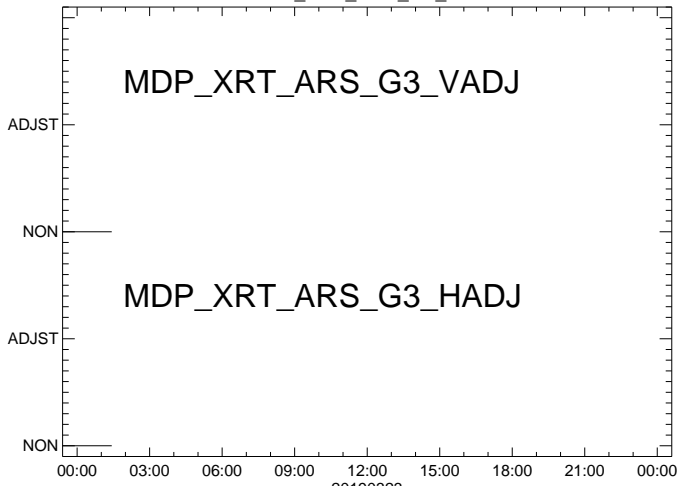
MDP_XRT_ARS_GL_STP



MDP_XRT_ARS_G2_ERR



MDP_XRT_ARS_G3_ADJ



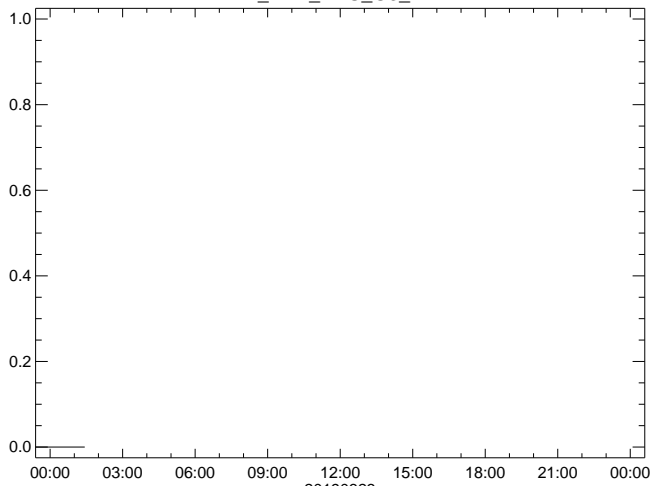
MDP_XRT_ARS_G3_VADJ

MDP_XRT_ARS_G4_VADJ

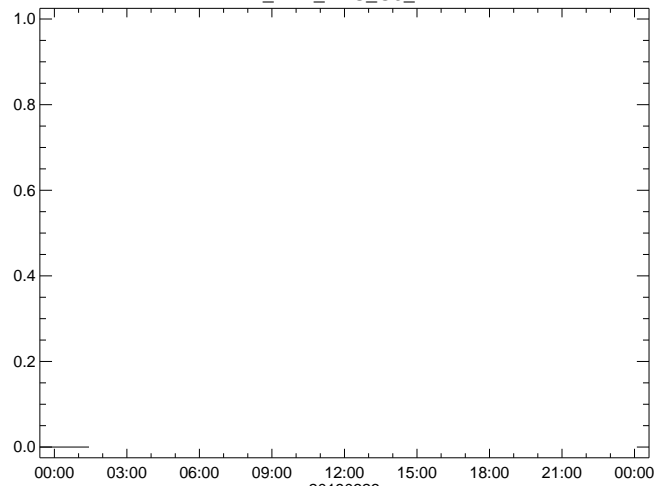
MDP_XRT_ARS_G3_HADJ

MDP_XRT_ARS_G4_HADJ

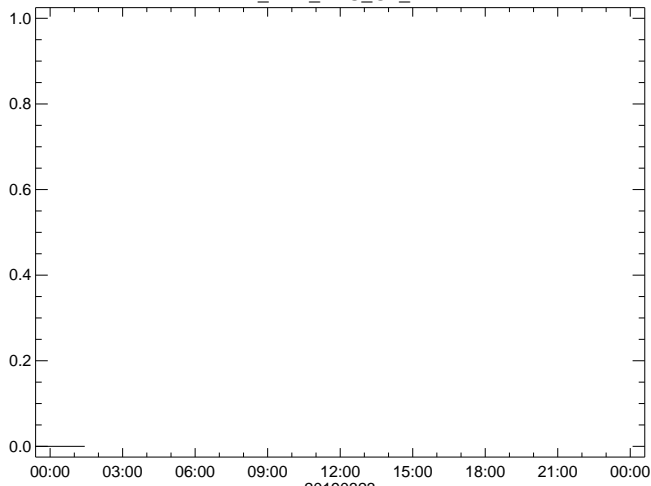
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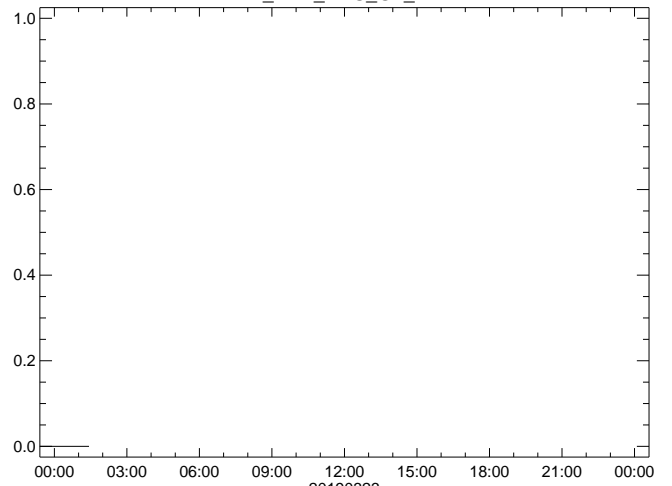
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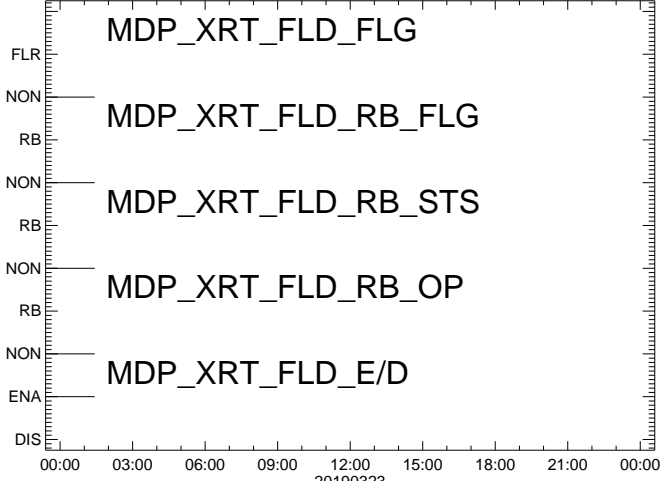
MDP_XRT_ARS_GL_HADR



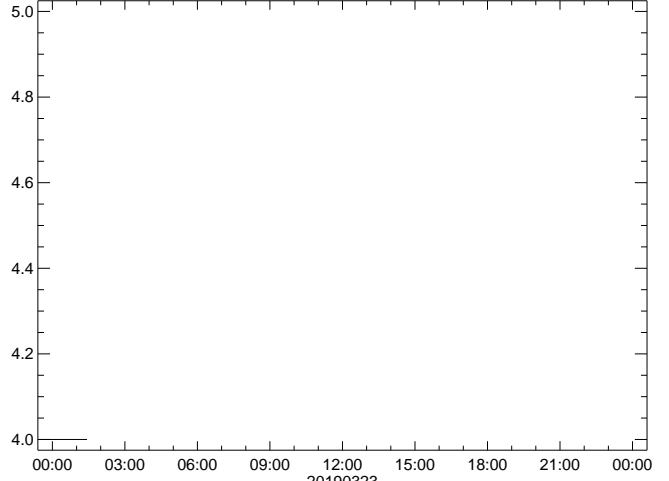
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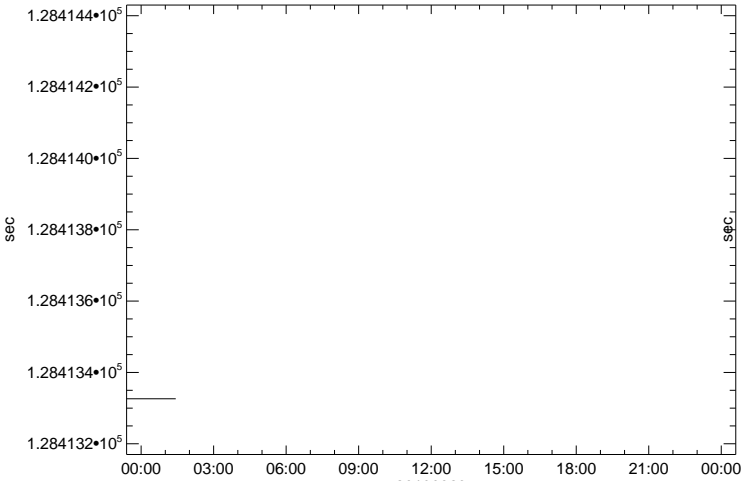
FLD



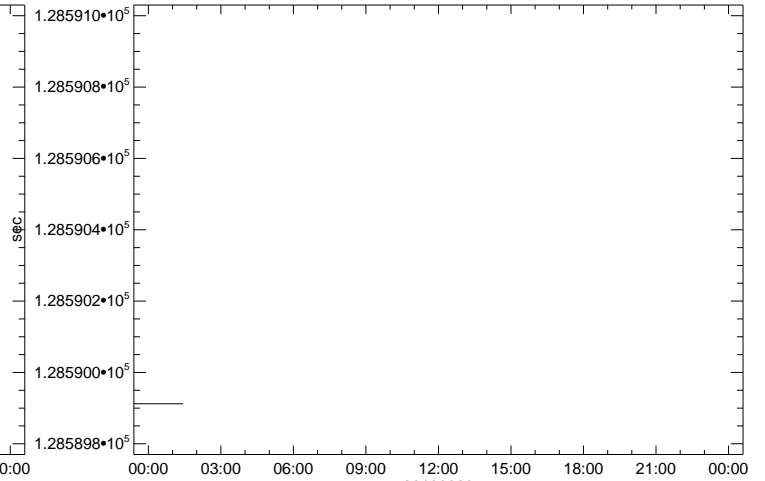
MDP_XRT_FLD_CANCEL



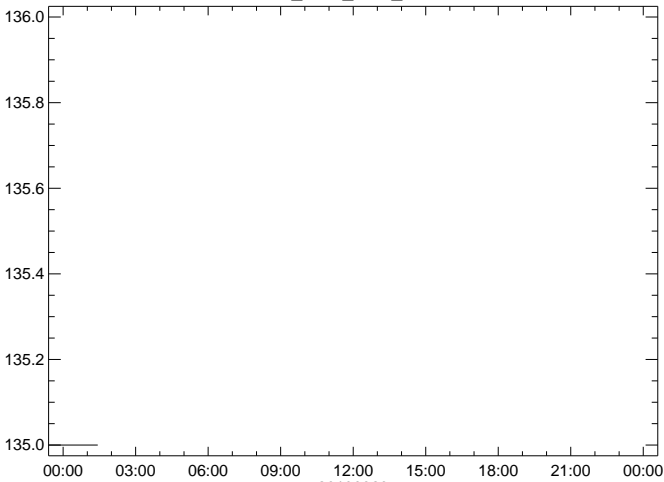
20190323
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_FLR_OCR_TIM



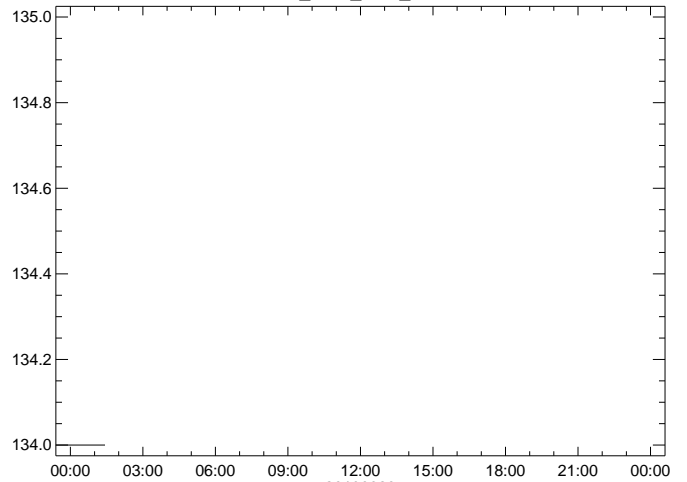
20190323
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_FLR_TRM_TIM



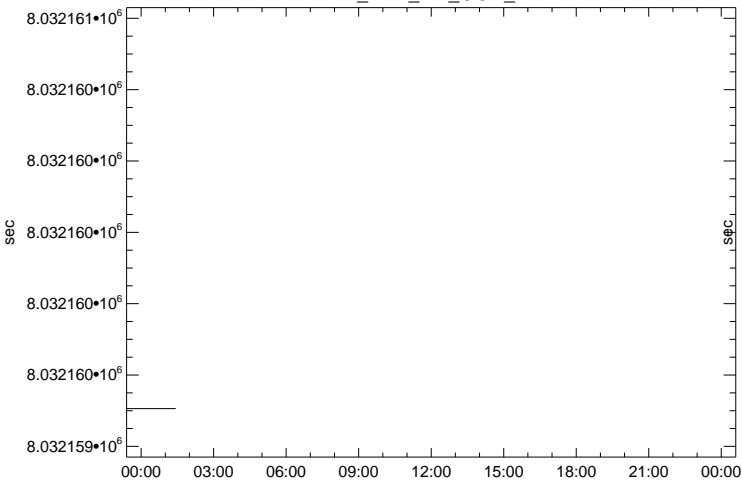
20190323
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_FLD_HADR



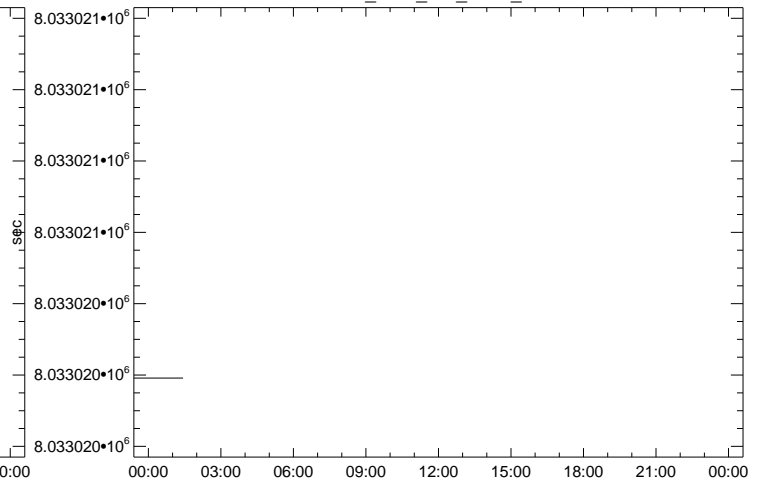
20190323
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_FLD_VADR

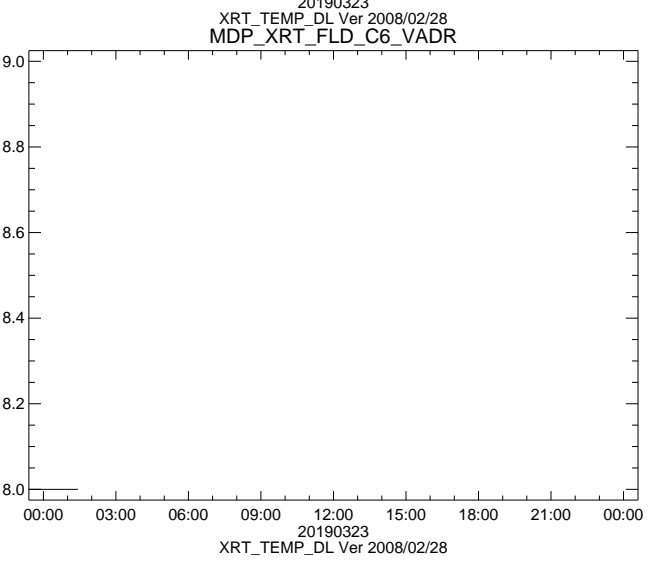
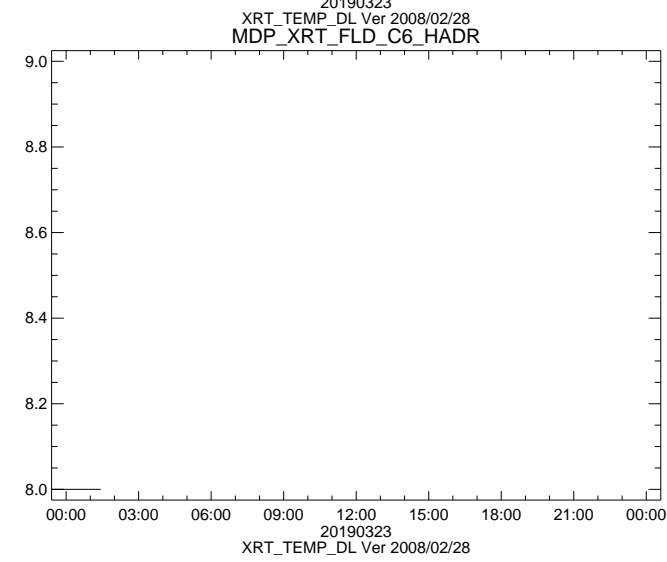
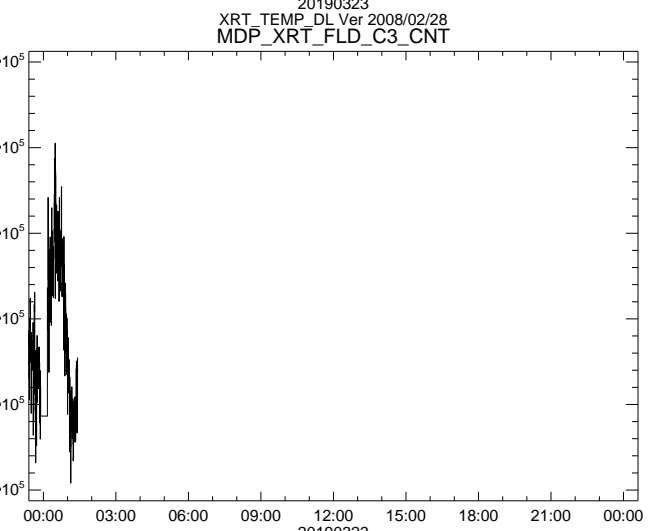
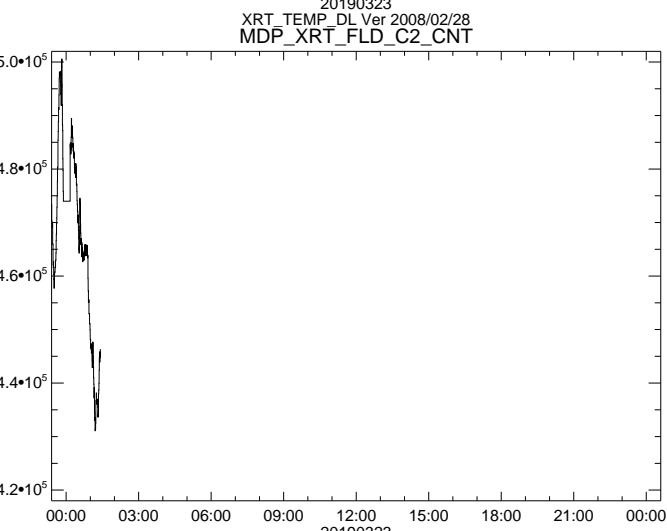
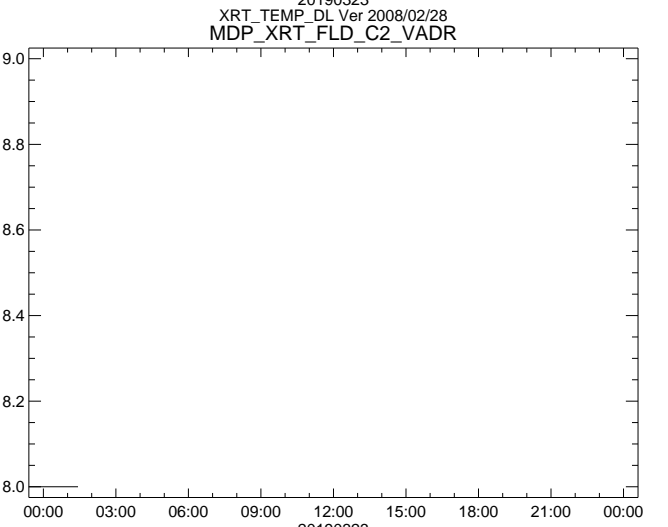
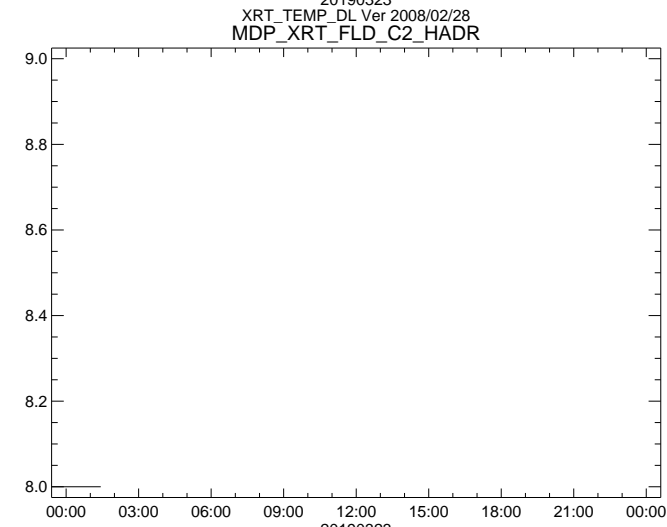
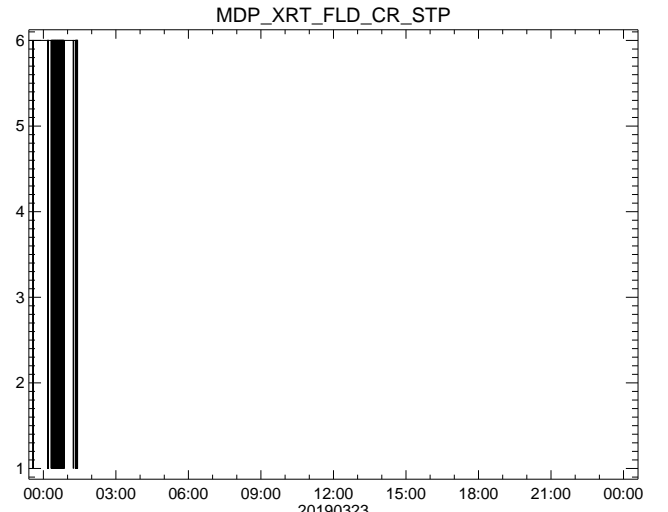
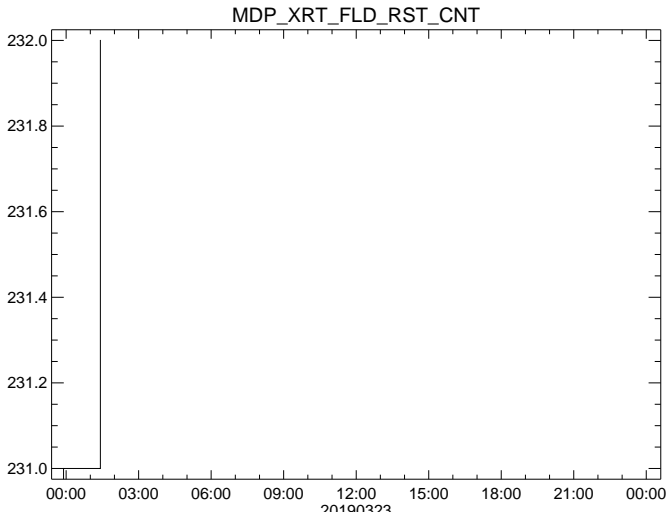


20190323
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_RD_OCR_TIM

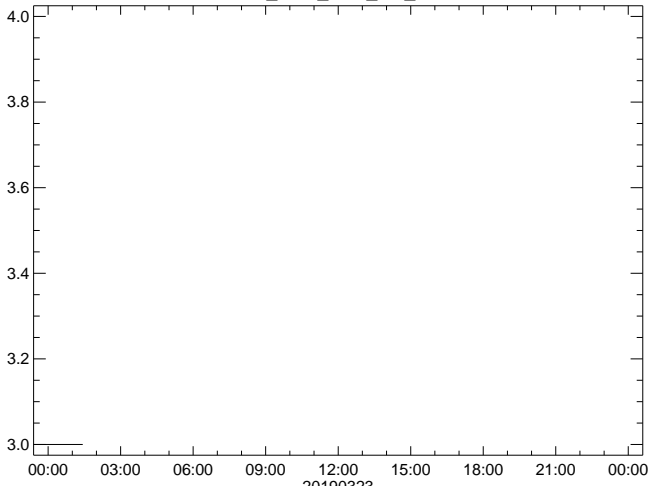


20190323
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_RD_TRM_TIM

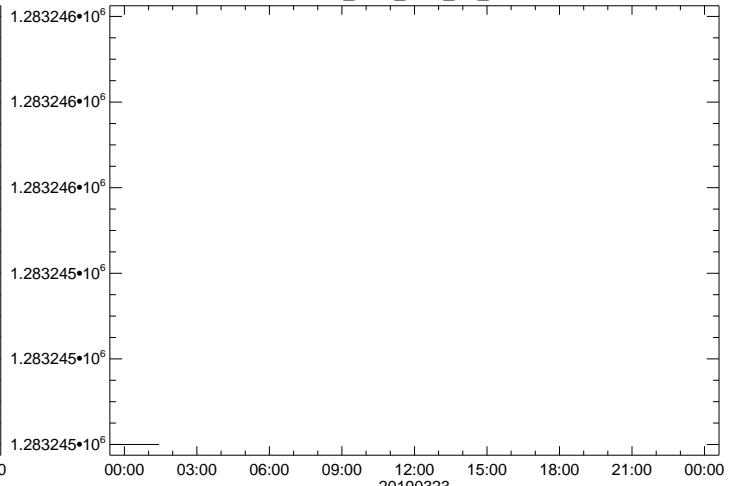




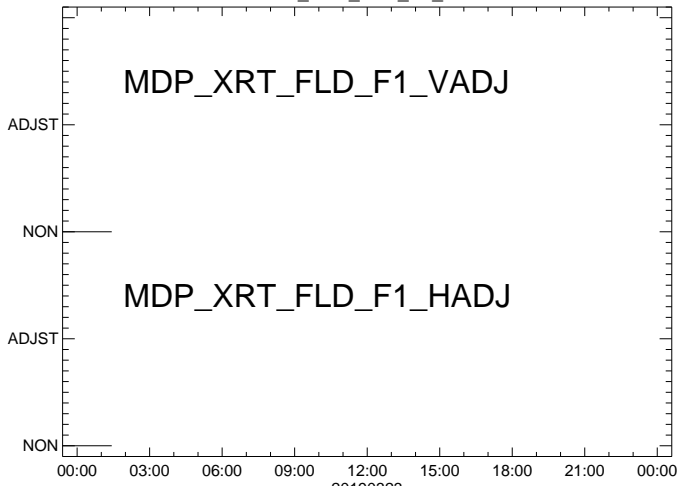
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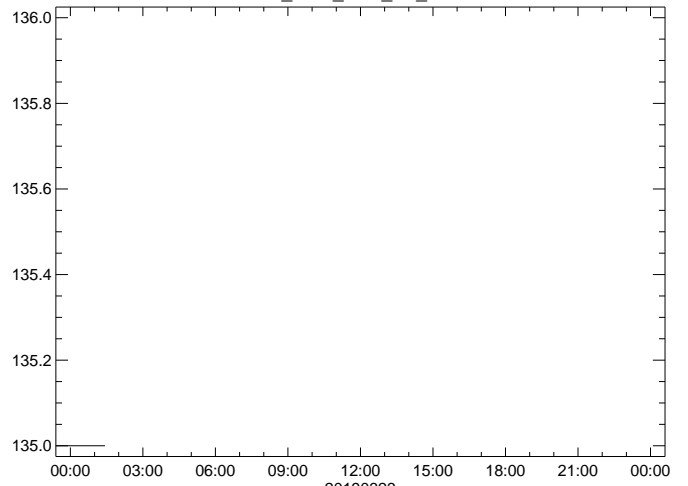
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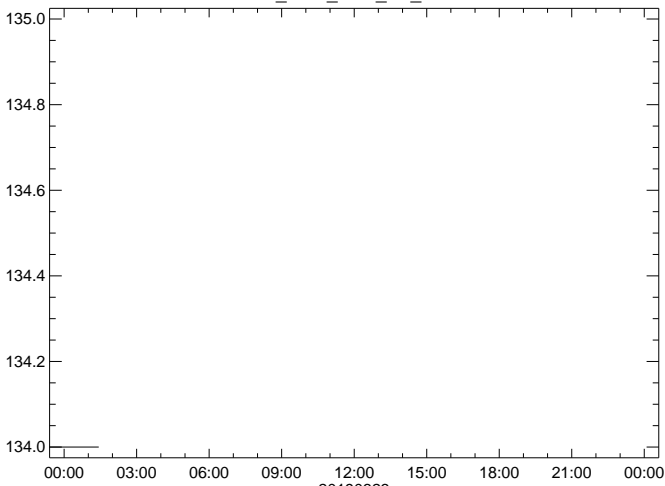
20190323
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_FLD_F1_ADJ



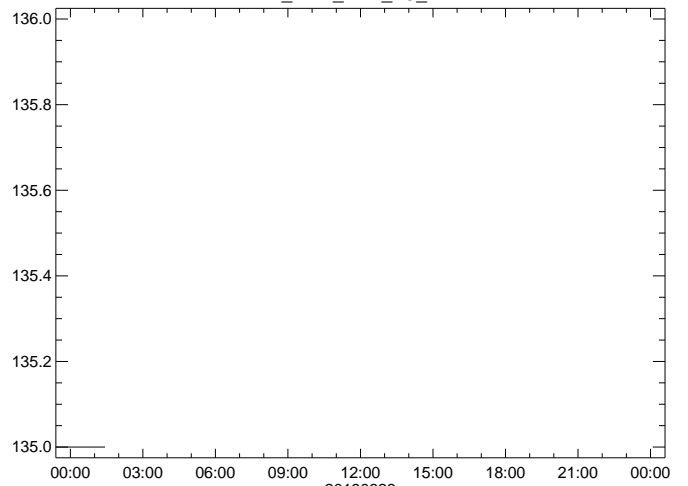
20190323
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_FLD_F2_HADR



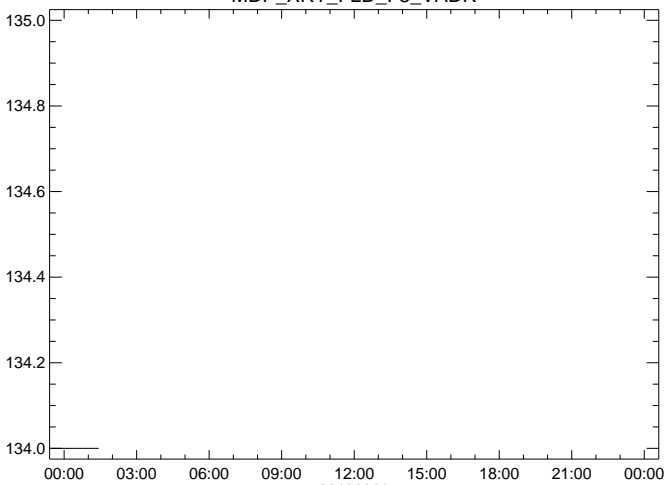
20190323
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_FLD_F2_VADR



20190323
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_FLD_F3_HADR

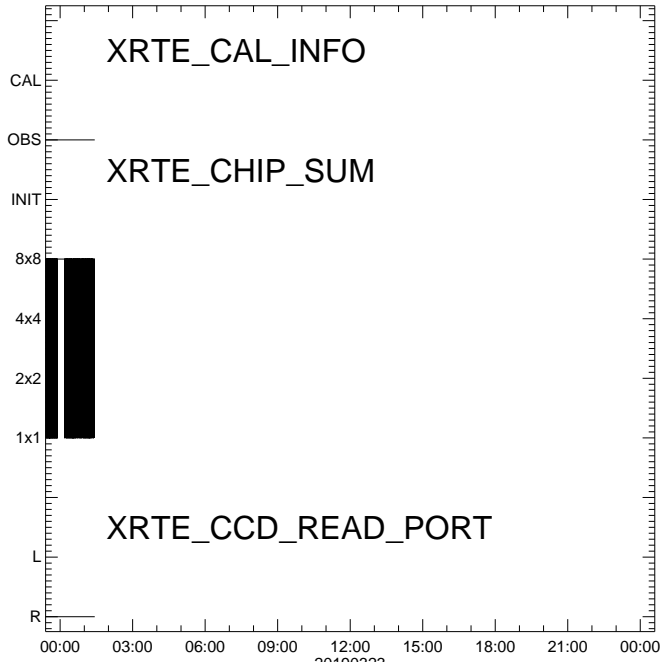


20190323
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_FLD_F3_VADR

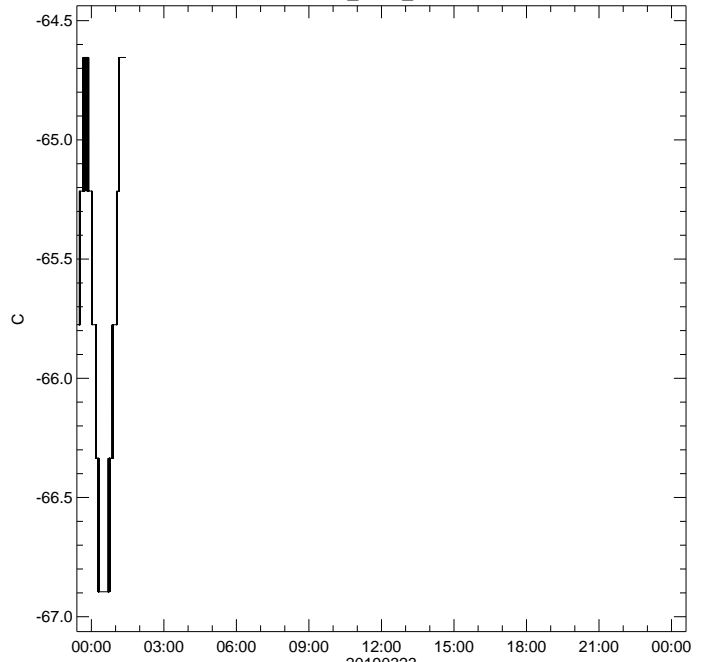


20190323
XRT_TEMP_DL Ver 2008/02/28

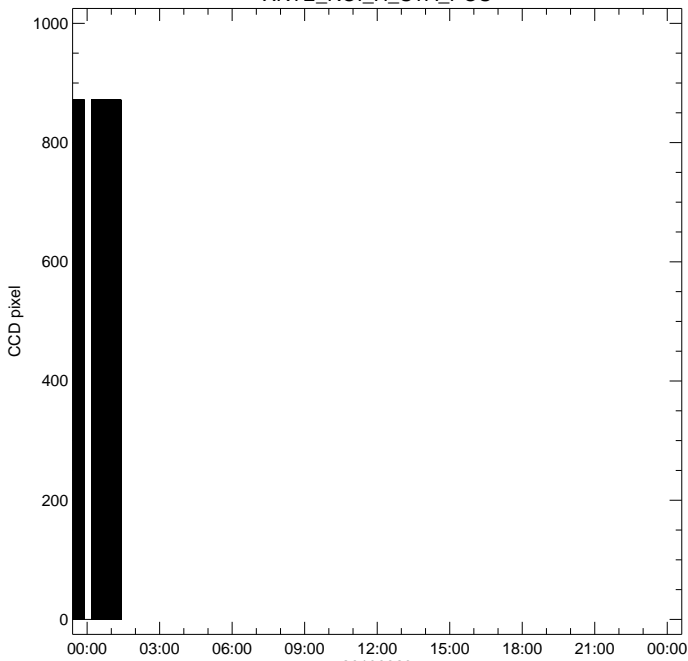
ON/OFF Statuses



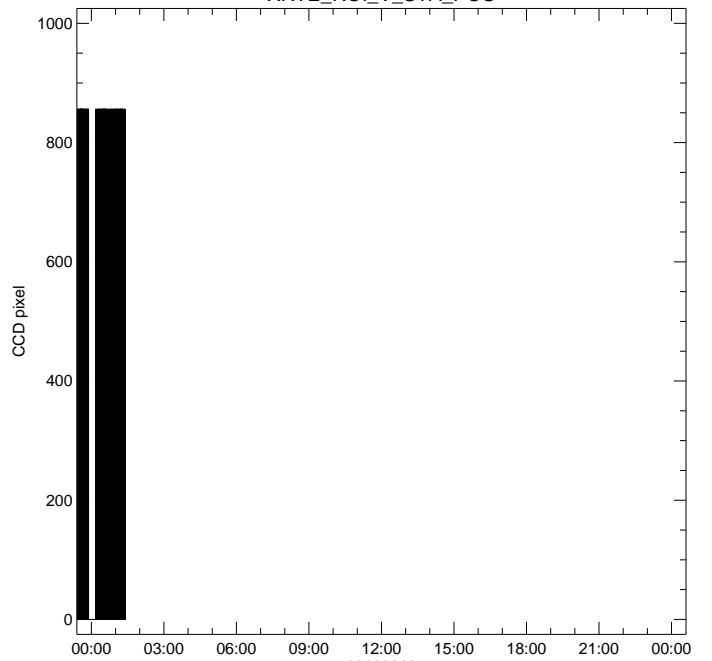
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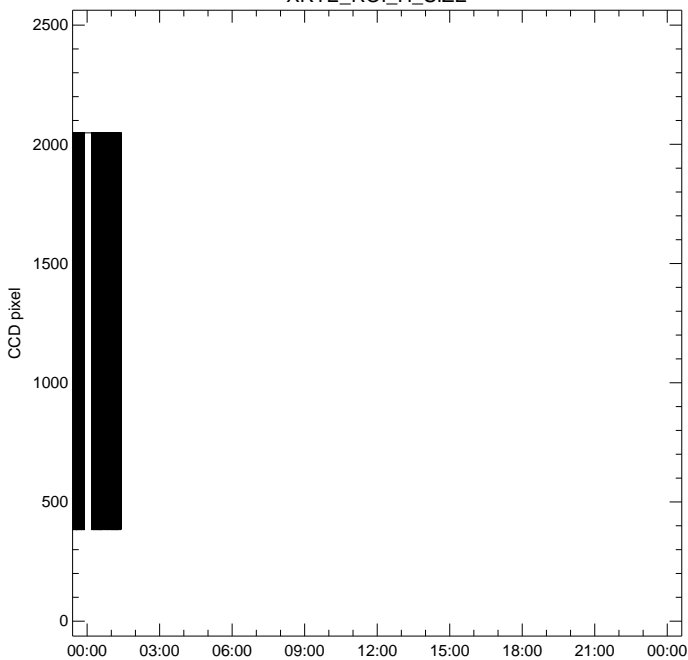
XRTE_ROI_H_STA_POS



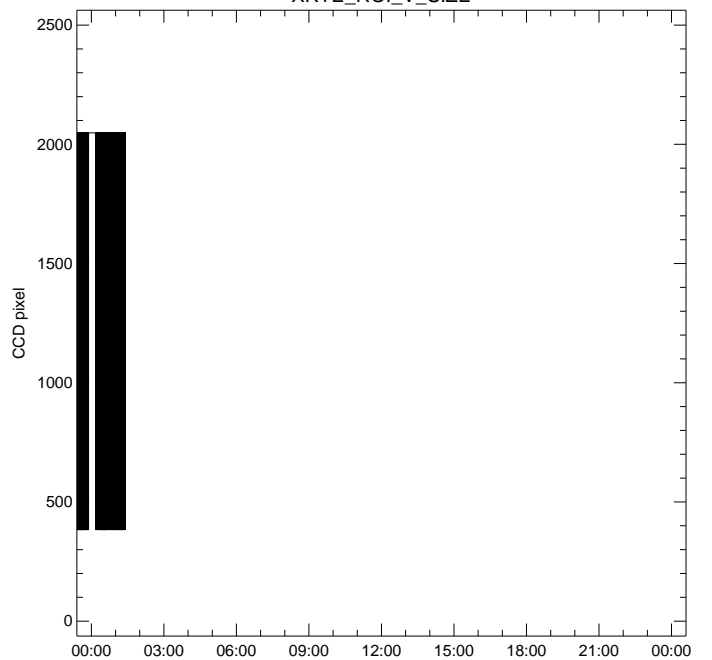
XRTE_ROI_V_STA_POS



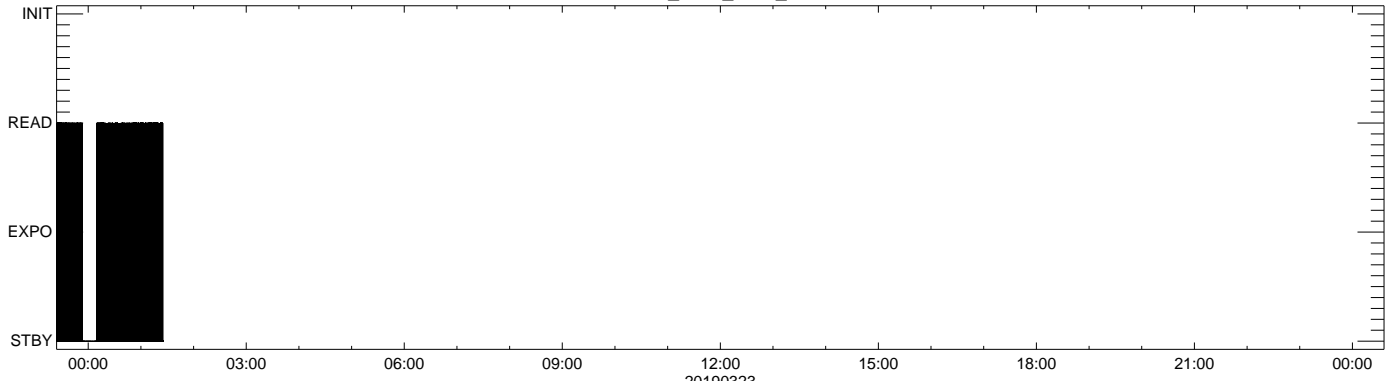
XRTE_ROI_H_SIZE



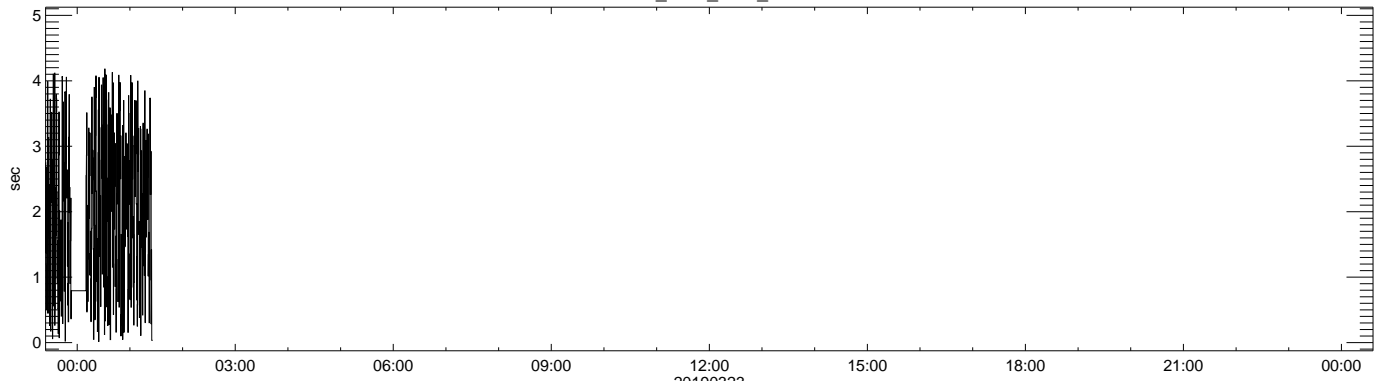
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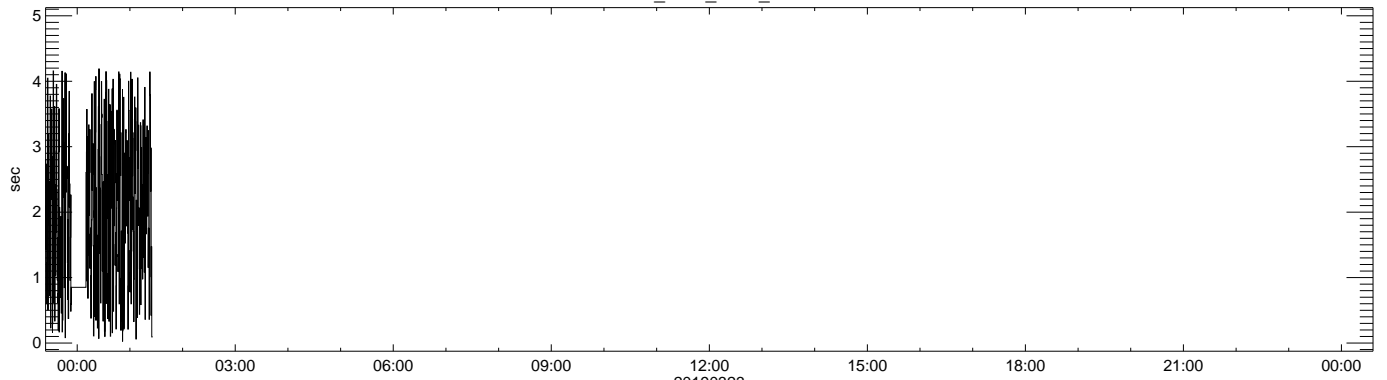
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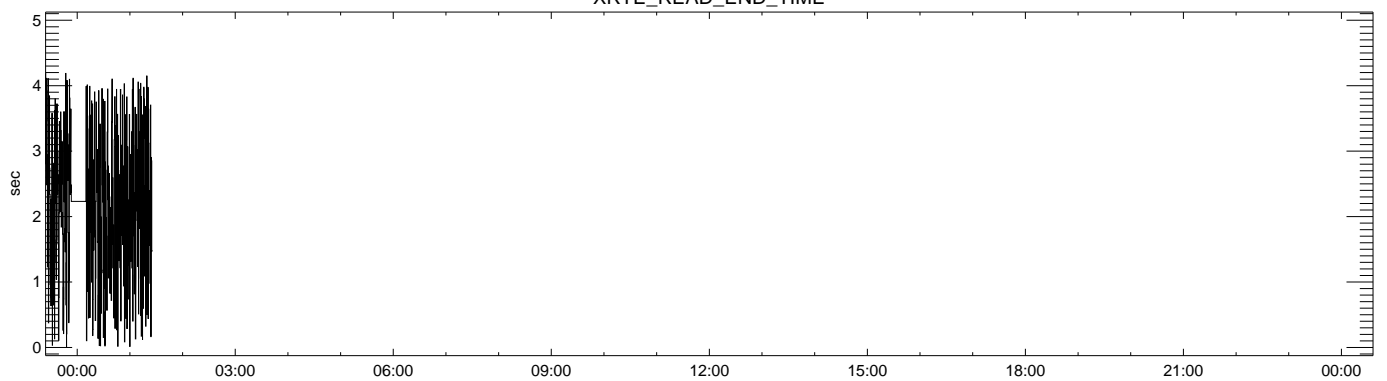
12:00
20190323
XRT_TEMP_DL Ver 2008/02/28
XRTE_EXP_STA_TIME



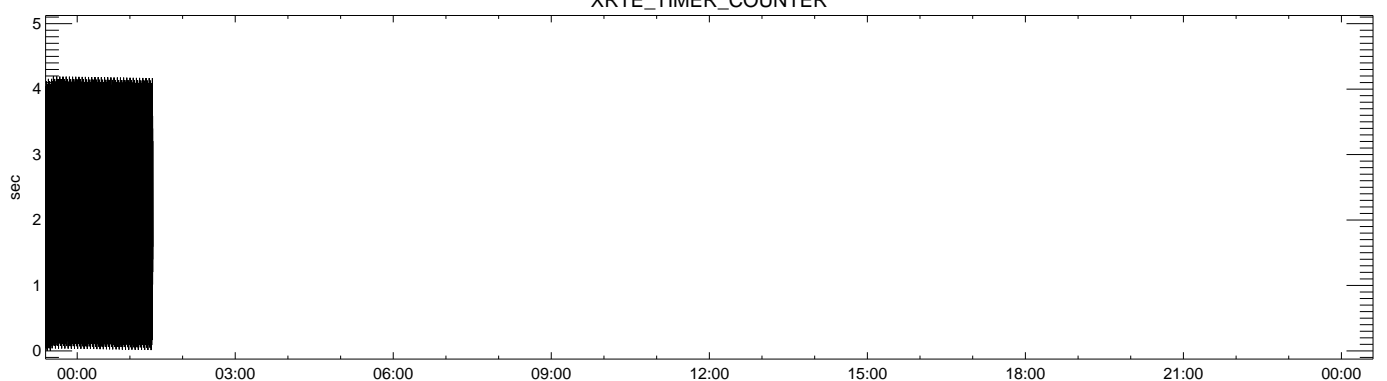
12:00
20190323
XRT_TEMP_DL Ver 2008/02/28
XRTE_EXP_END_TIME



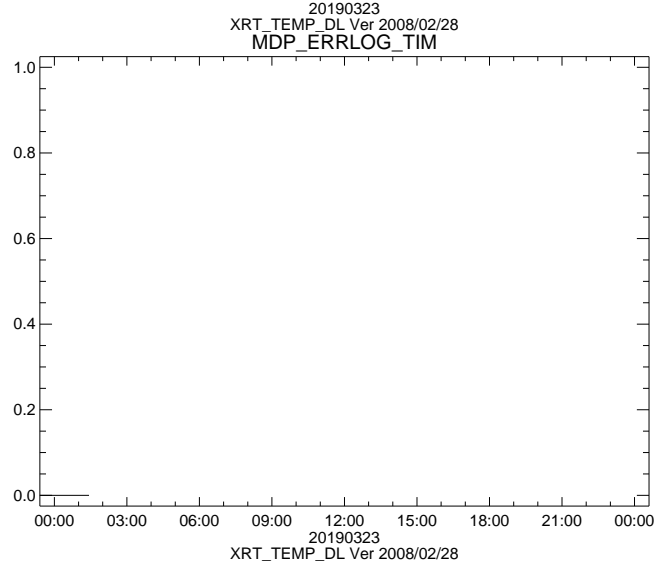
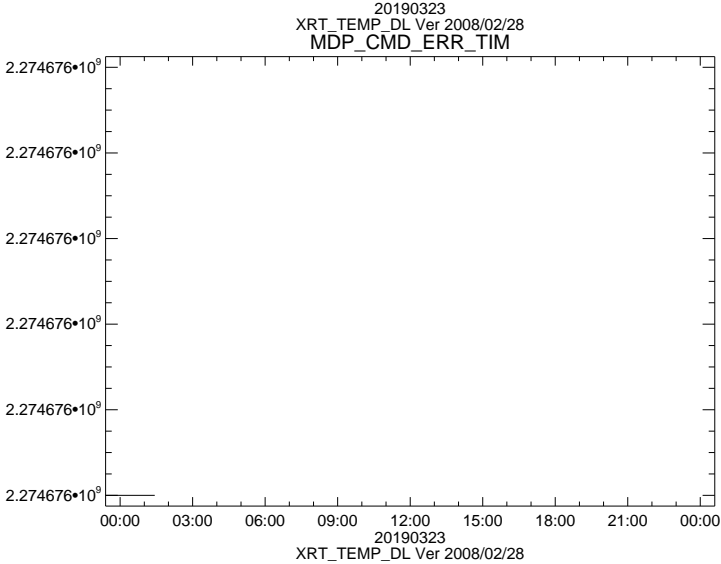
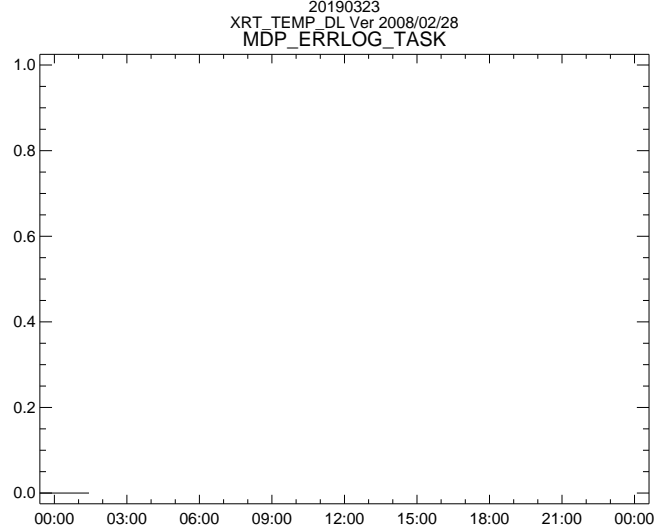
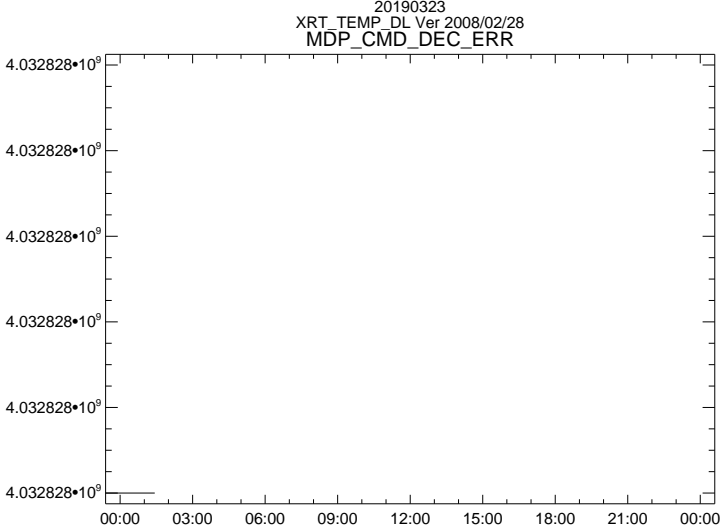
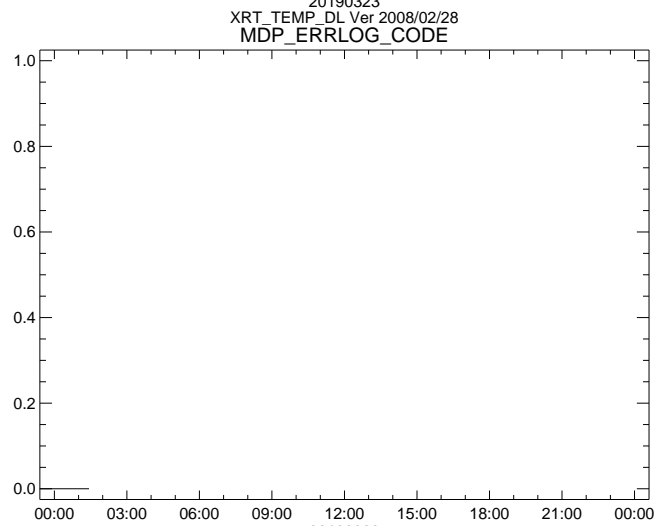
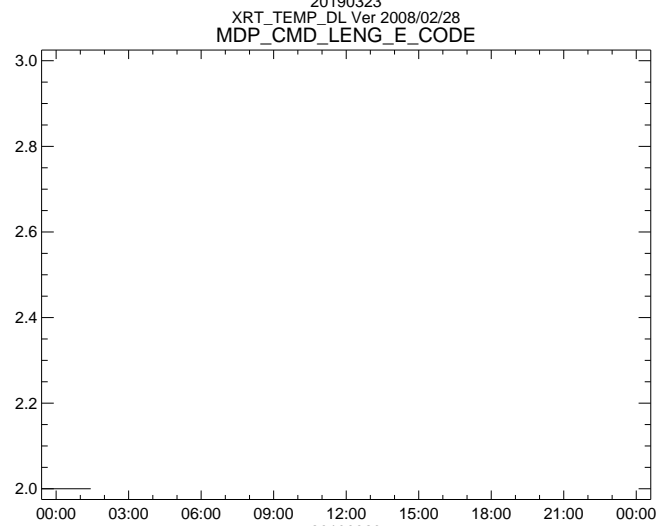
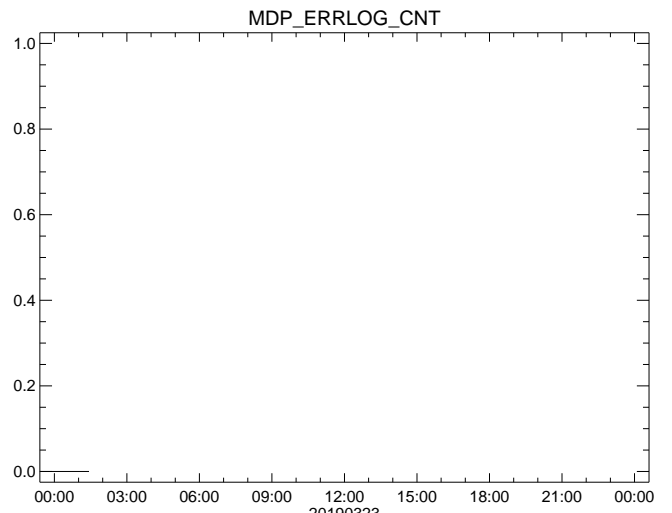
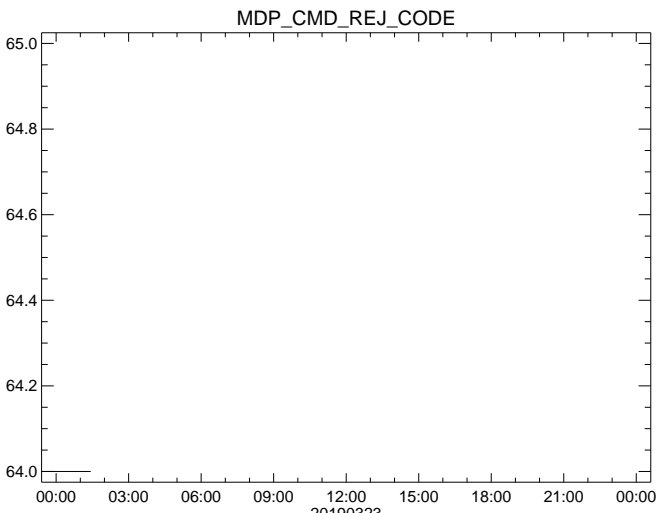
12:00
20190323
XRT_TEMP_DL Ver 2008/02/28
XRTE_READ_END_TIME



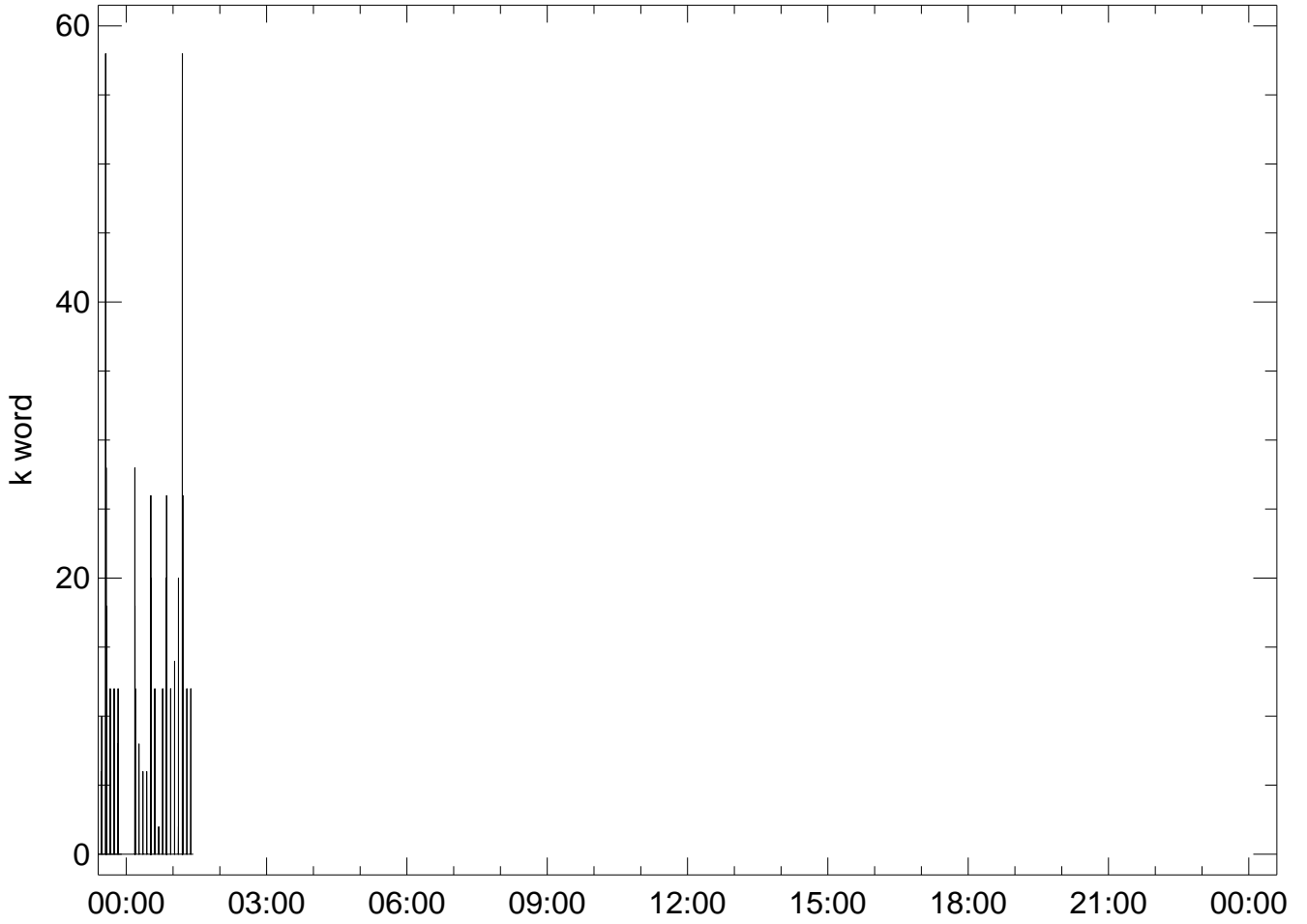
12:00
20190323
XRT_TEMP_DL Ver 2008/02/28
XRTE_TIMER_COUNTER



12:00
20190323
XRT_TEMP_DL Ver 2008/02/28

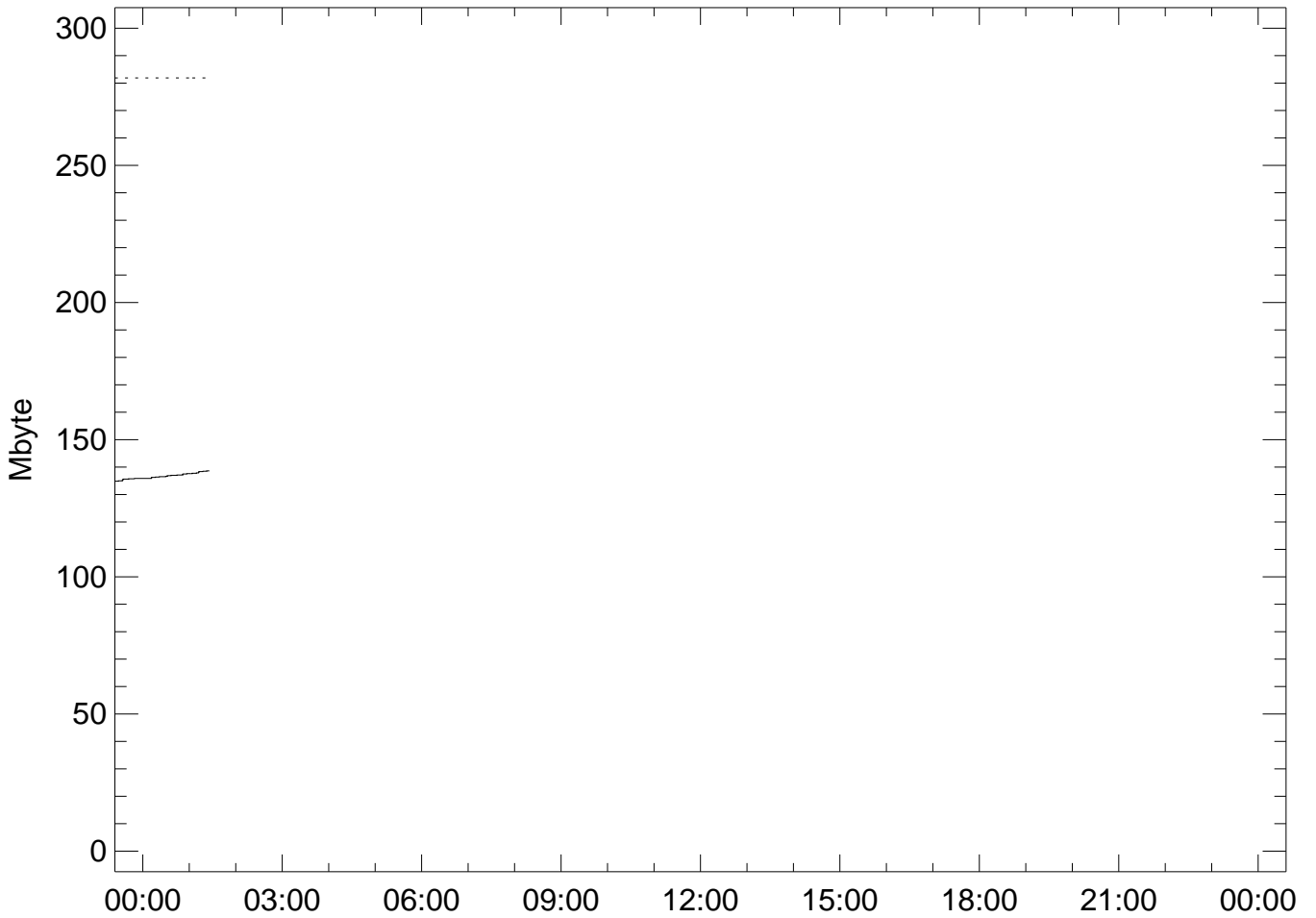


MDP_PST_CMP_XRT_ACM



20190323

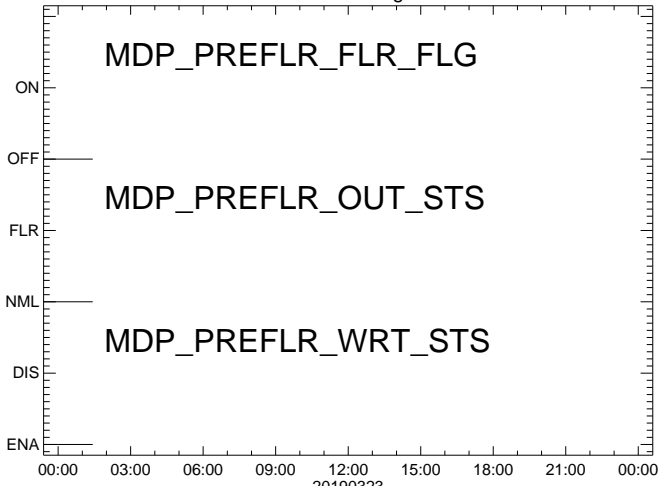
XRT_TEMP_DL Ver 2008/02/28 MDP_XRT_TLM_LIM & CNT



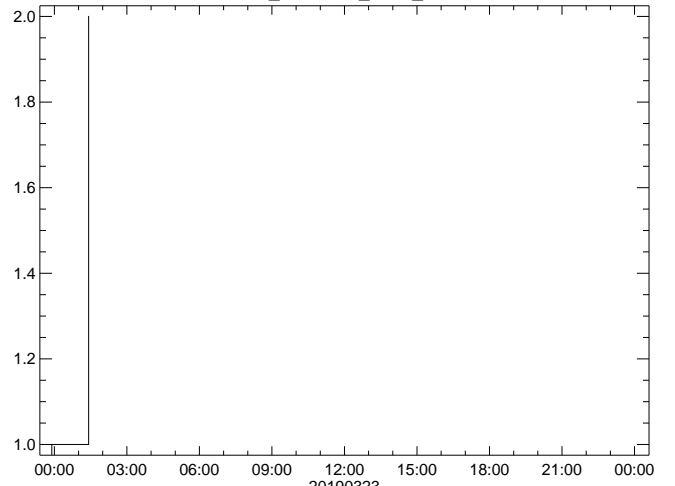
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XRT_TEMP_DL Ver 2008/02/28

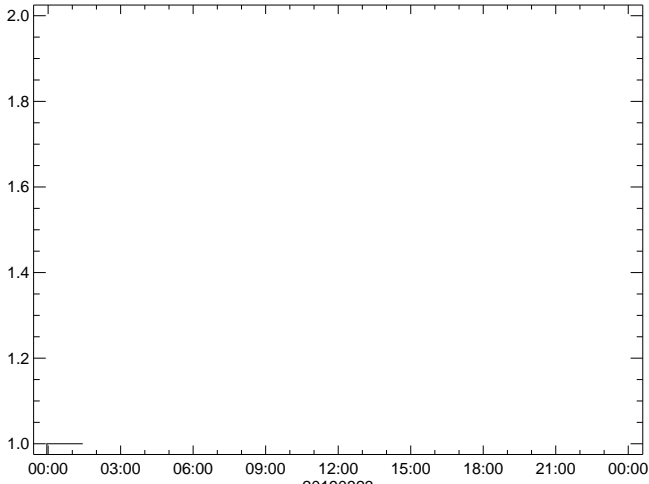
PreFlare Flags



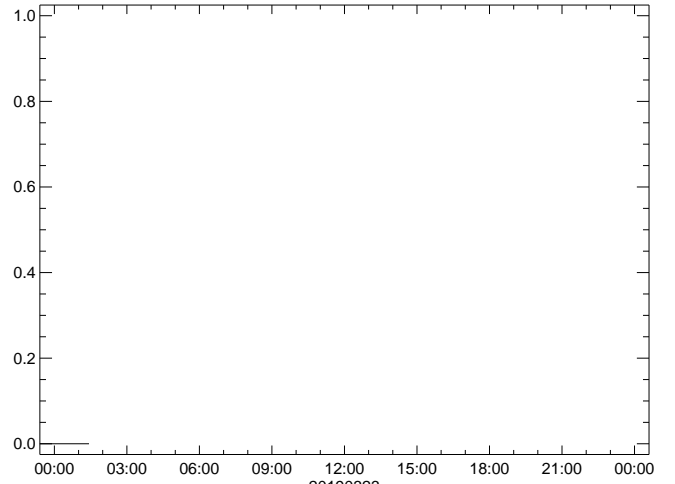
MDP_PREFLR_OUT_CNT



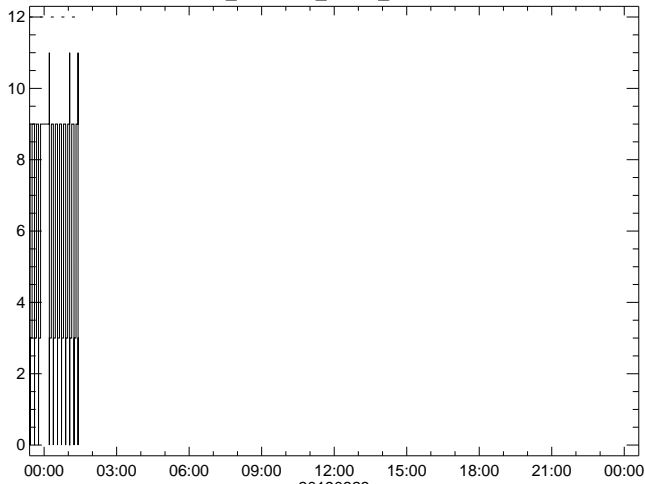
MDP_PREFLR_STP_CNT



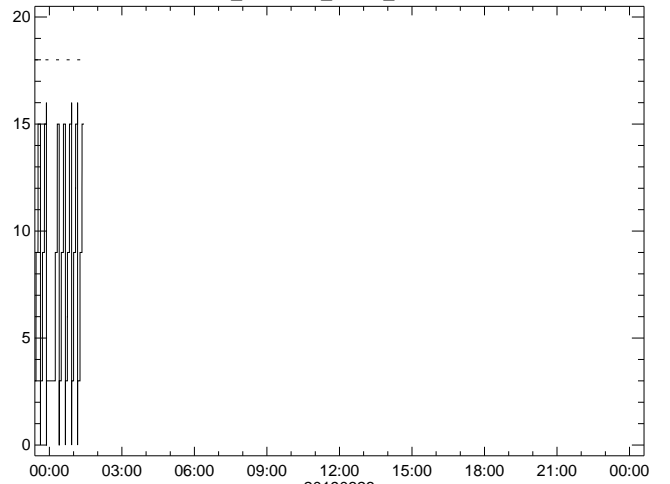
MDP_PREFLR_RST_CNT



MDP_PREFLR_BUF1_CNT & WP



MDP_PREFLR_BUF2_CNT & WP



MDP_PREFLR_BUF3_CNT & WP

