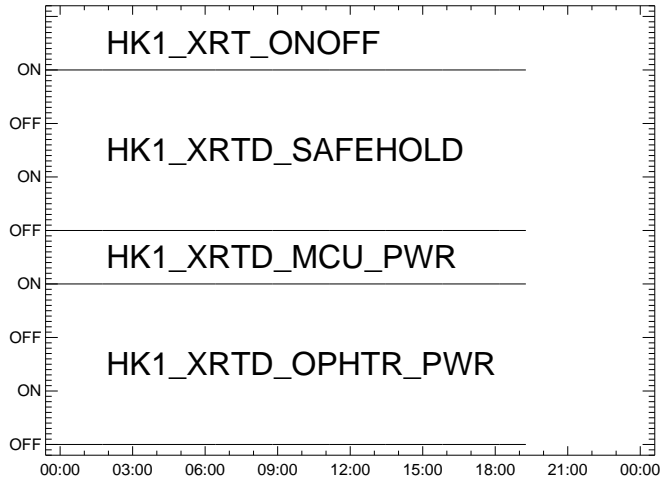
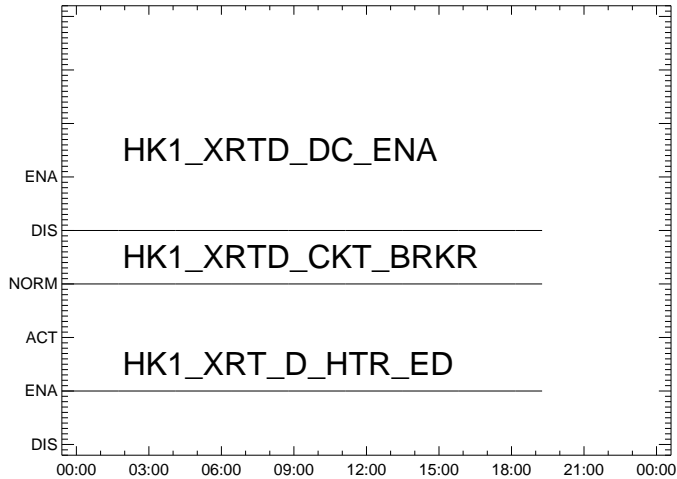


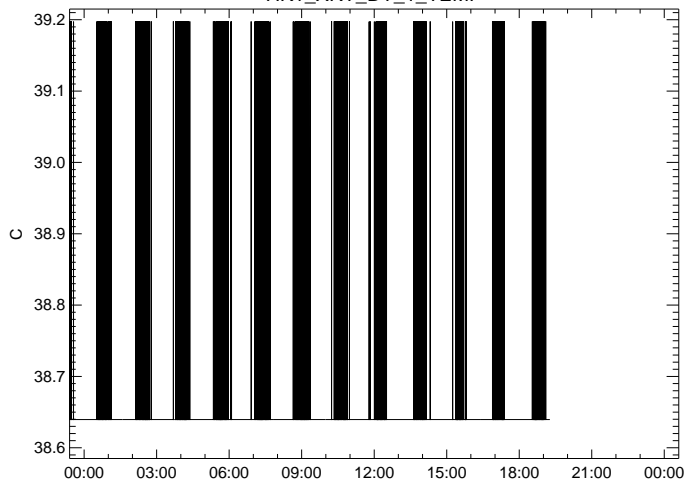
ON/OFF Statuses



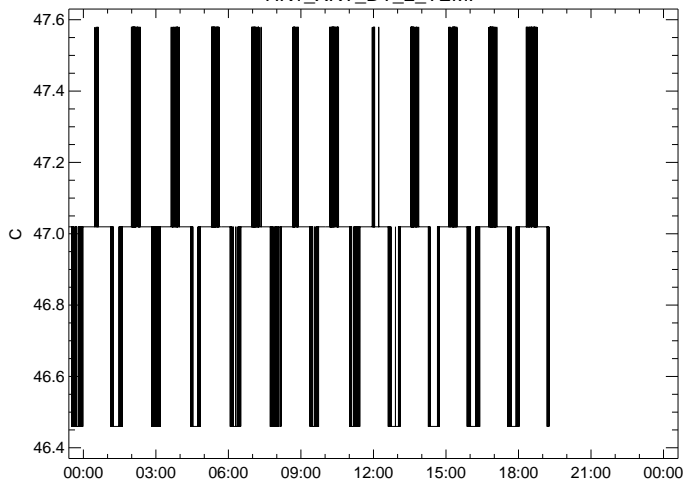
ON/OFF Statuses



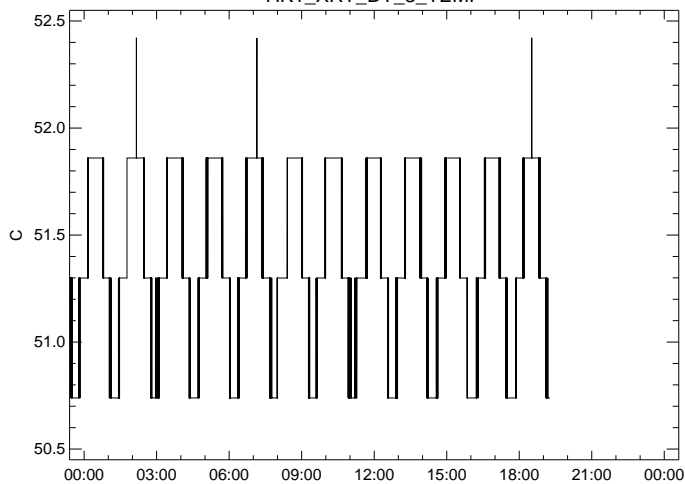
XRT_TEMP_DL Ver 2008/02/28
20180925
HK1_XRT_DT_1_TEMP



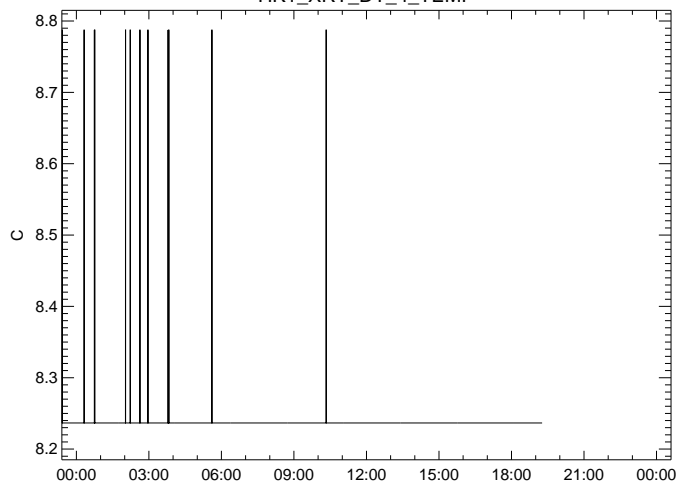
XRT_TEMP_DL Ver 2008/02/28
20180925
HK1_XRT_DT_2_TEMP



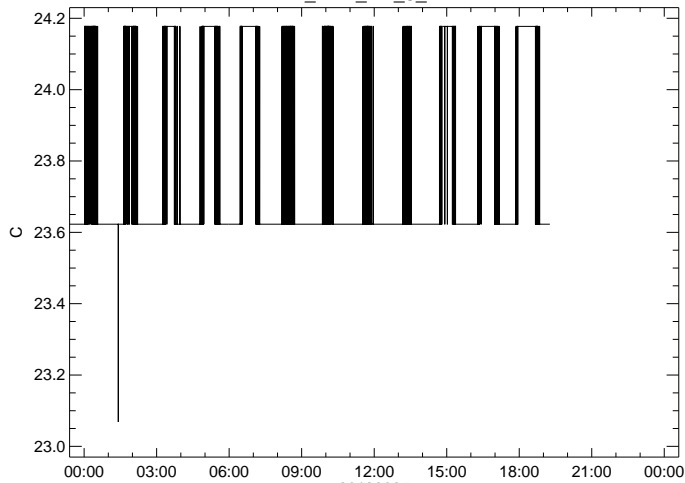
XRT_TEMP_DL Ver 2008/02/28
20180925
HK1_XRT_DT_3_TEMP



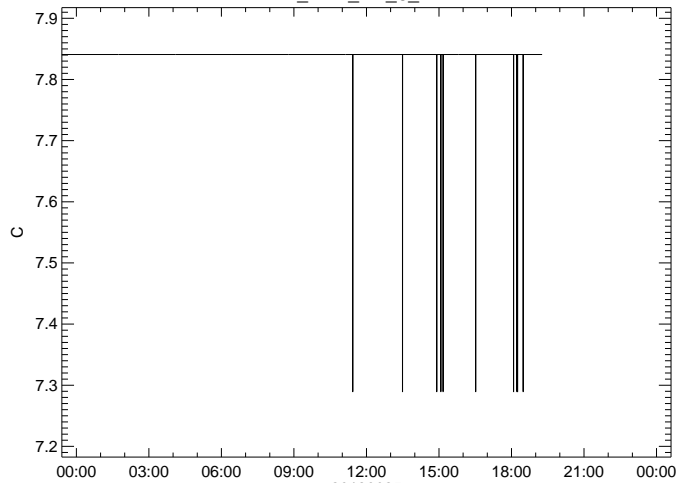
XRT_TEMP_DL Ver 2008/02/28
20180925
HK1_XRT_DT_4_TEMP



XRT_TEMP_DL Ver 2008/02/28
20180925
HK1_XRT_DT_5_TEMP



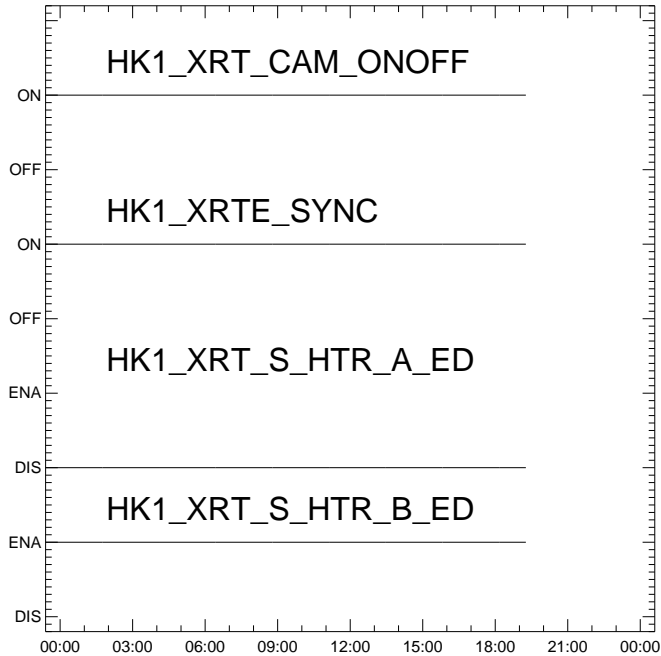
XRT_TEMP_DL Ver 2008/02/28
20180925
HK1_XRT_DT_6_TEMP



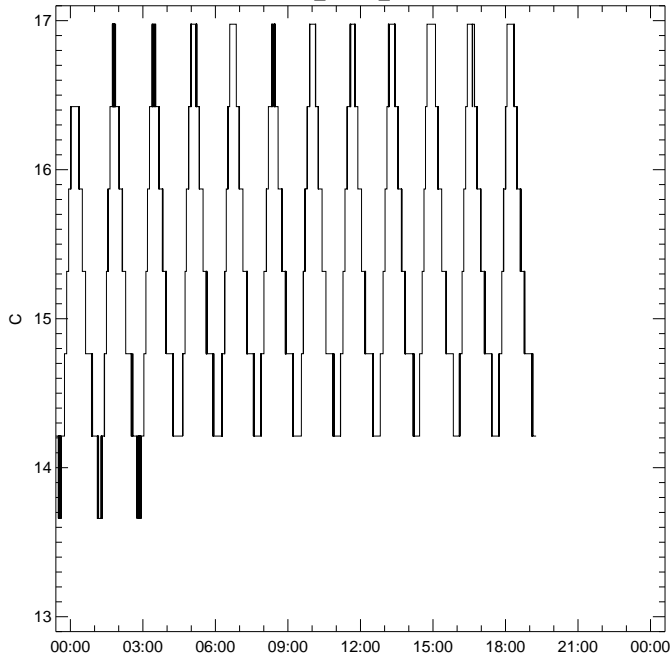
XRT_TEMP_DL Ver 2008/02/28

XRT_TEMP_DL Ver 2008/02/28

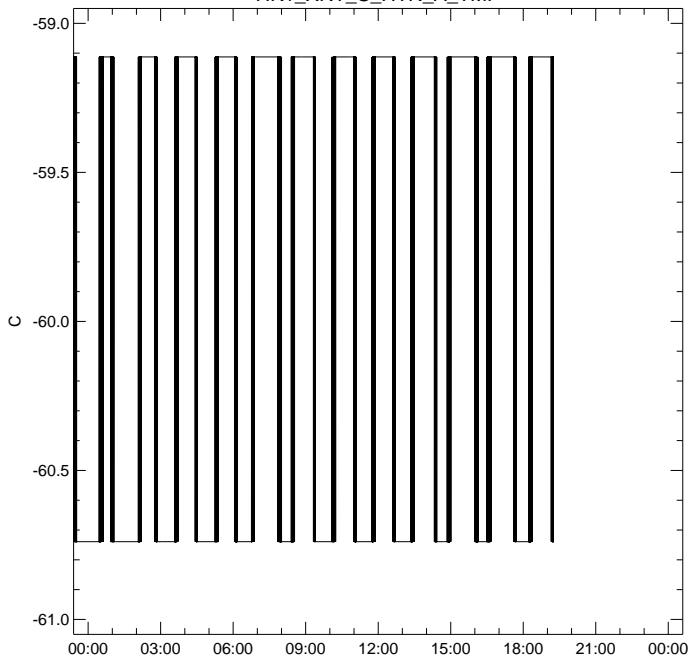
ON/OFF Statuses



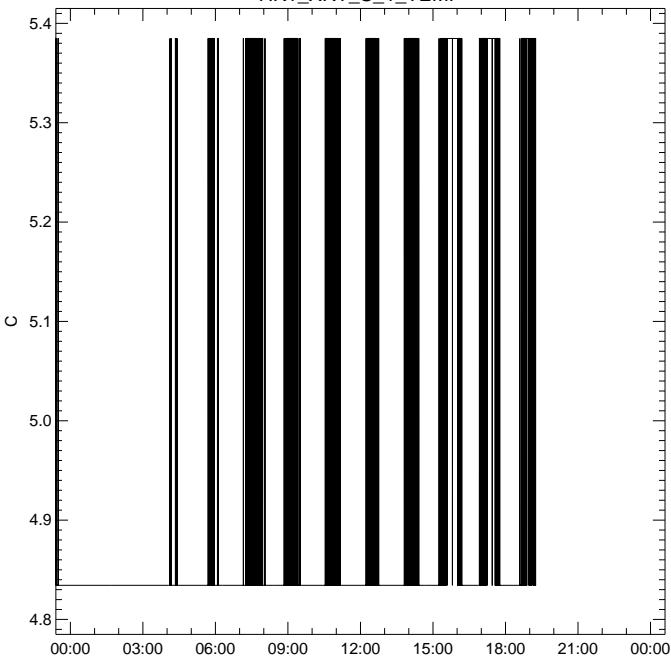
HK1_XRTE_TEMP



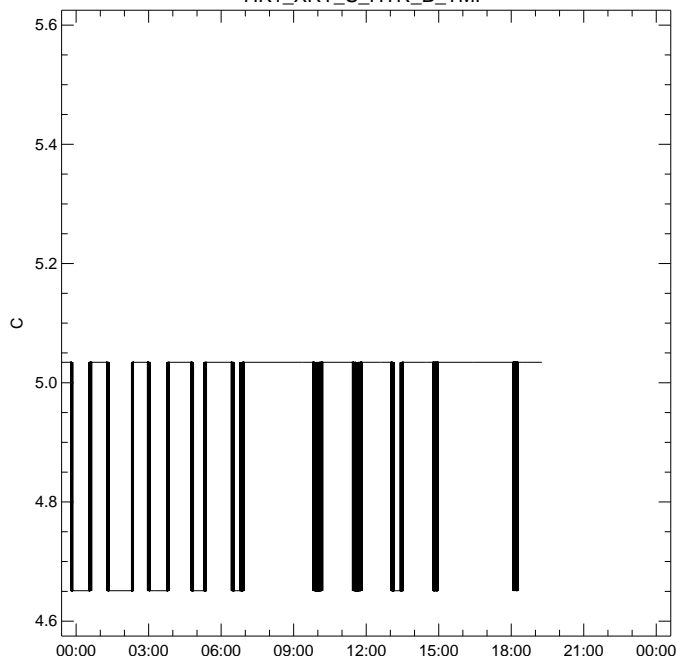
20180925
XRT_TEMP_DL Ver 2008/02/28
HK1_XRT_S_HTR_A_TMP



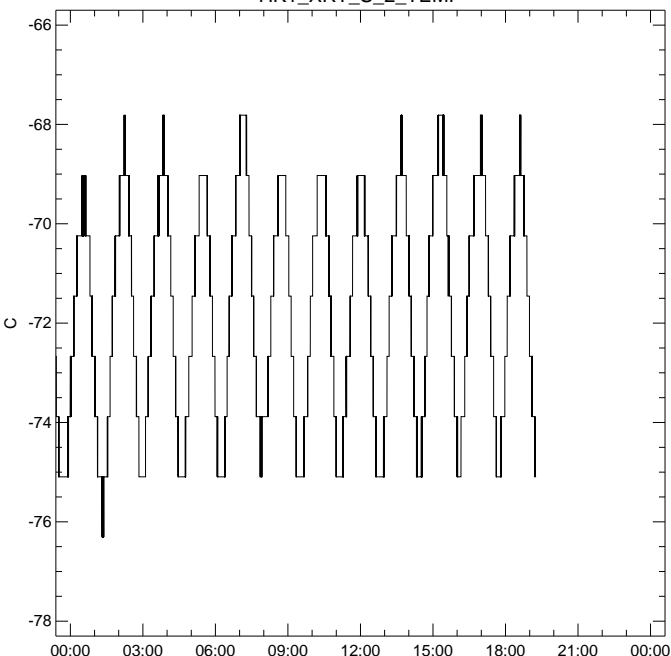
20180925
XRT_TEMP_DL Ver 2008/02/28
HK1_XRT_S_1_TEMP



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XRT_TEMP_DL Ver 2008/02/28
HK1_XRT_S_HTR_B_TMP

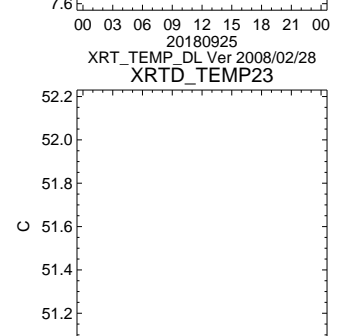
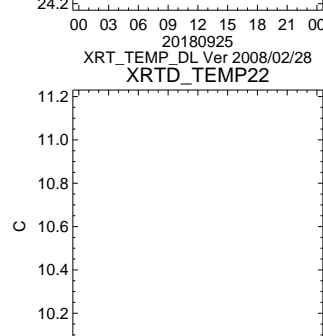
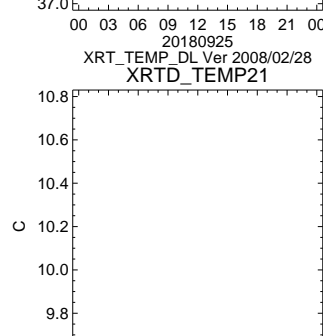
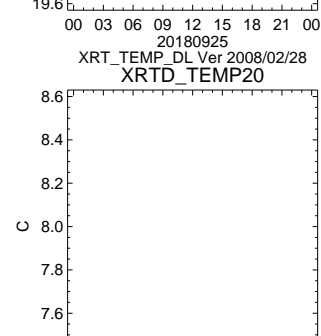
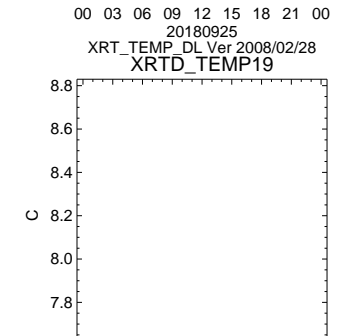
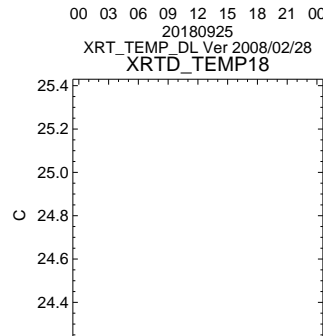
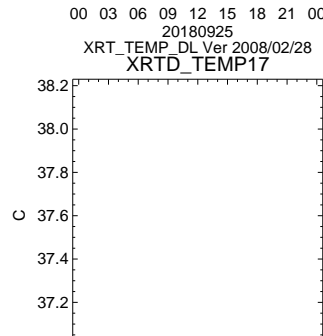
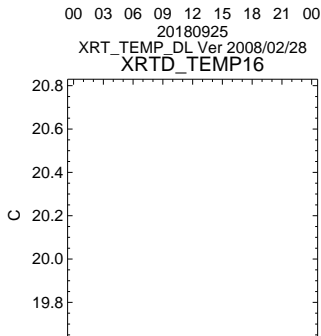
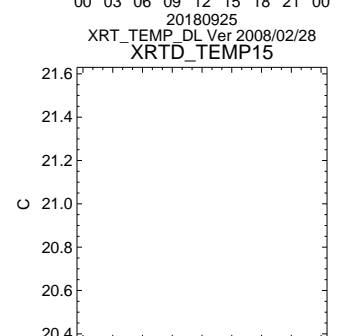
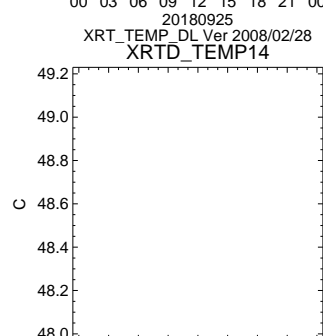
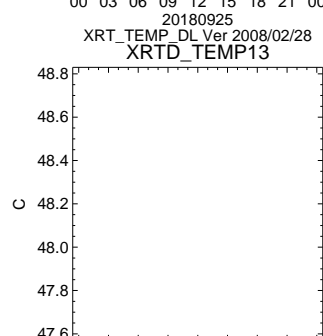
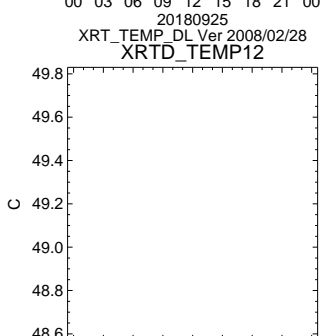
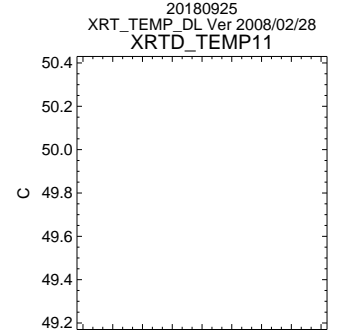
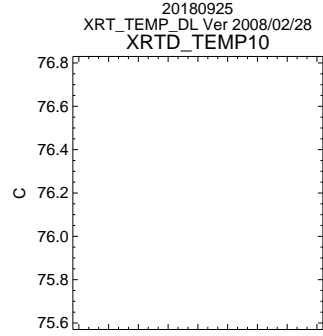
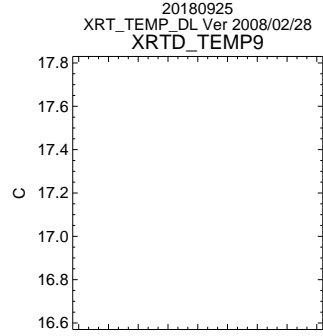
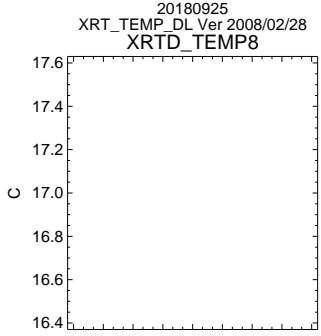
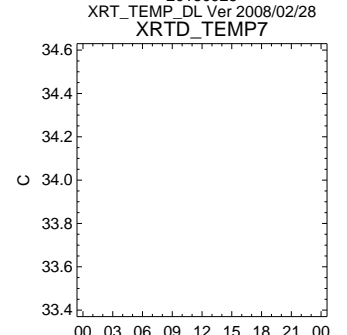
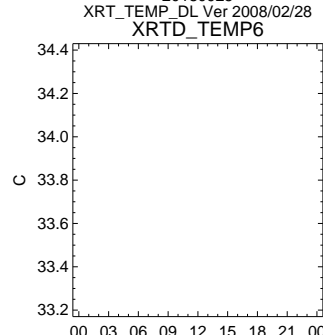
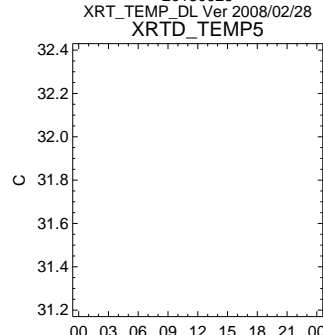
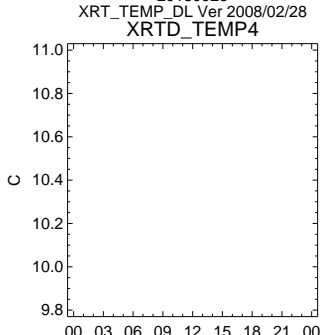
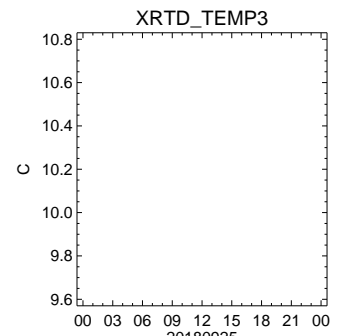
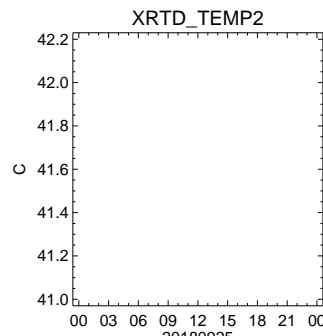
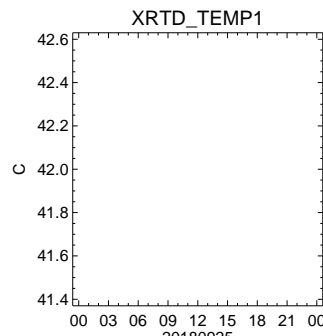
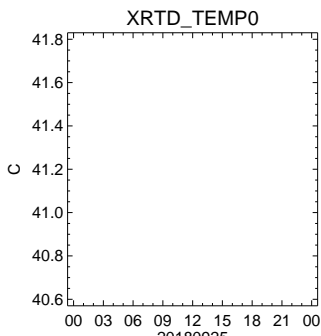


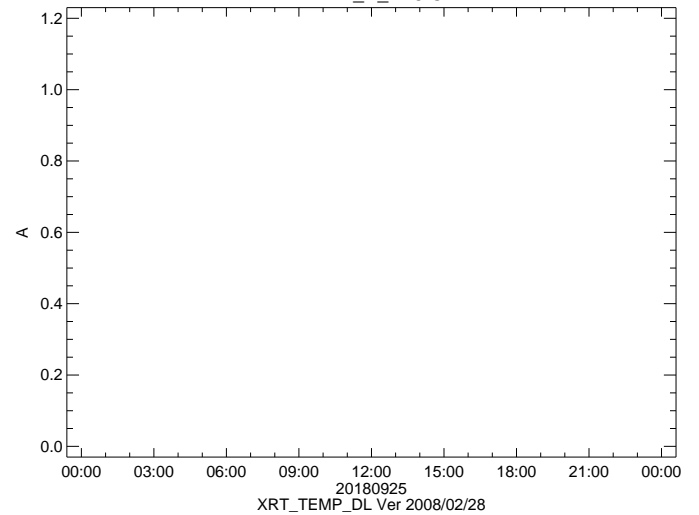
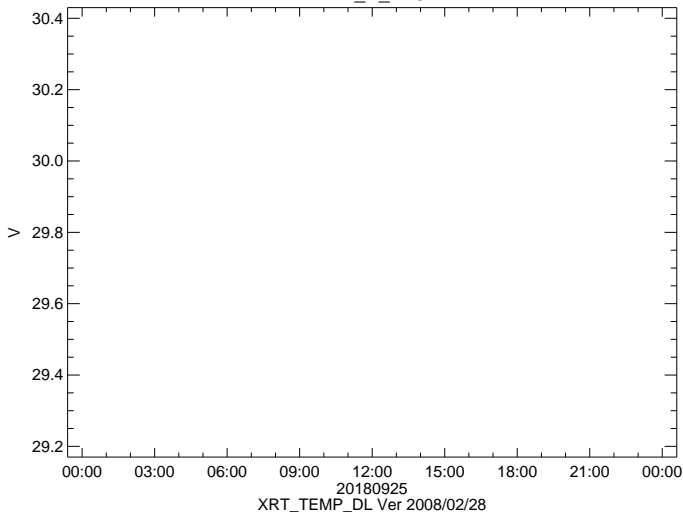
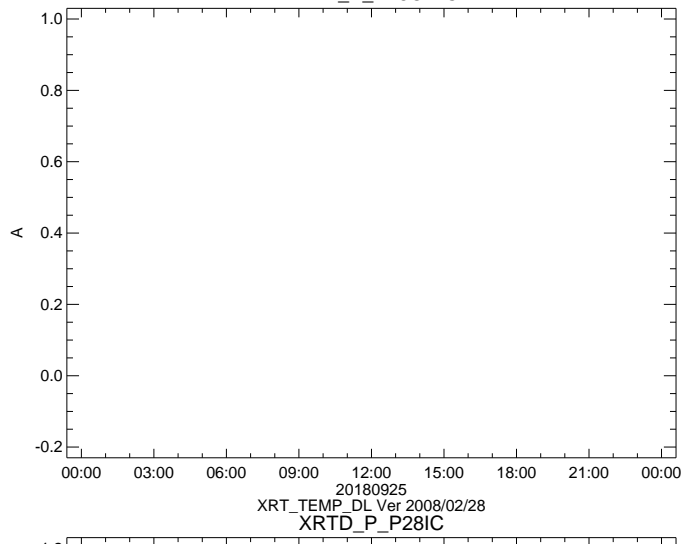
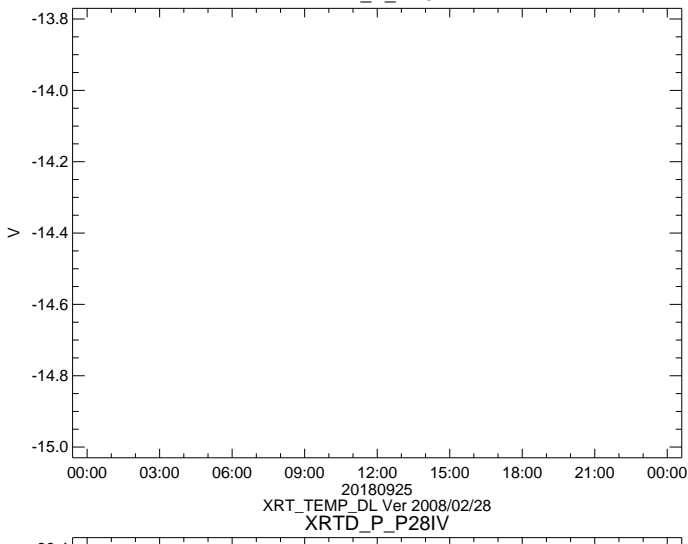
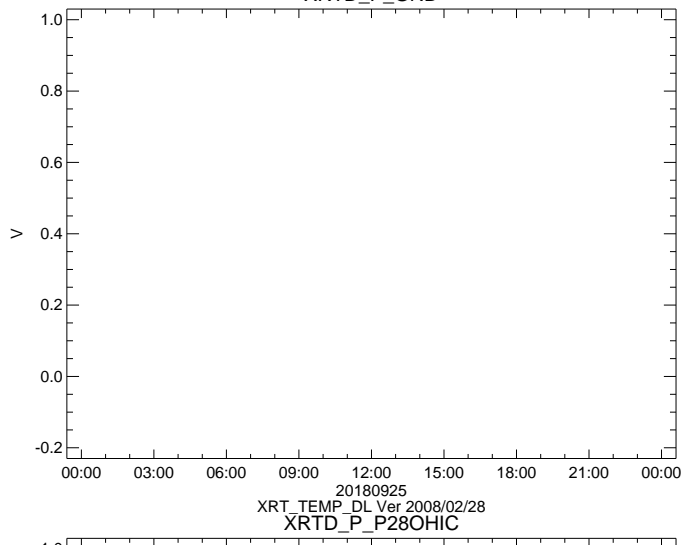
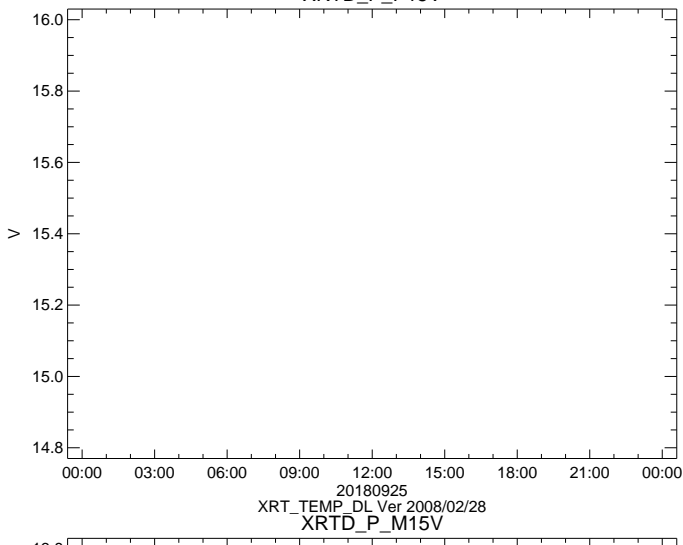
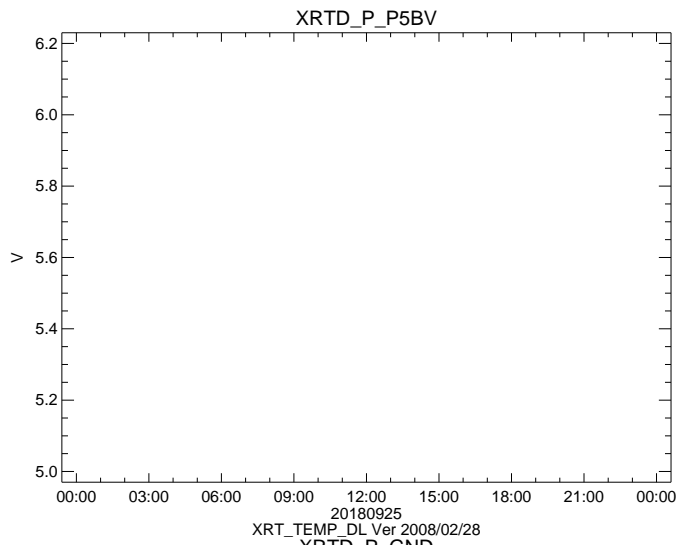
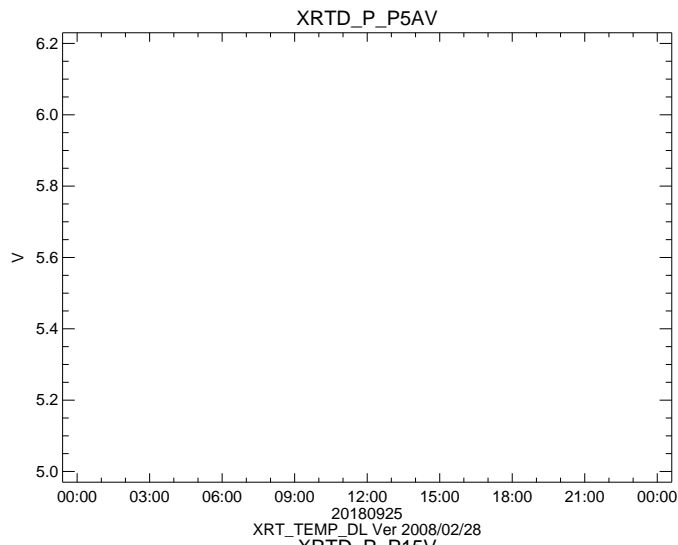
20180925
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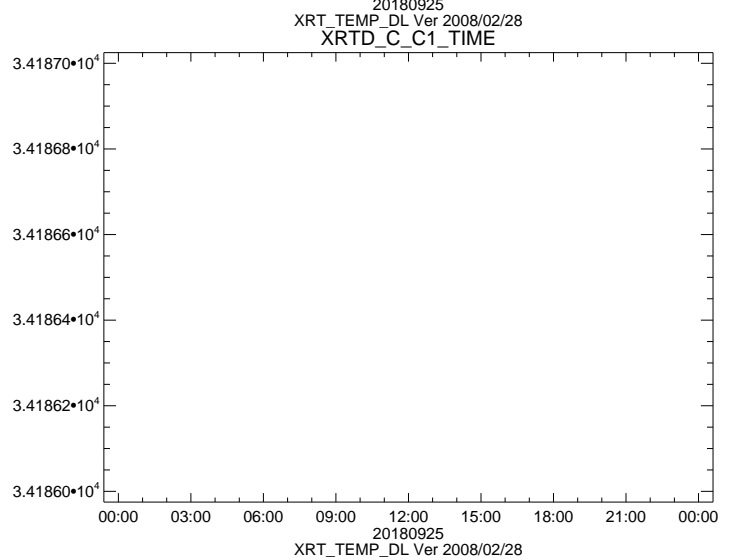
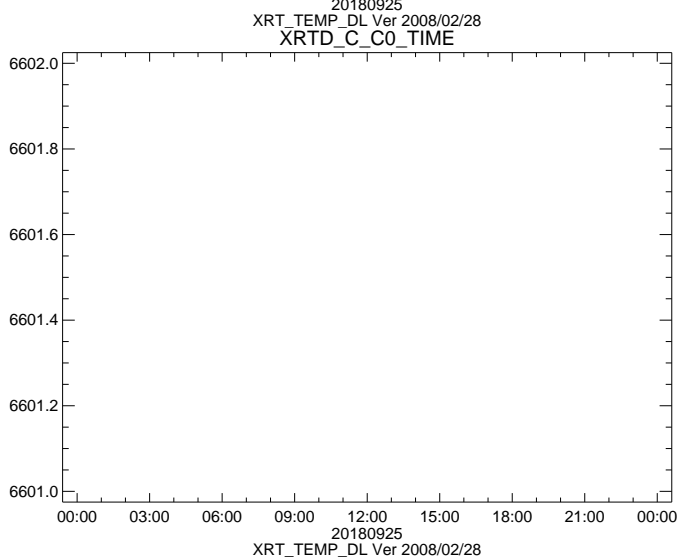
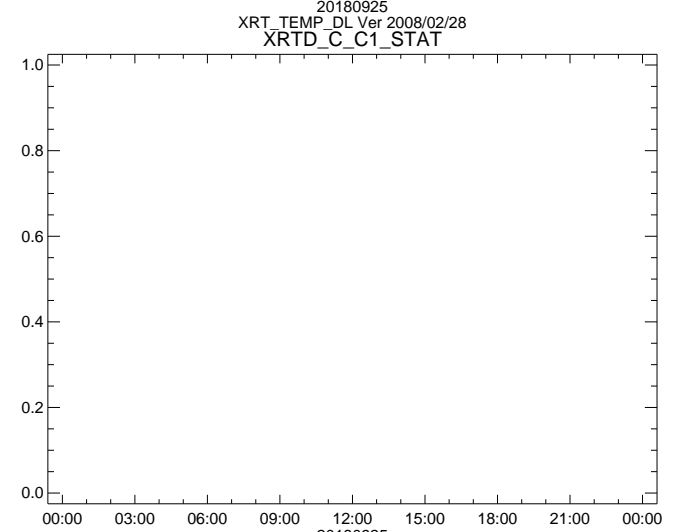
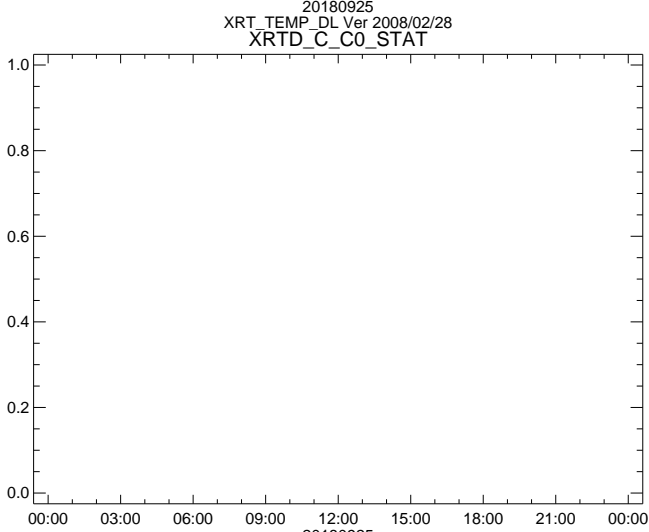
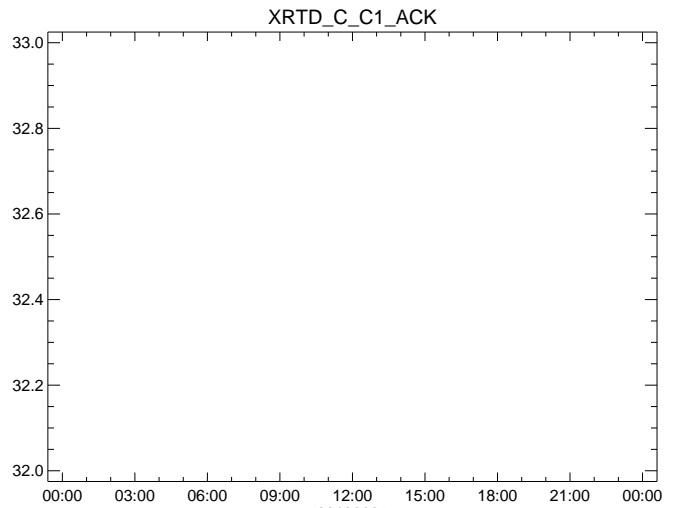
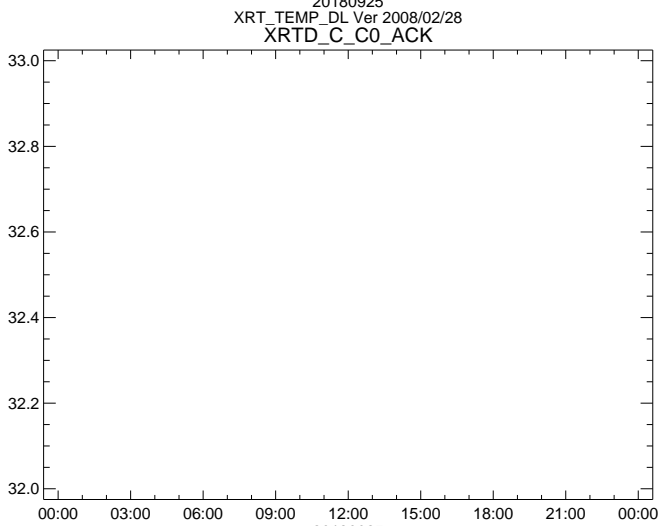
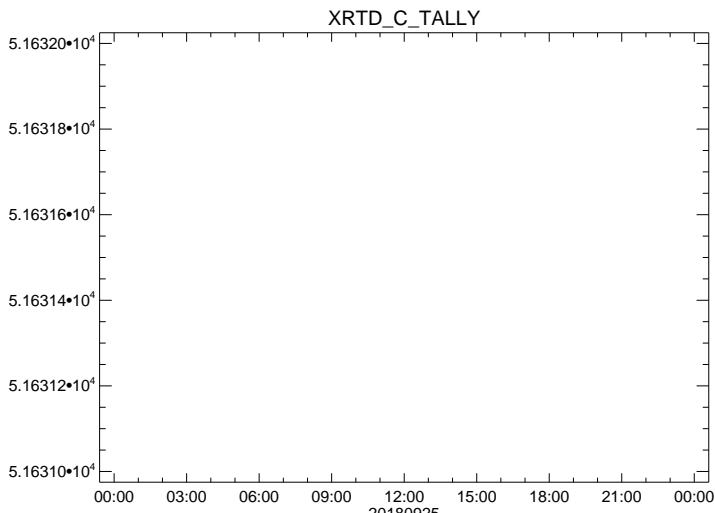


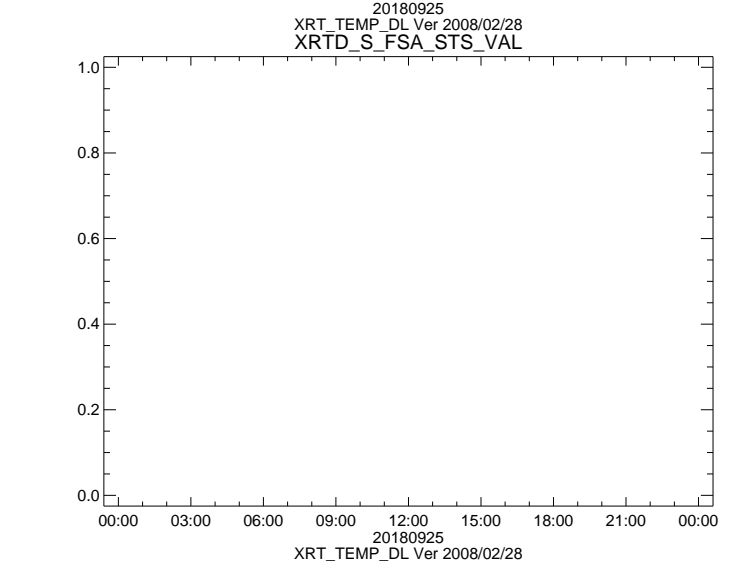
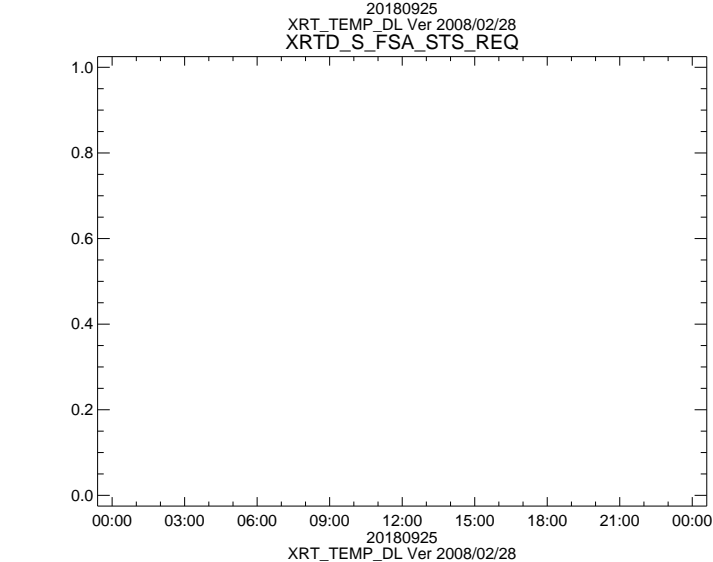
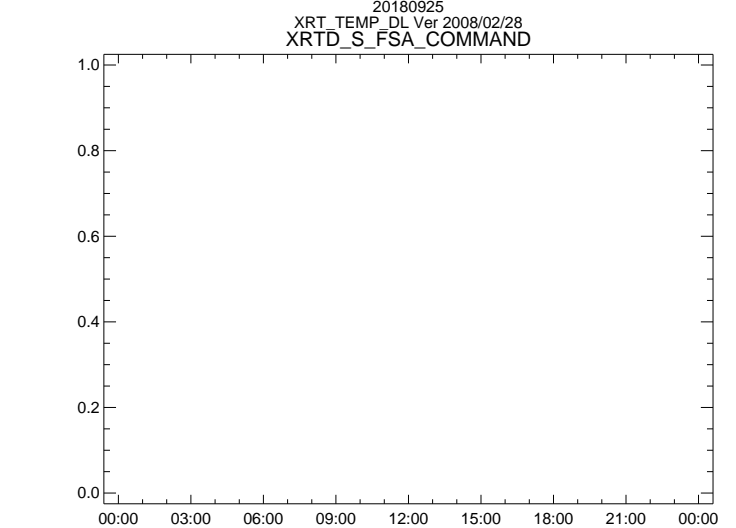
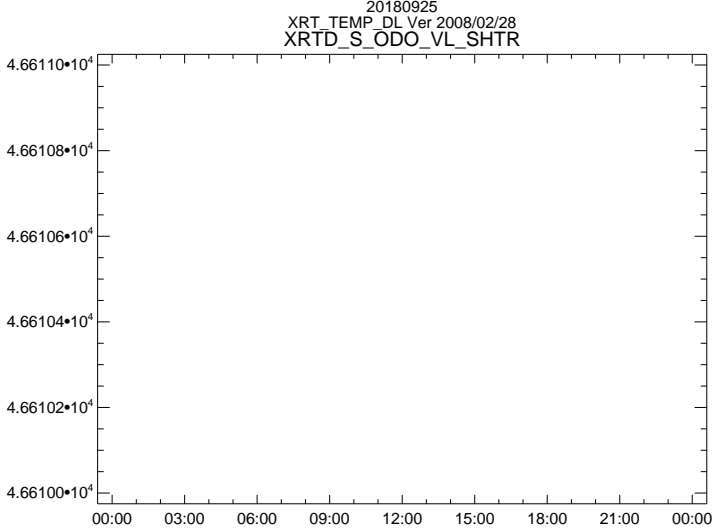
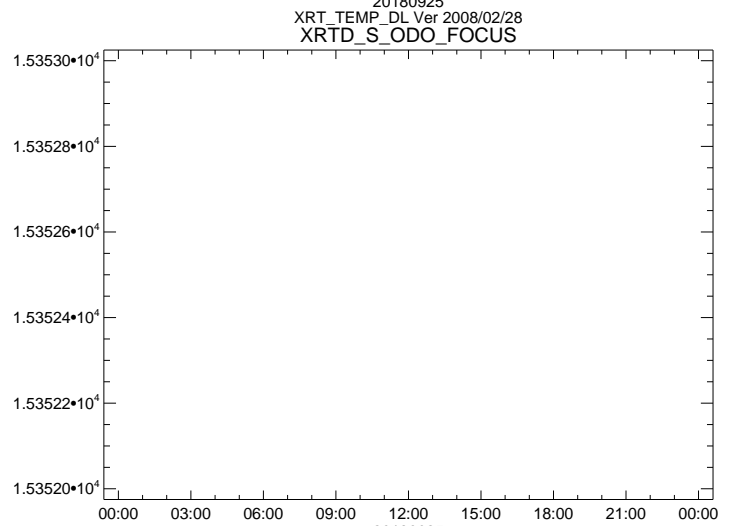
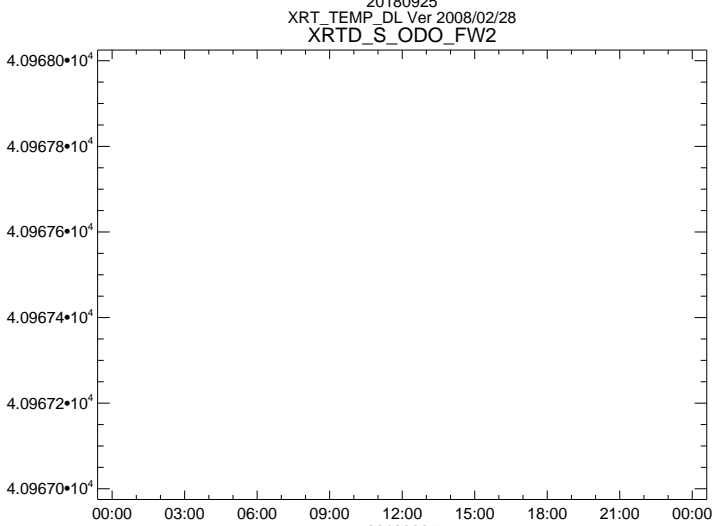
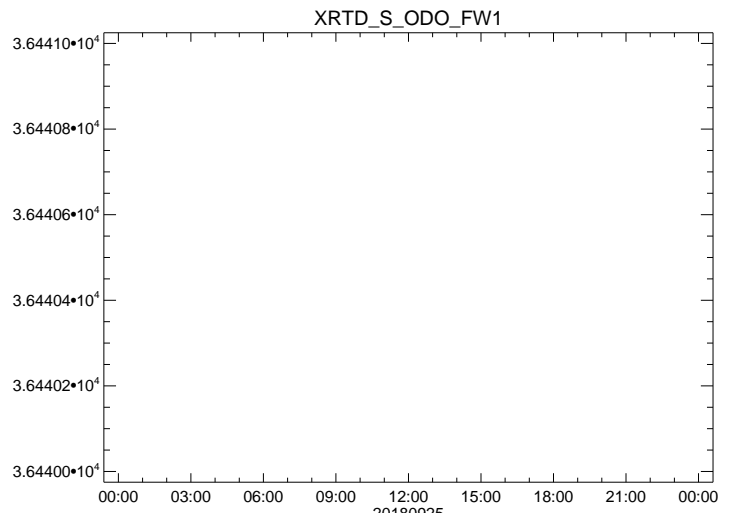
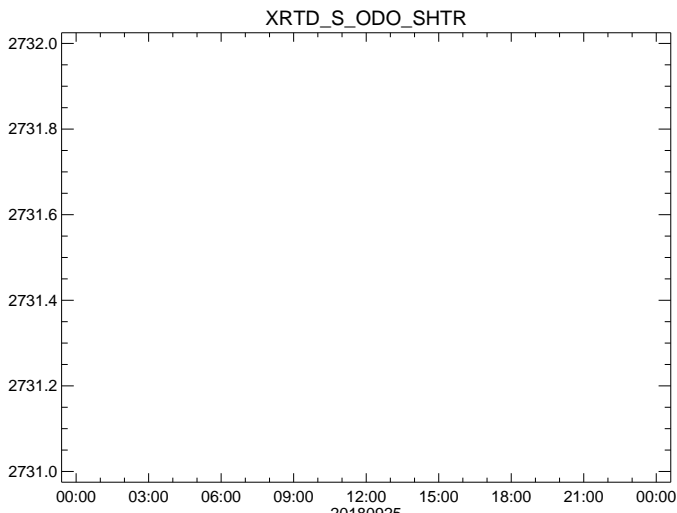
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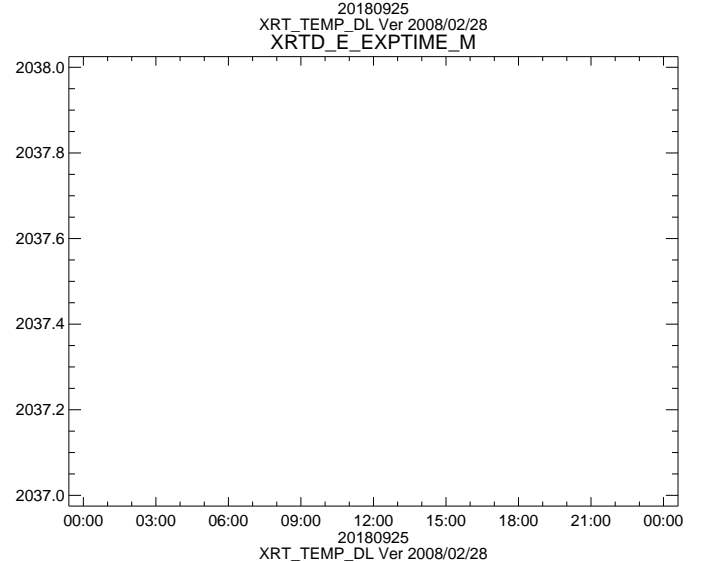
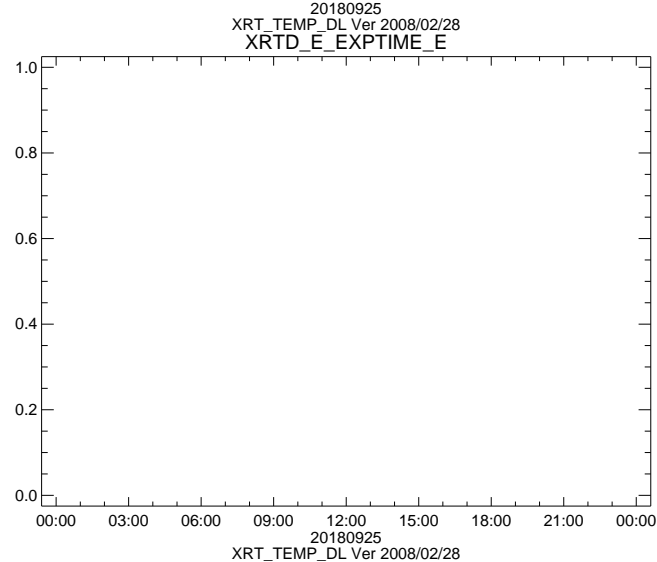
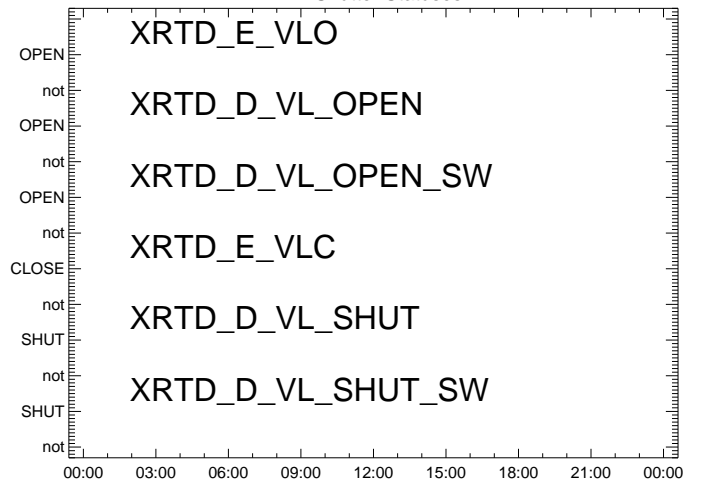
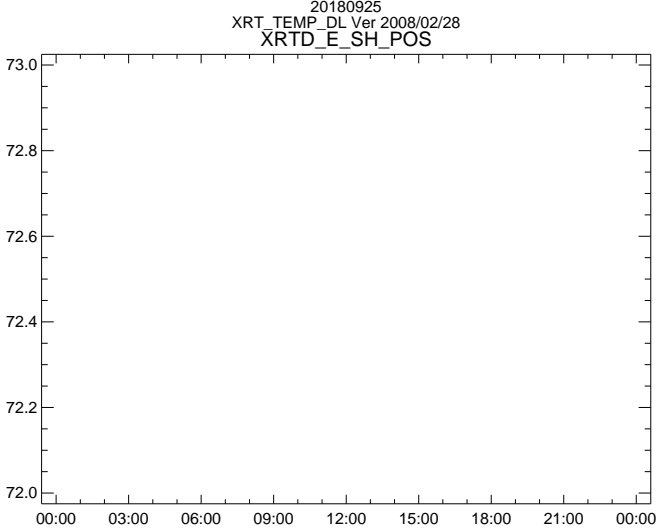
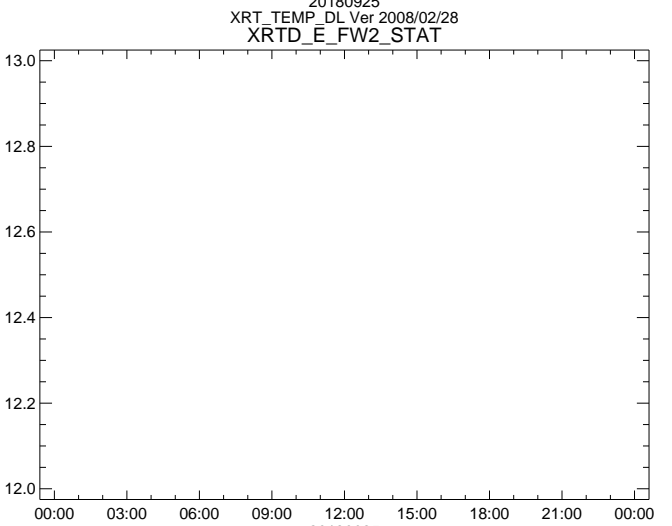
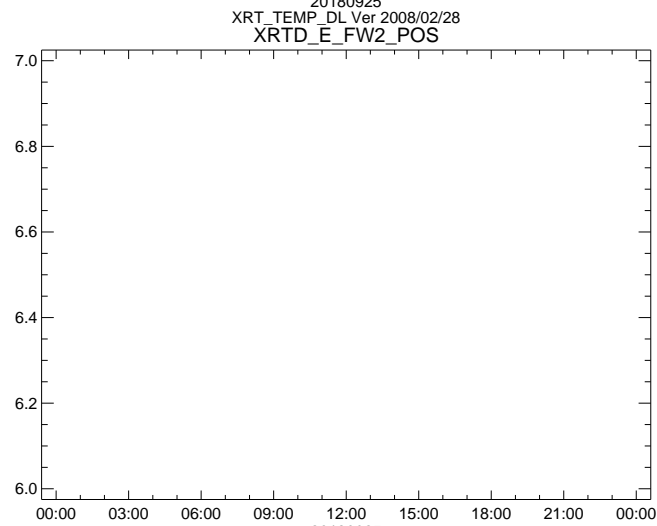
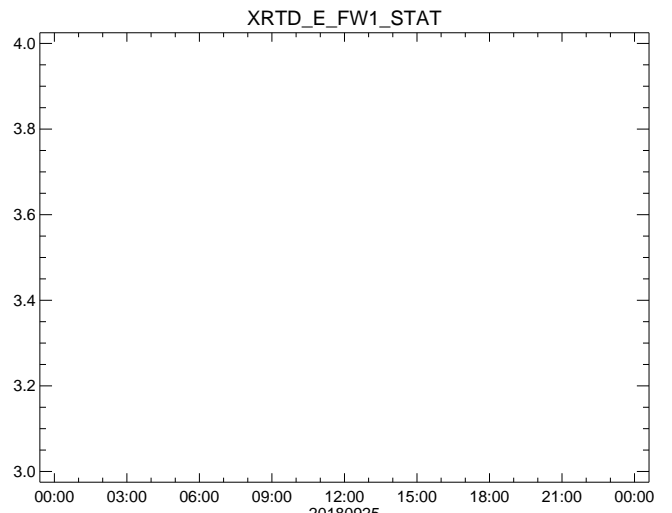
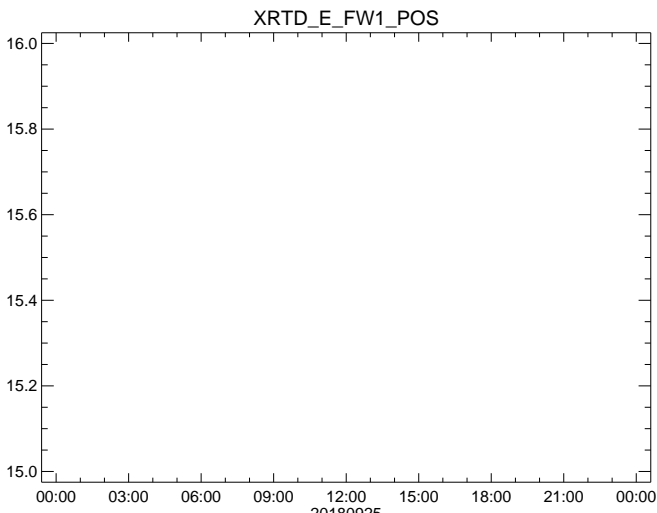
20180925
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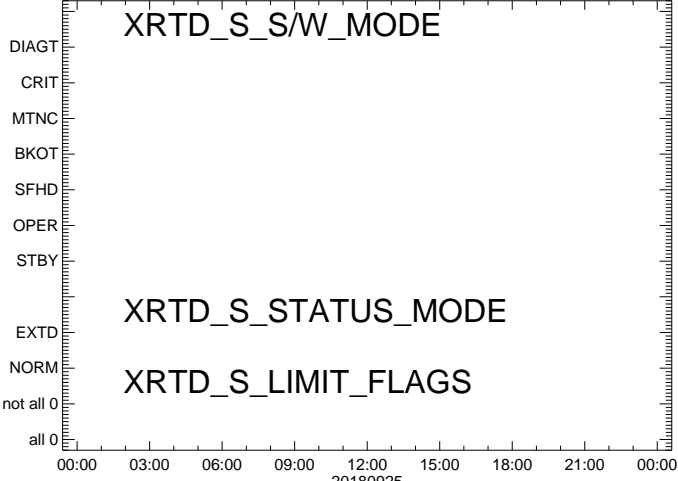




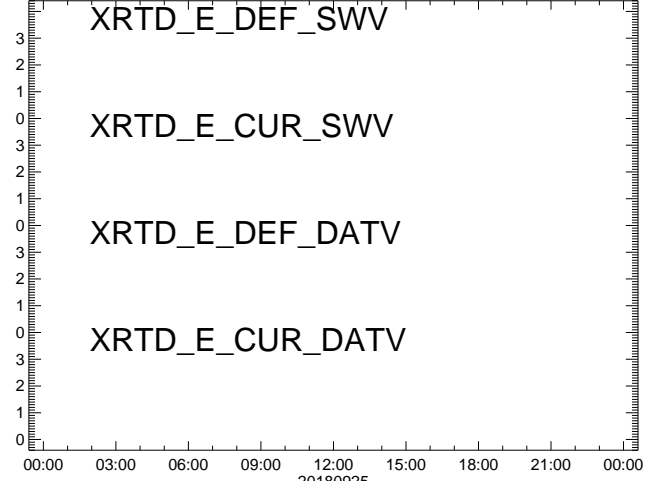




Software Statuses

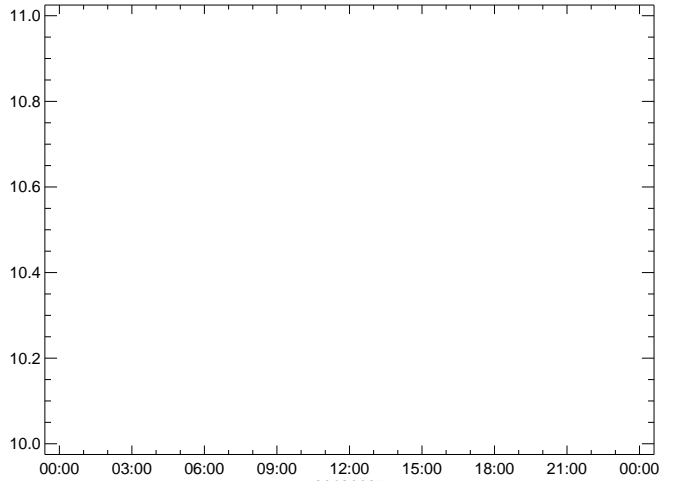
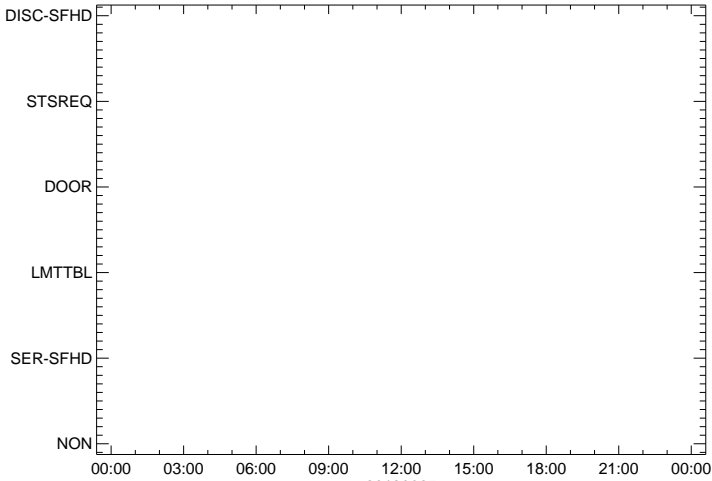


SW/DATA Version



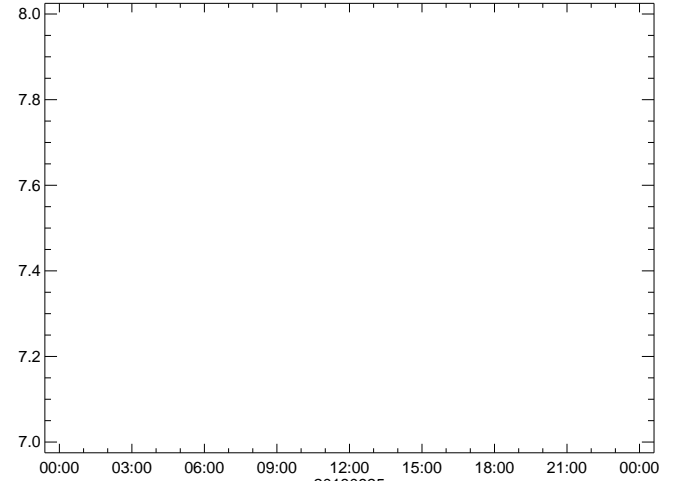
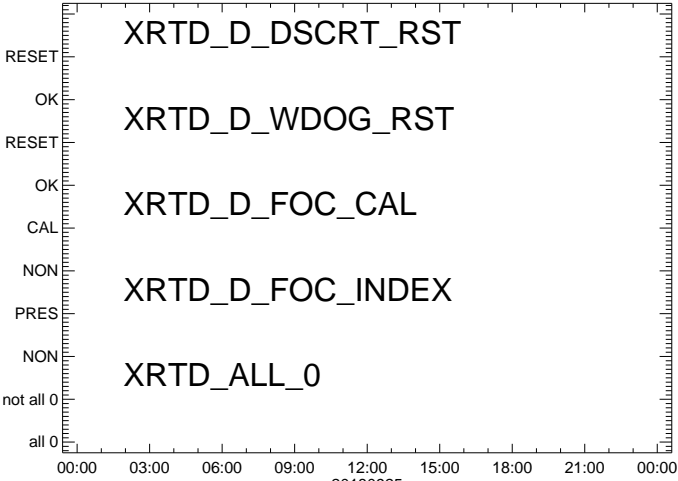
20180925
XRT_TEMP_DL Ver 2008/02/28
XRTD_S_SFHD_CAUSE

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XRT_TEMP_DL Ver 2008/02/28
XRTD_E_DSCRT_RST



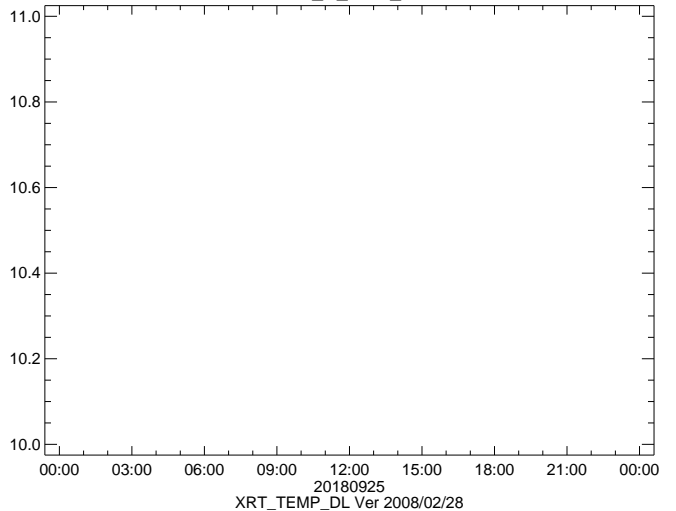
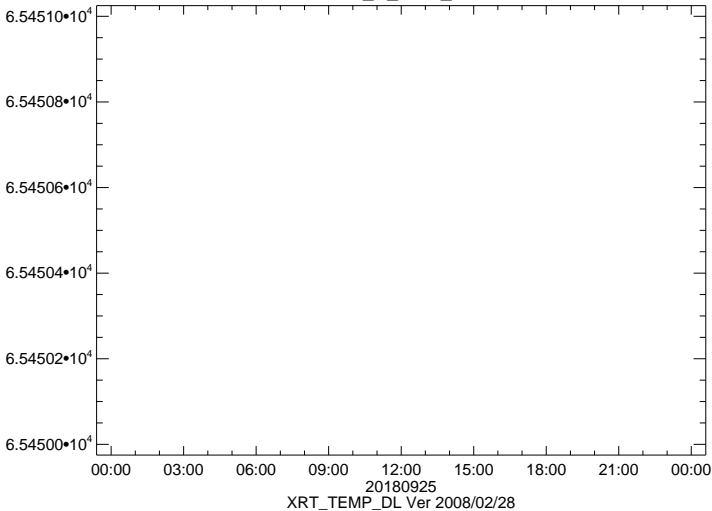
20180925
XRT_TEMP_DL Ver 2008/02/28
Digital Statuses

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XRT_TEMP_DL Ver 2008/02/28
XRTD_E_WDOG_RST



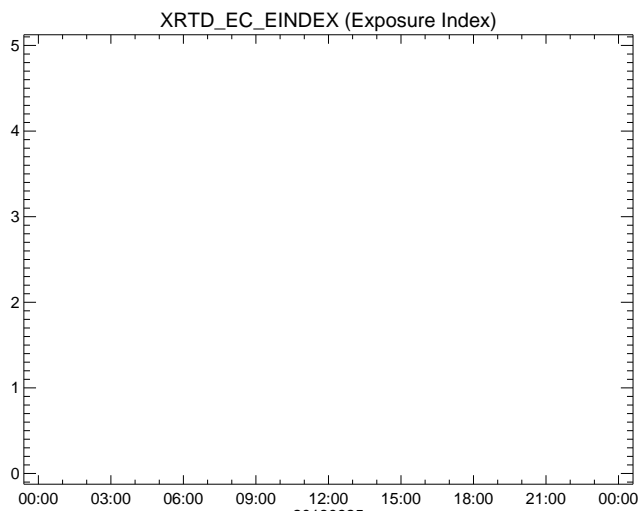
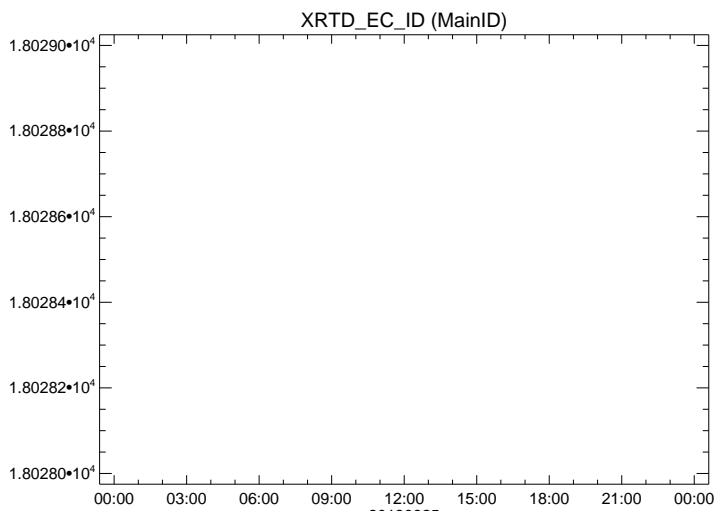
20180925
XRT_TEMP_DL Ver 2008/02/28
XRTD_E_FOC_POS

20180925
XRT_TEMP_DL Ver 2008/02/28
XRTD_E_OTH_RST



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XRT_TEMP_DL Ver 2008/02/28

20180925
XRT_TEMP_DL Ver 2008/02/28

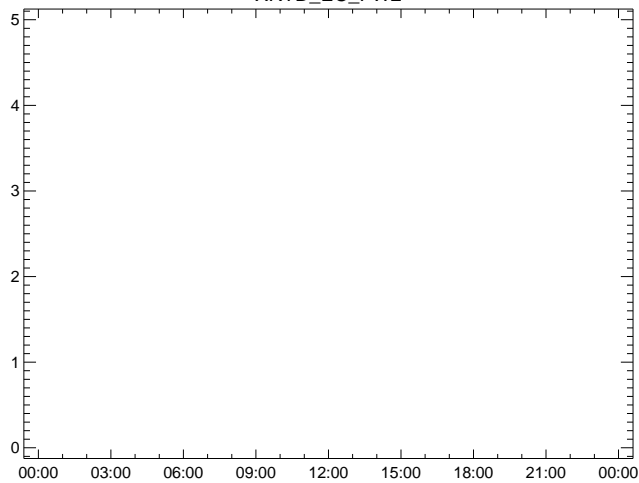
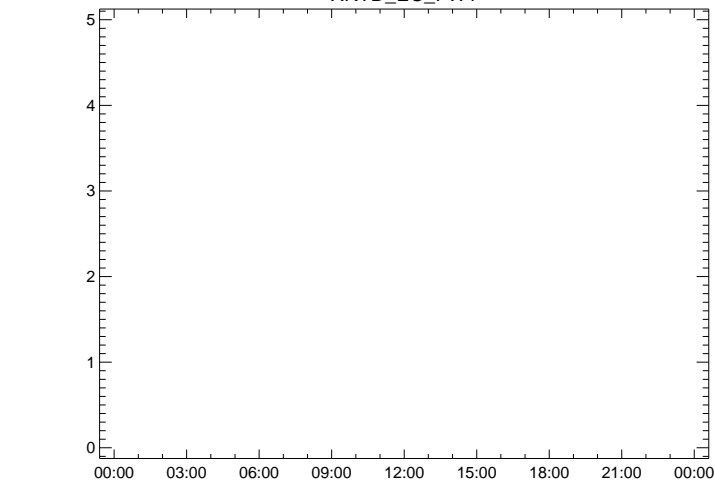


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XRTD_EC_FW1

XRTD_EC_FW2

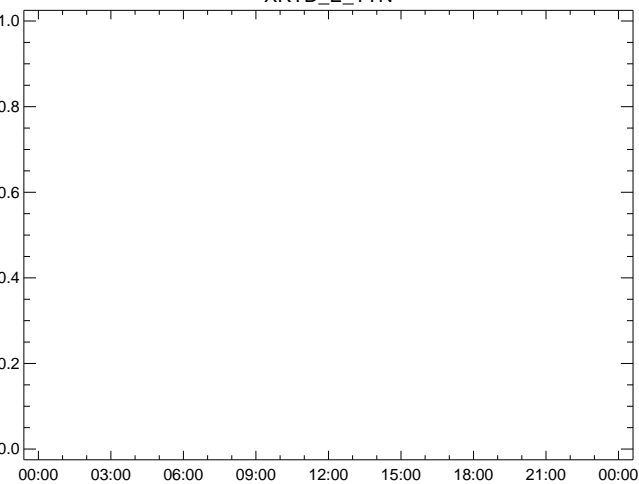
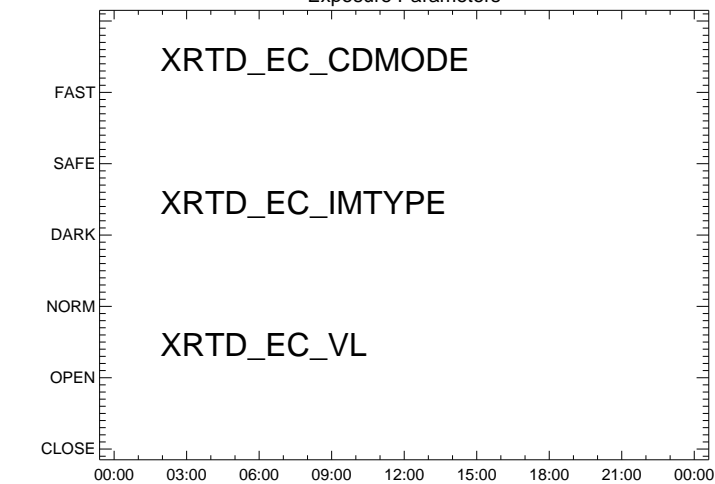


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XRT_TEMP_DL Ver 2008/02/28

Exposure Parameters

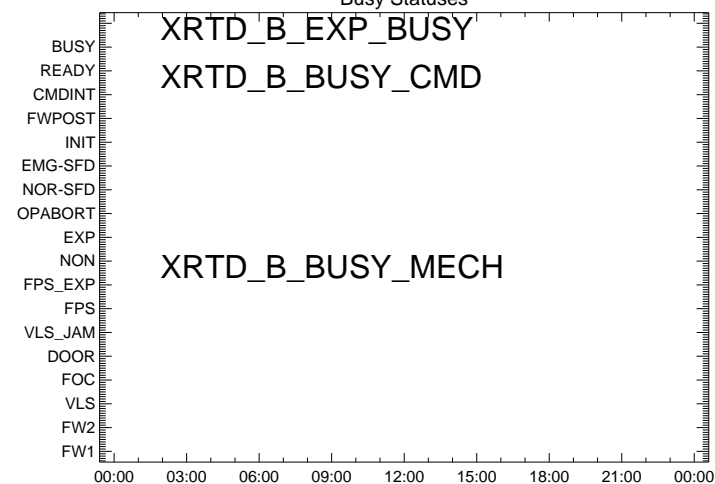
XRTD_E_TTN



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XRT_TEMP_DL Ver 2008/02/28

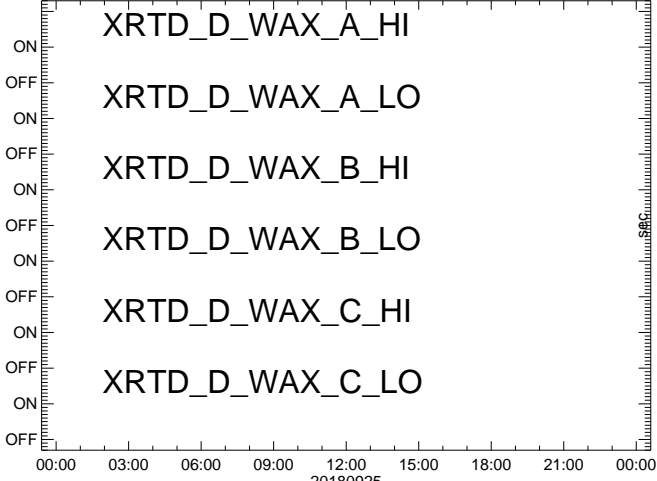
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Busy Statuses

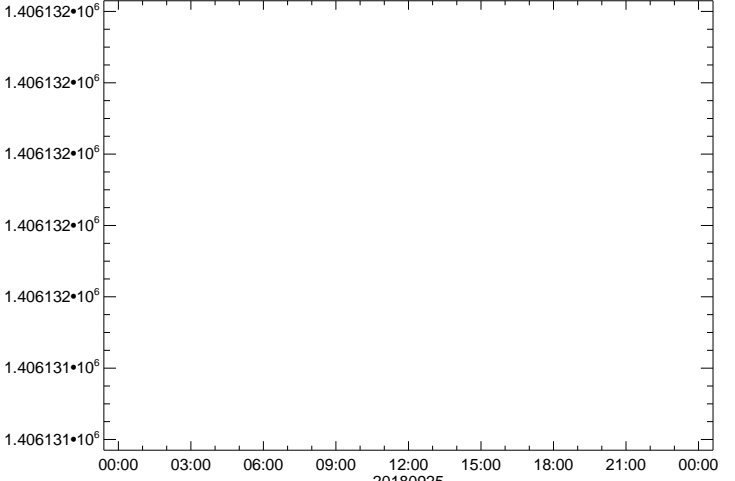


20180925
XRT_TEMP_DL Ver 2008/02/28

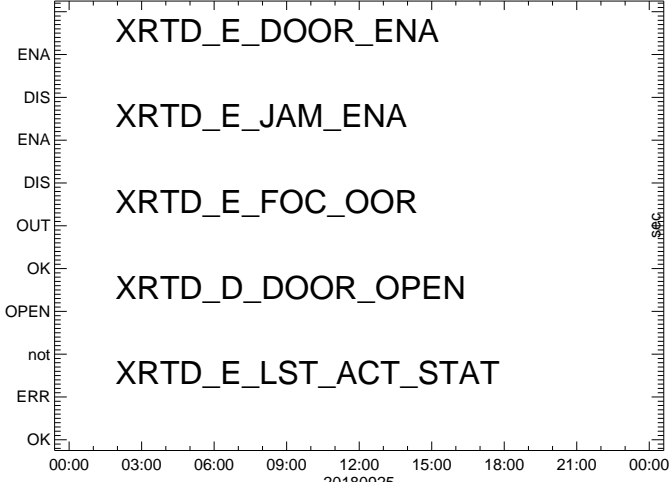
WAX Actuator Statuses



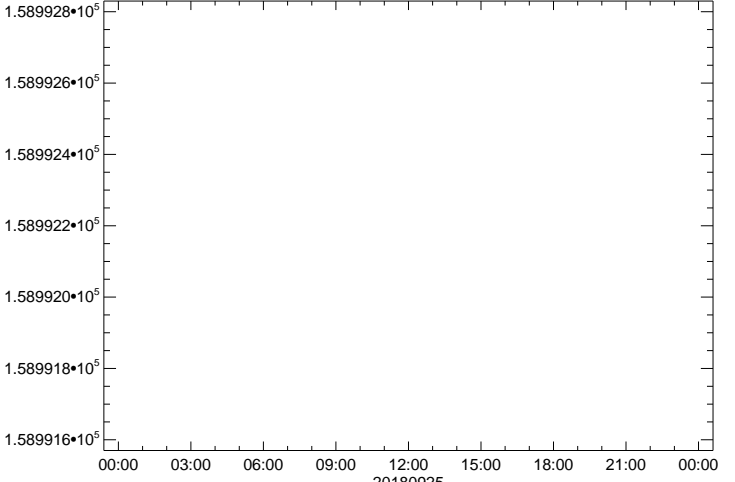
XRTD_T_SC_CLOCK



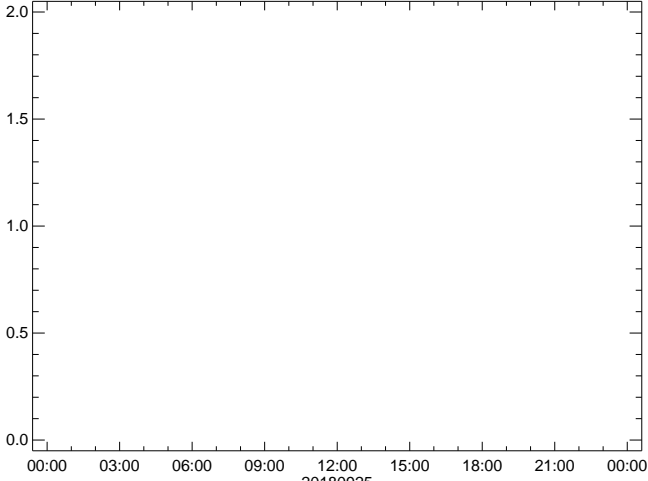
E Statuses



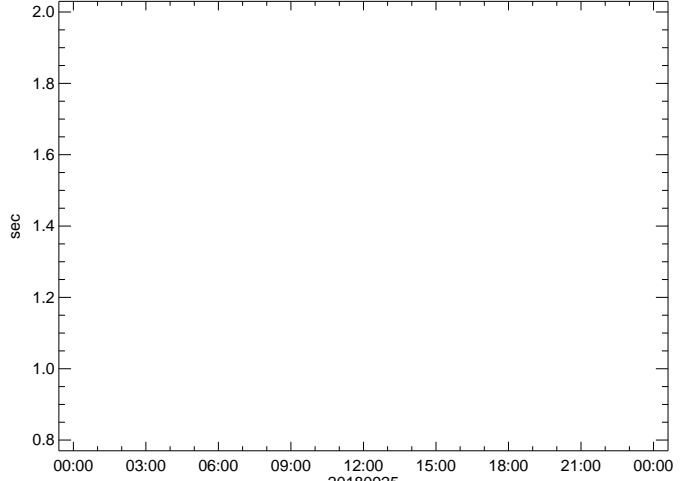
XRTD_T_LO_CLOCK



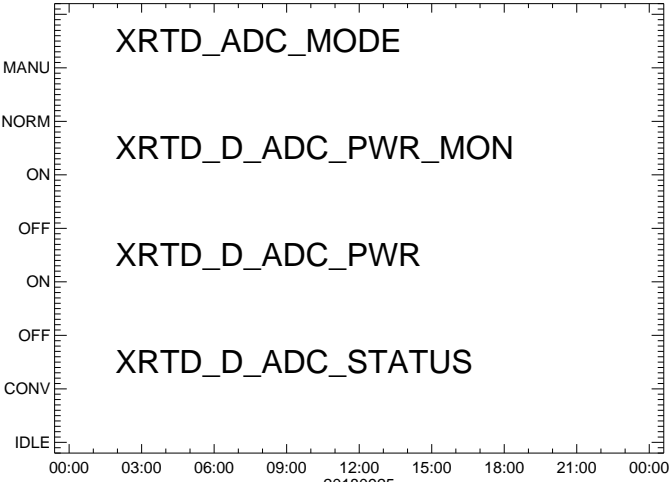
XRTD_TEMP_SENS_GROUP



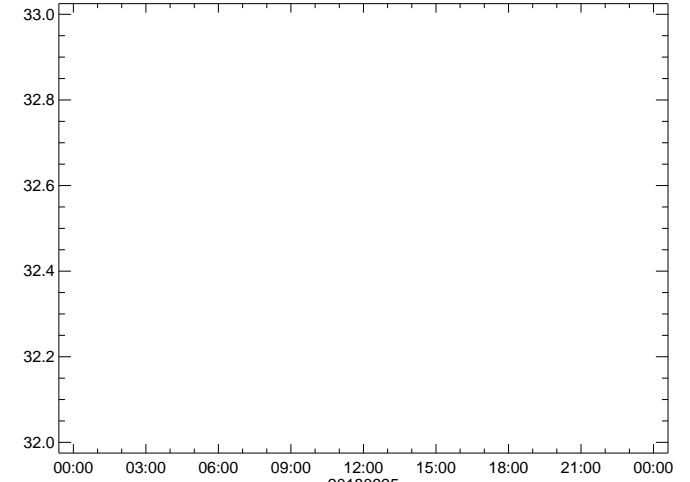
XRTD_T_LST_ACT

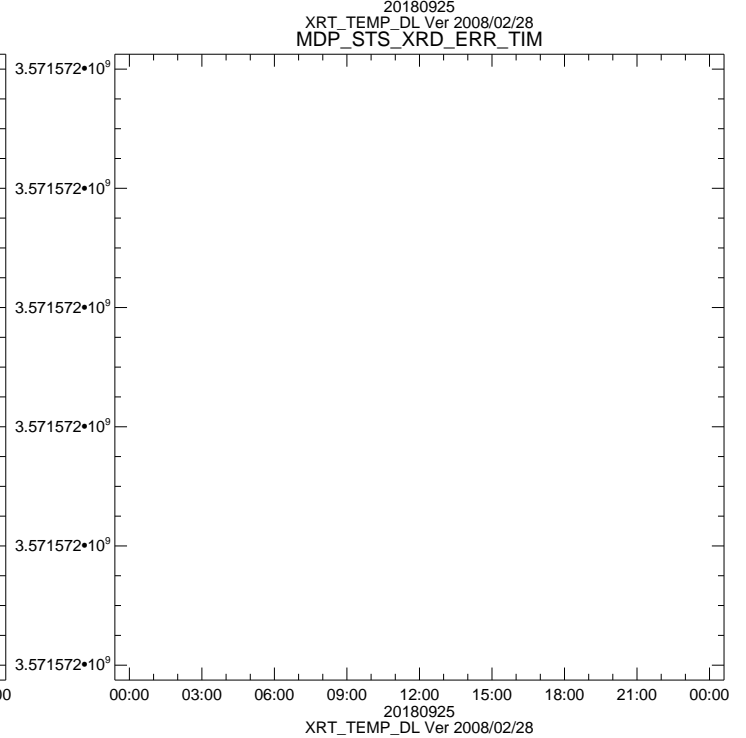
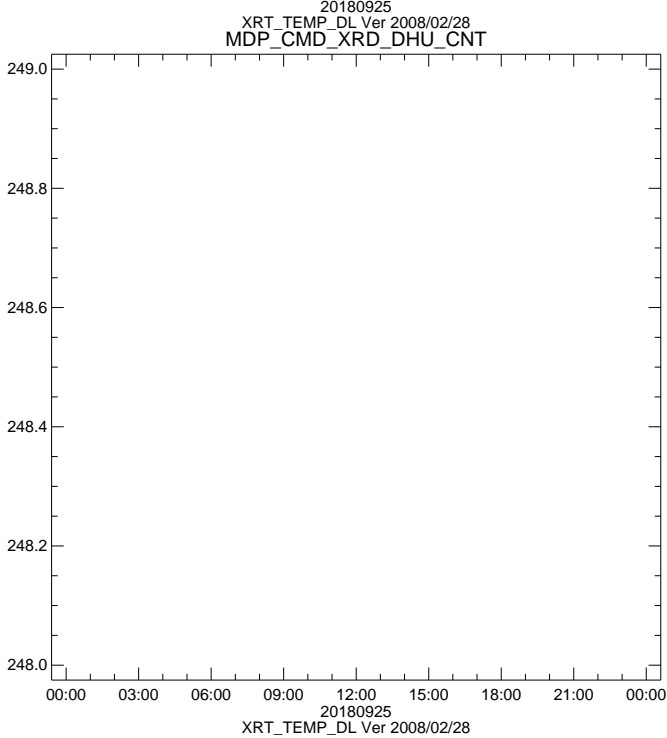
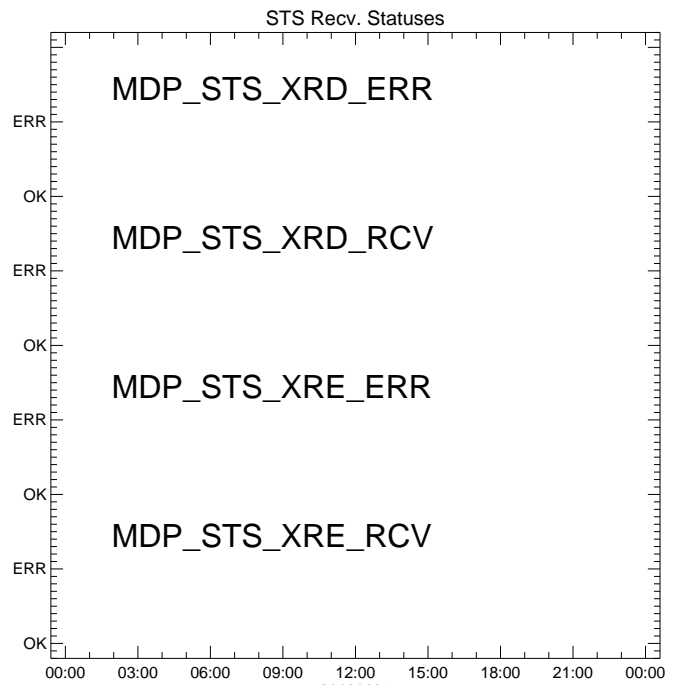
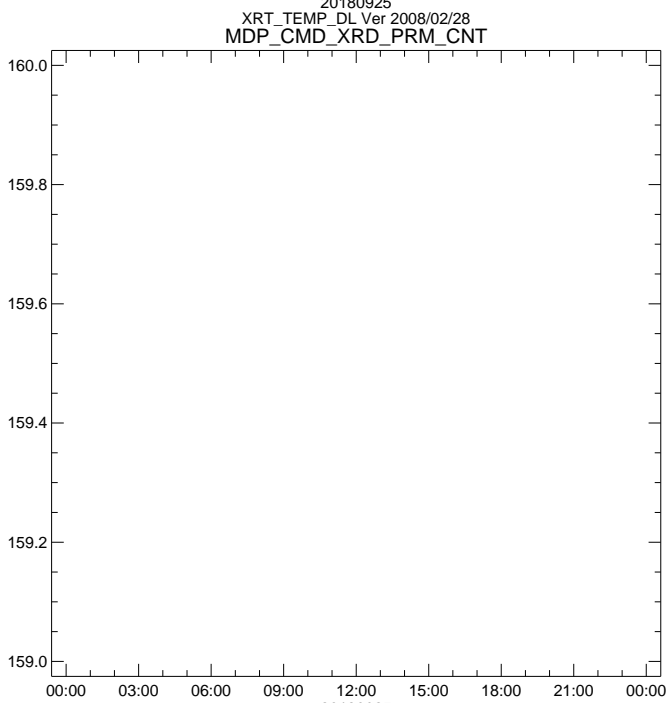
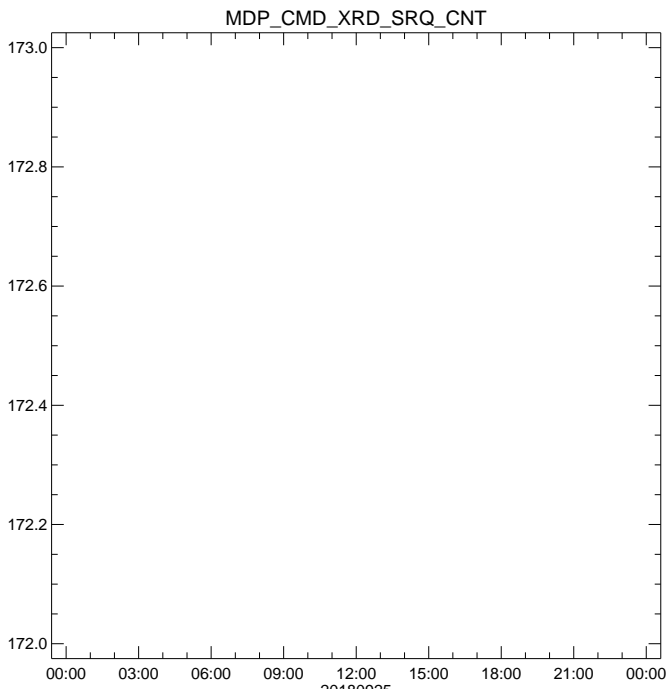


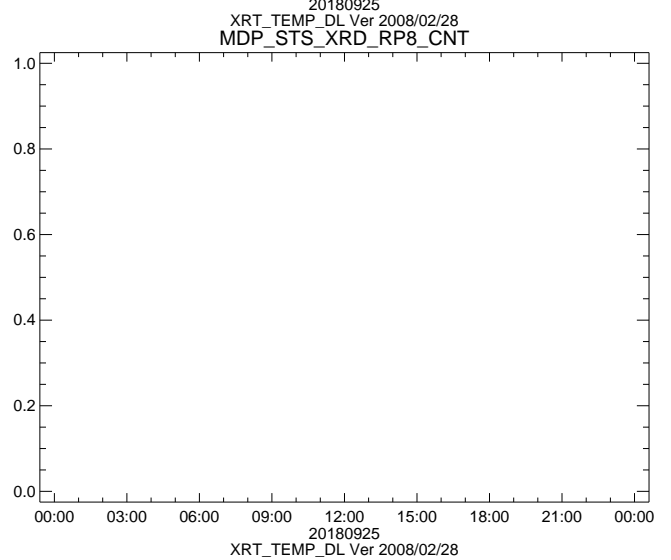
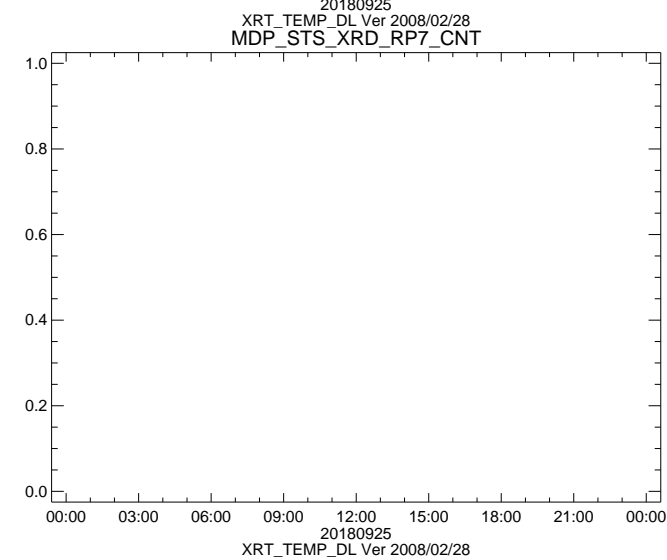
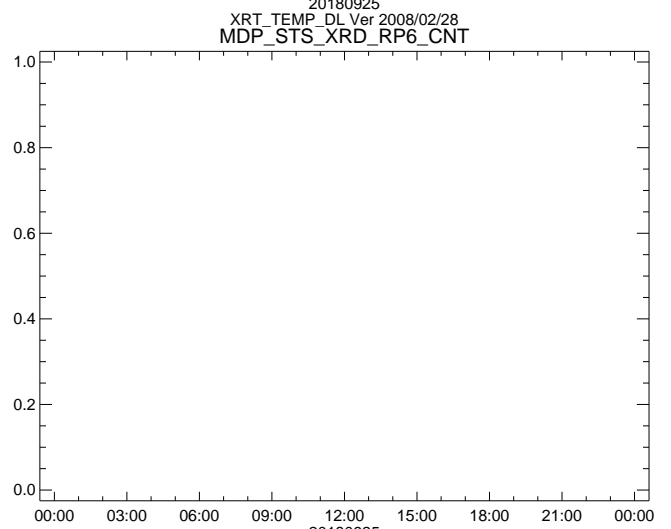
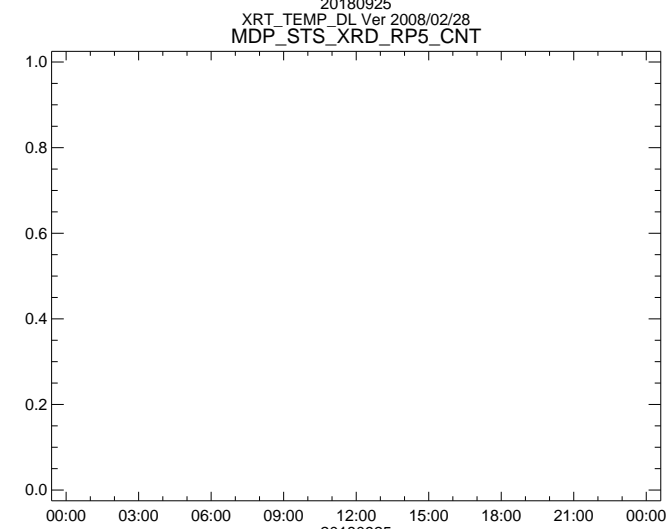
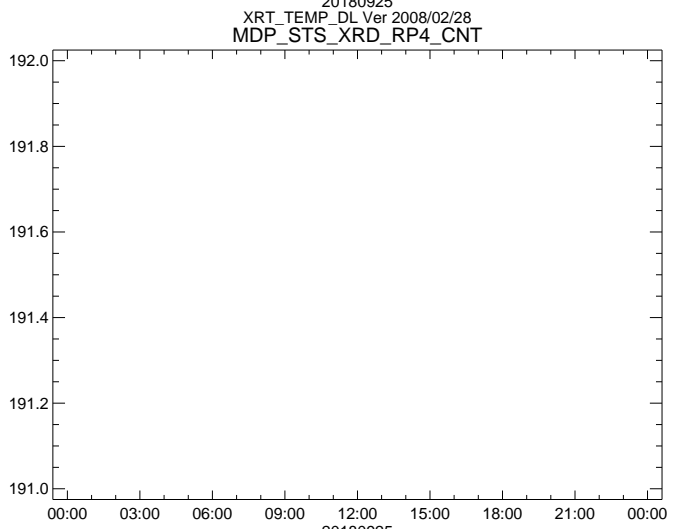
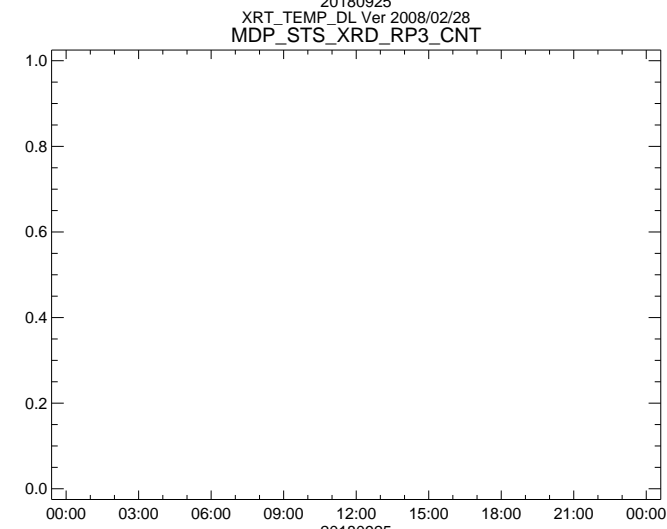
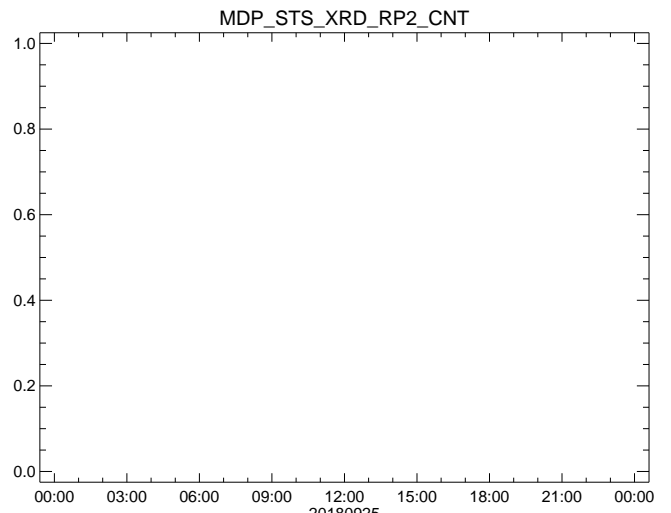
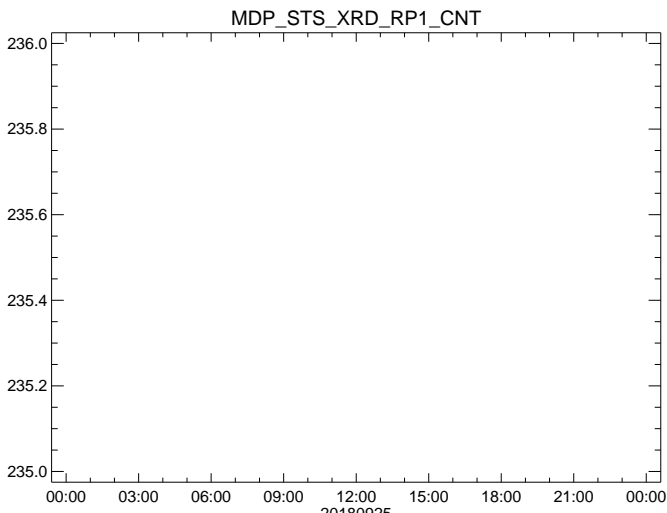
ADC Statuses

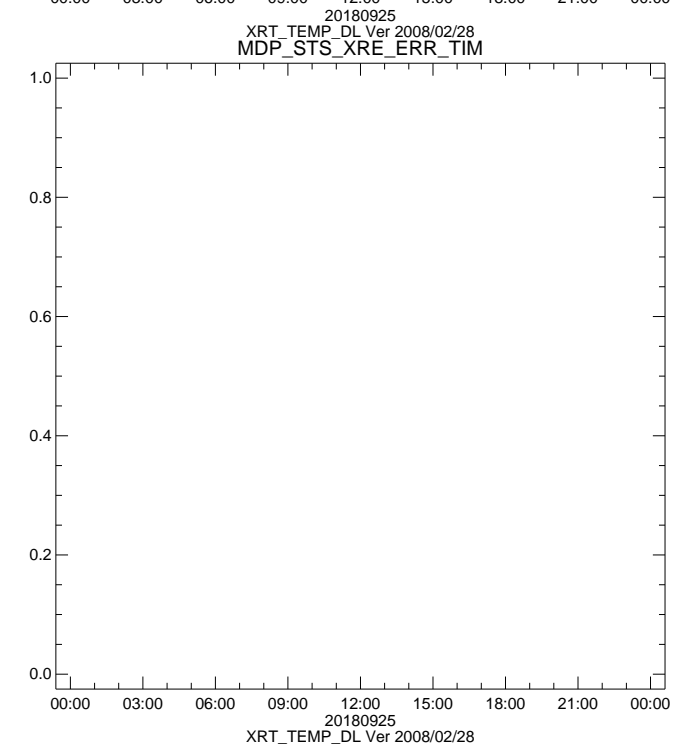
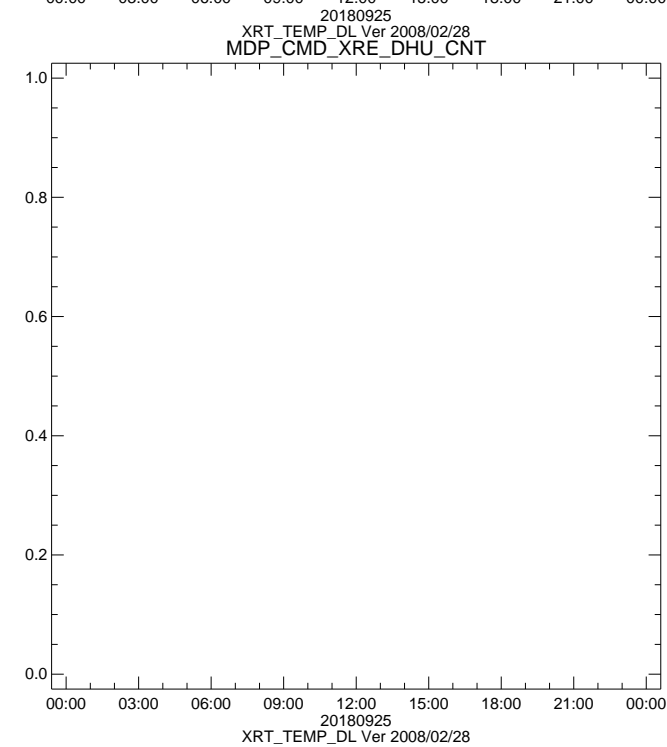
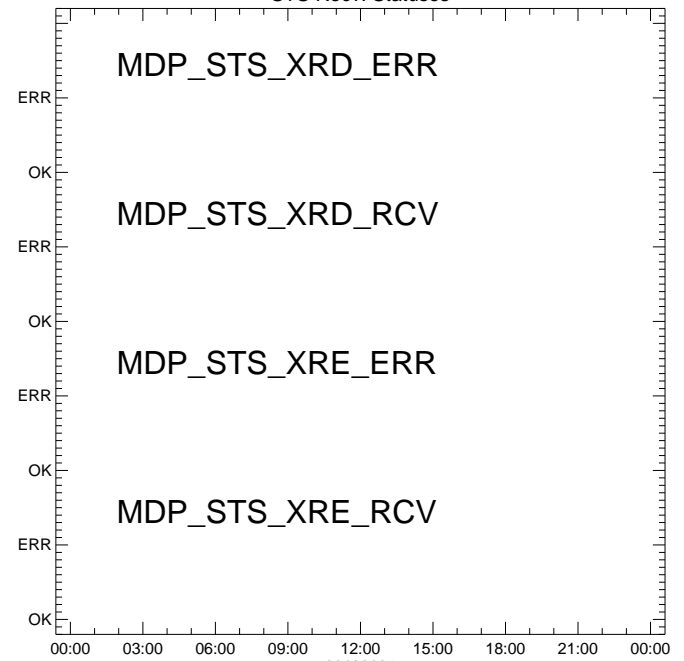
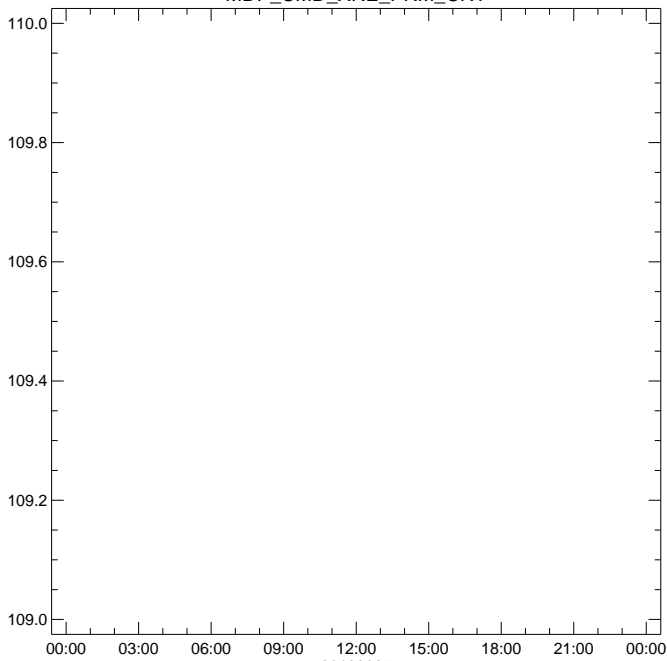
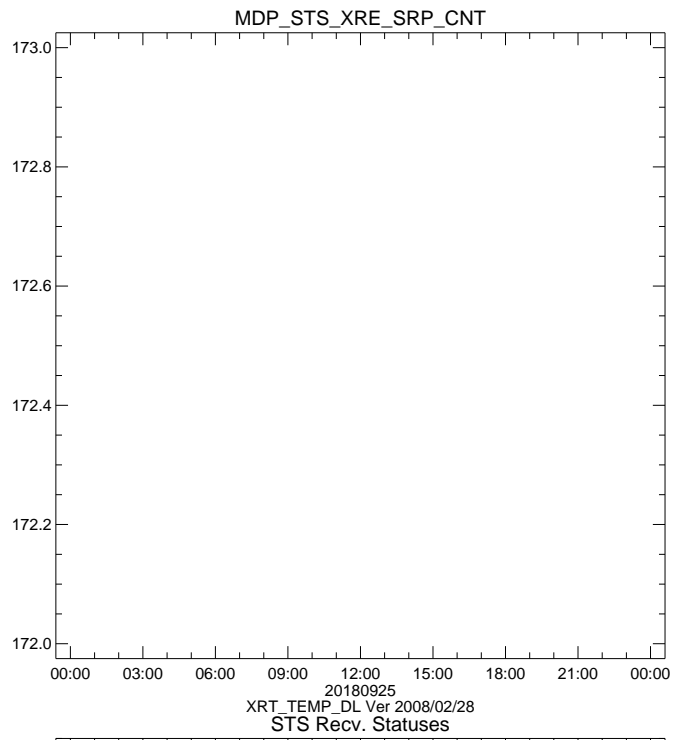
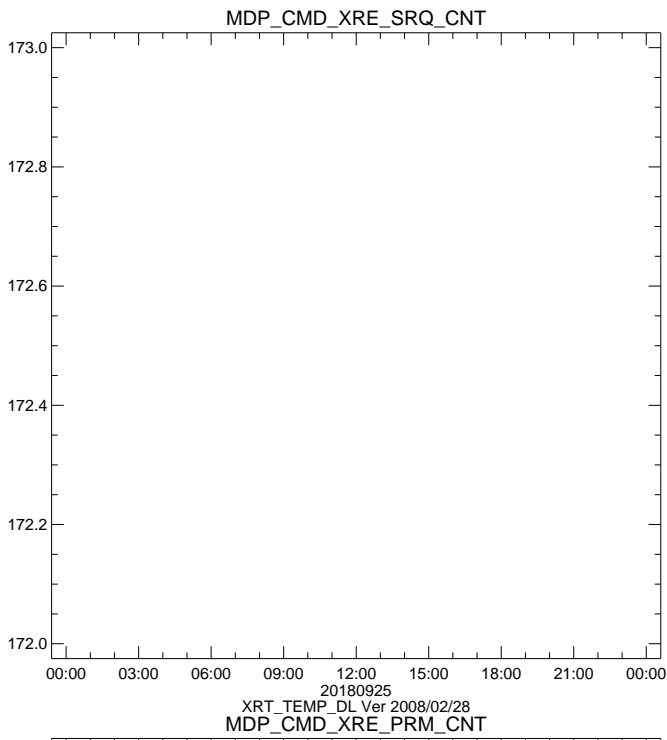


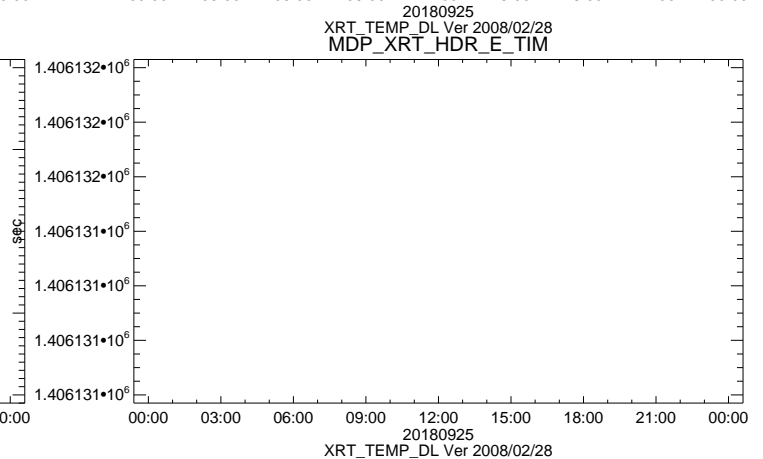
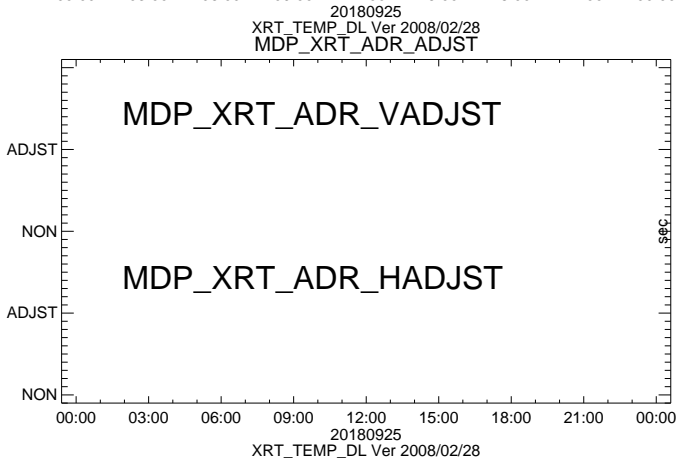
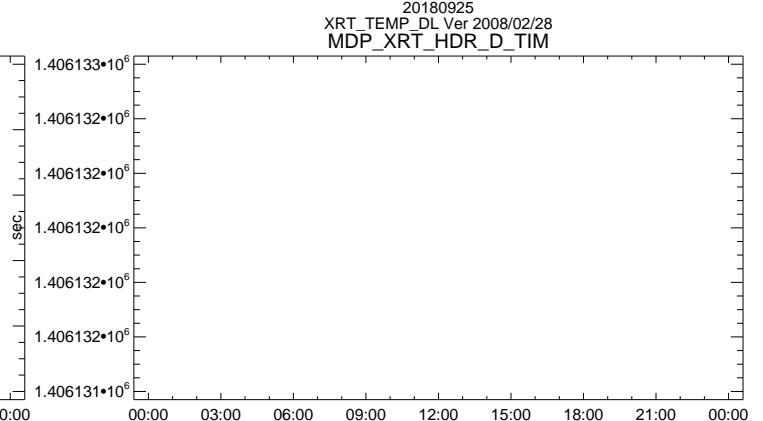
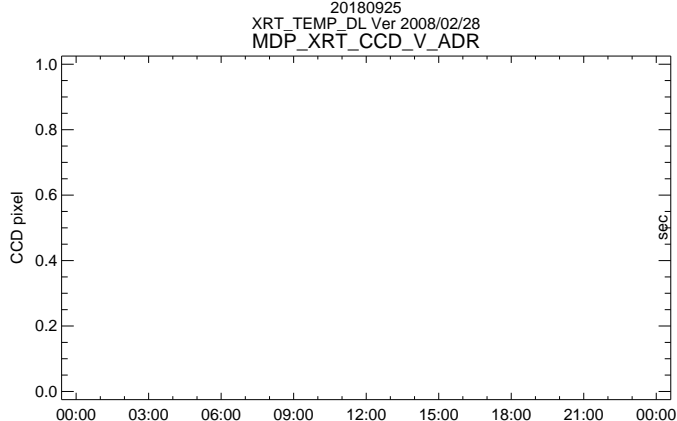
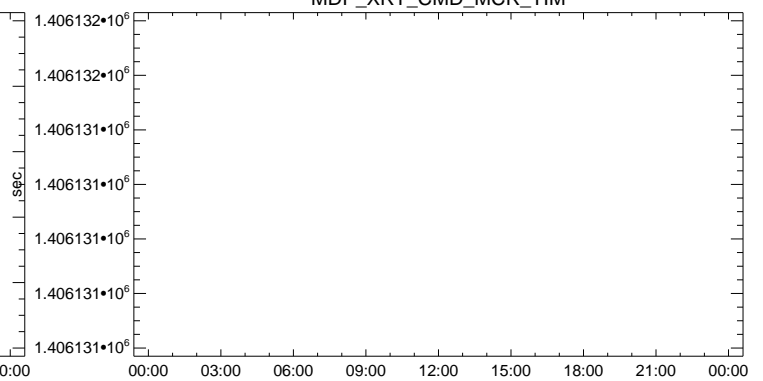
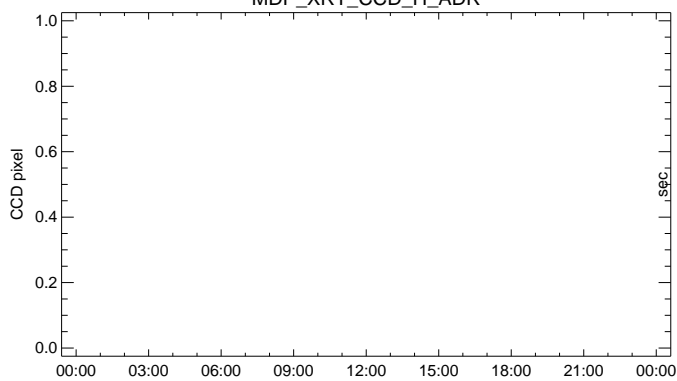
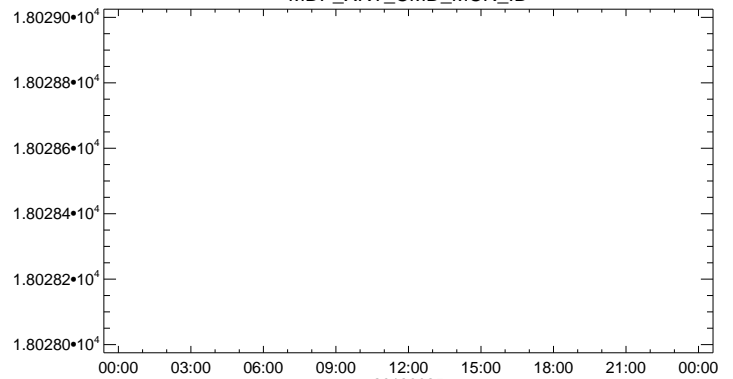
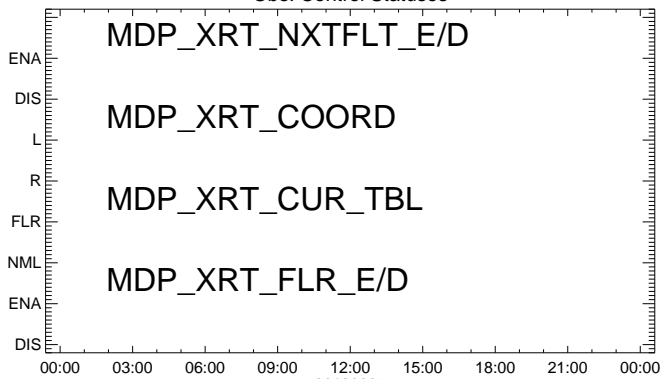
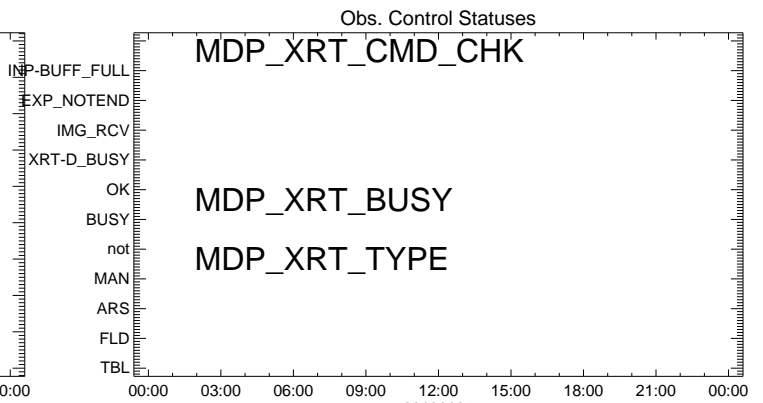
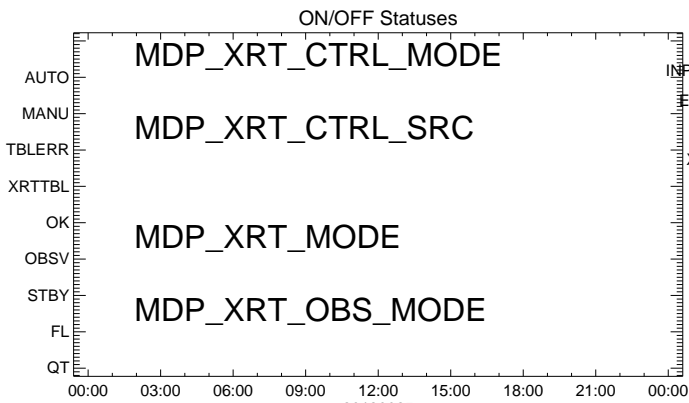
XRTD_E_LST_ACT

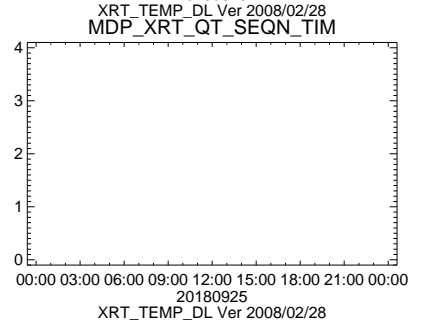
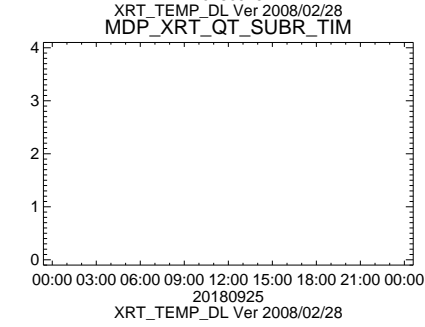
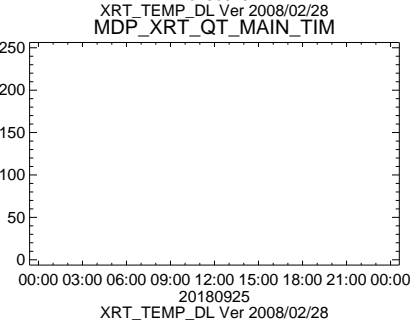
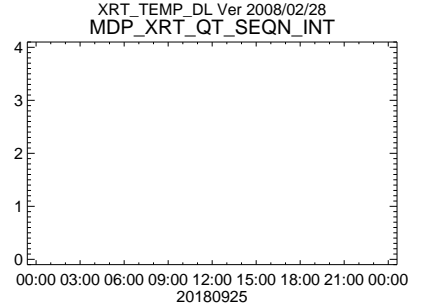
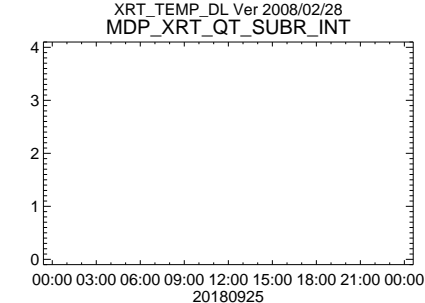
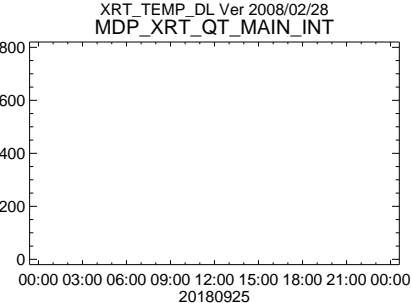
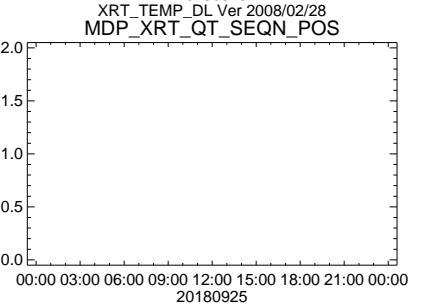
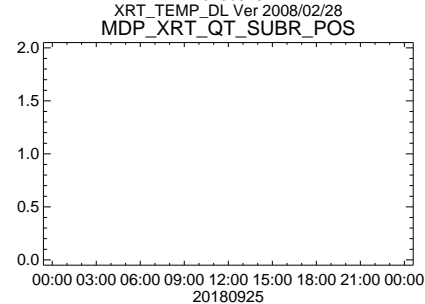
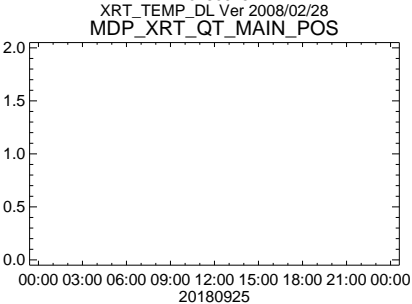
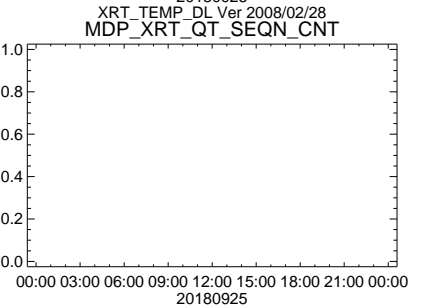
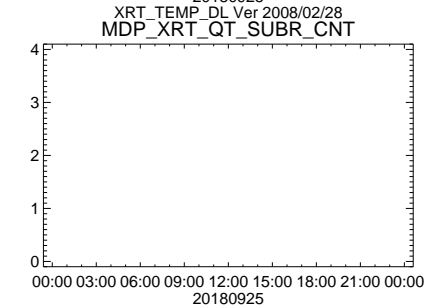
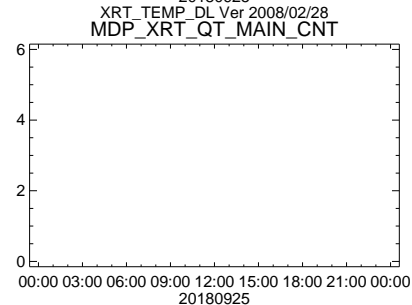
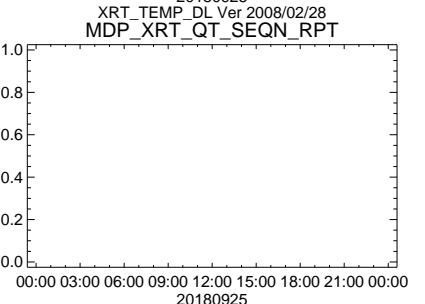
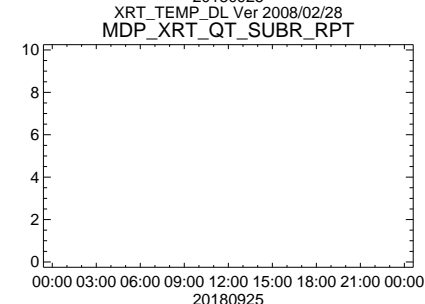
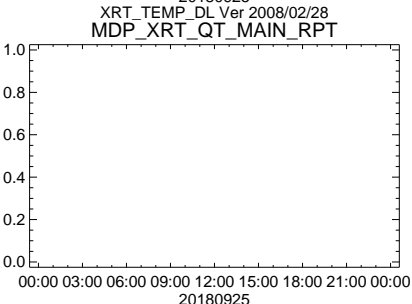
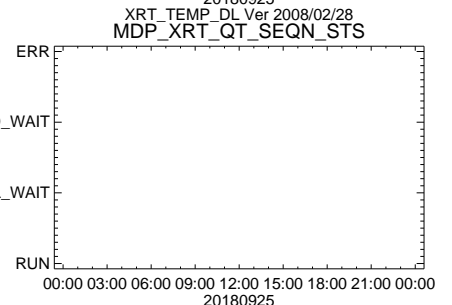
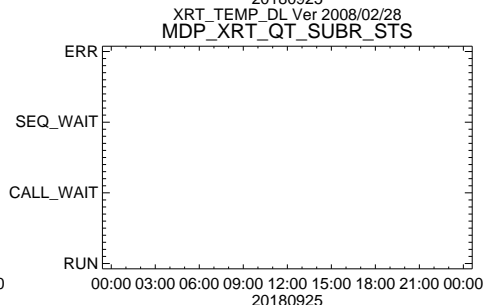
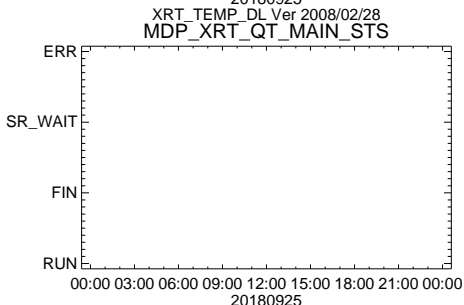
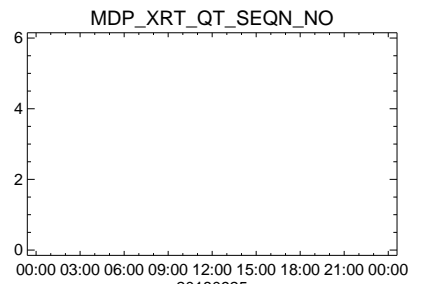
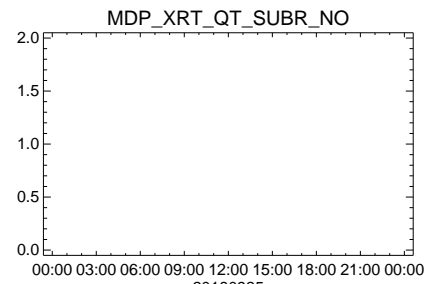
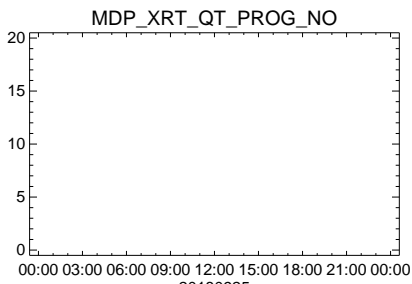


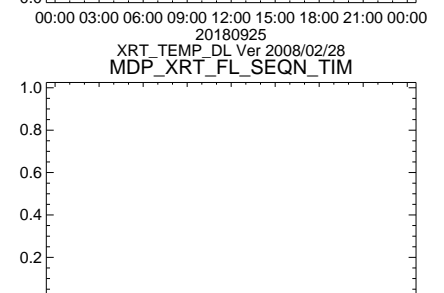
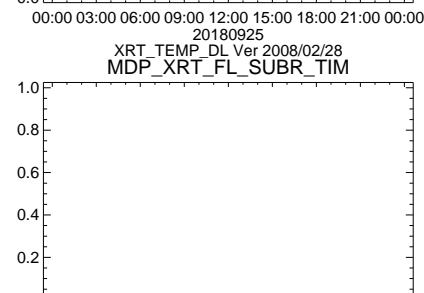
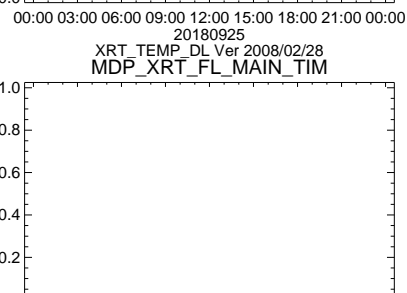
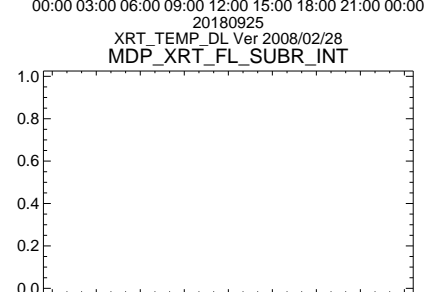
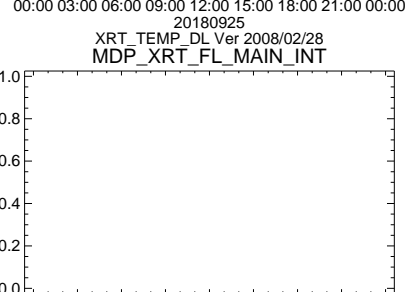
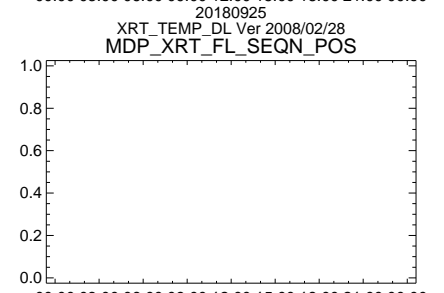
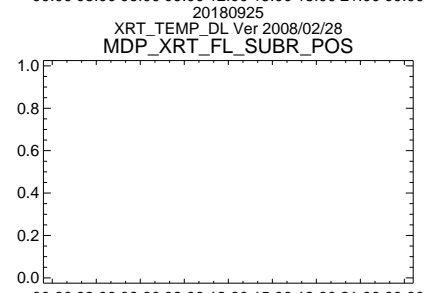
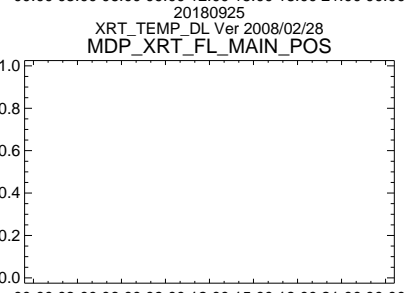
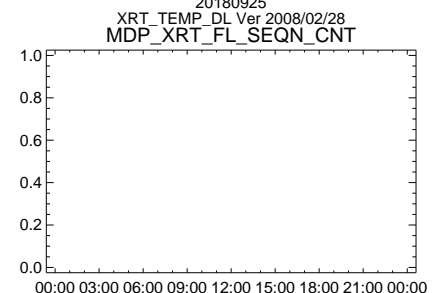
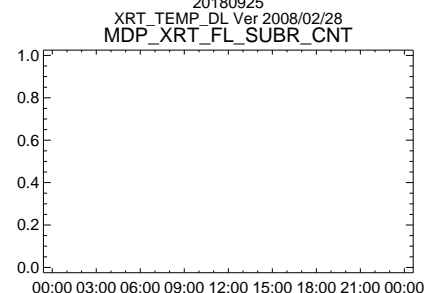
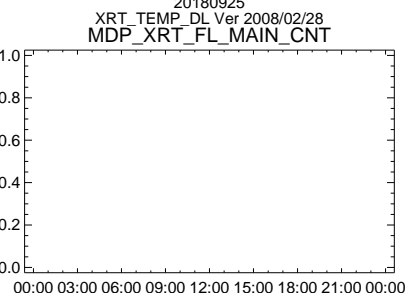
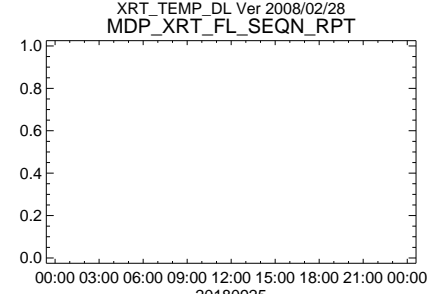
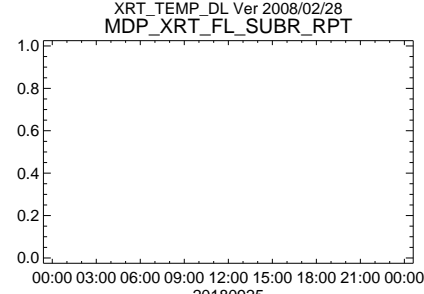
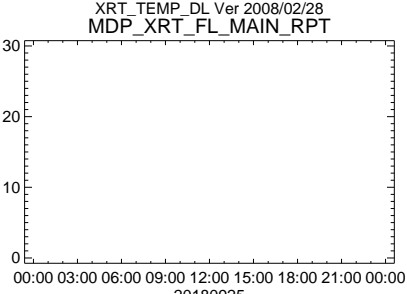
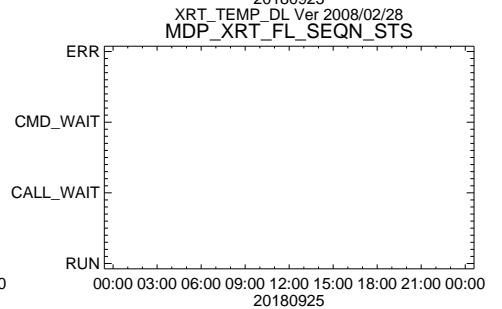
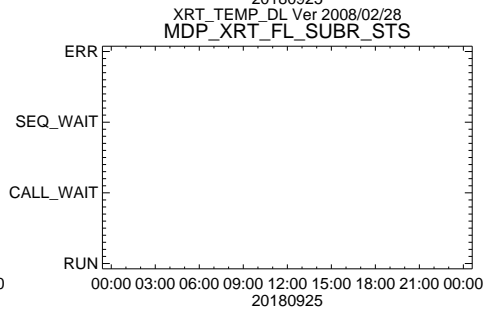
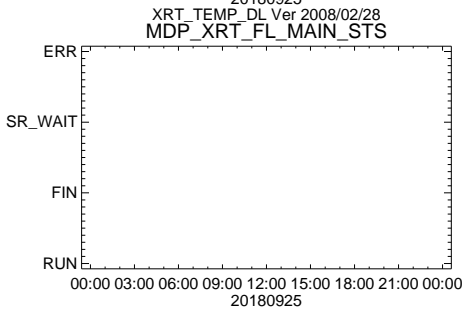
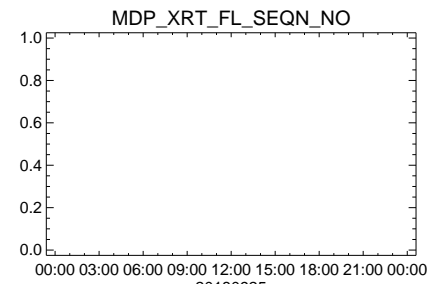
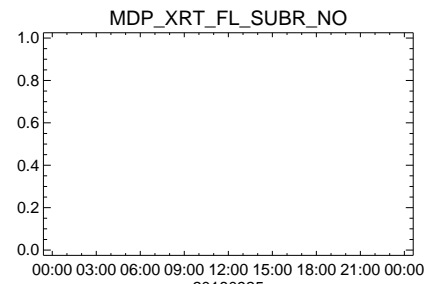
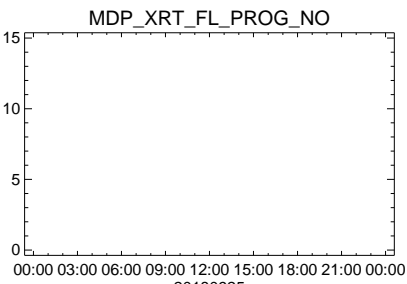




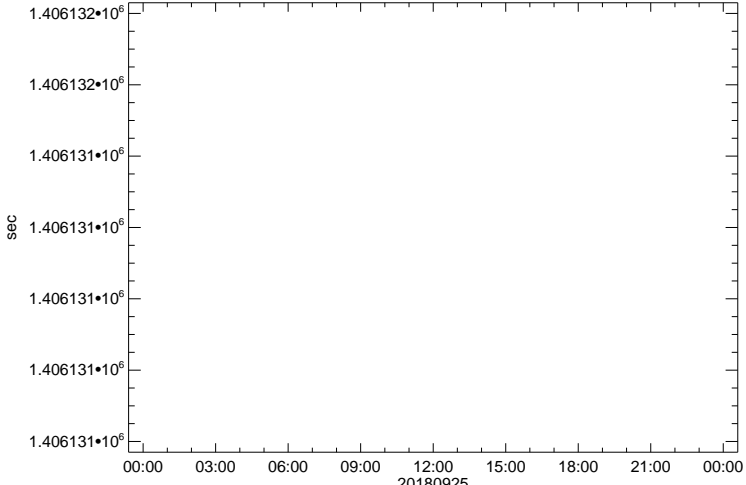




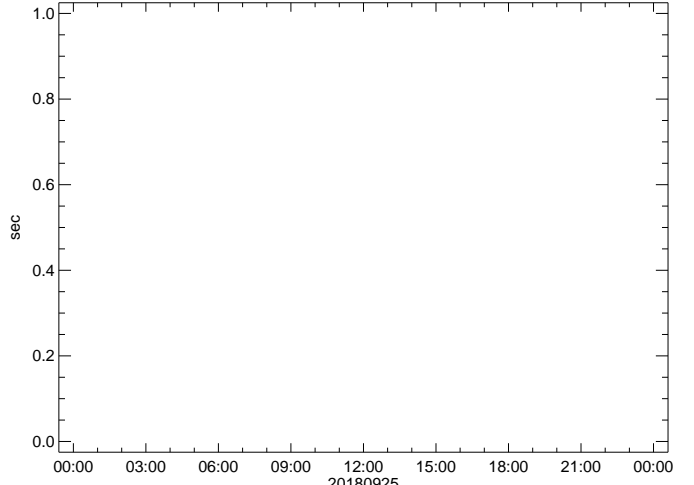




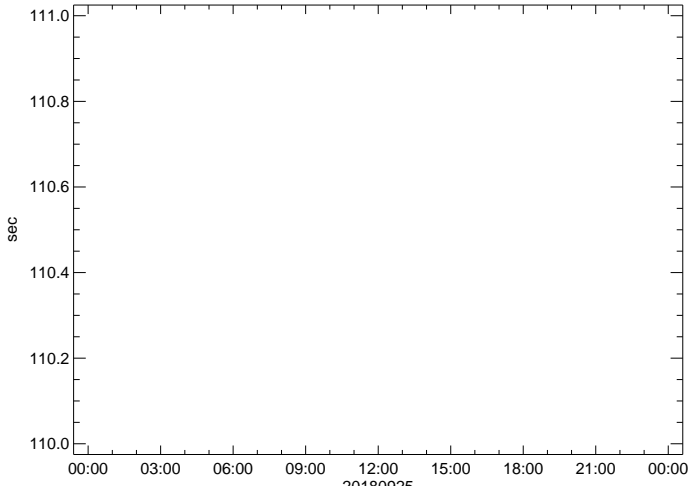
MDP_XRT_FLD_EXP_TIM



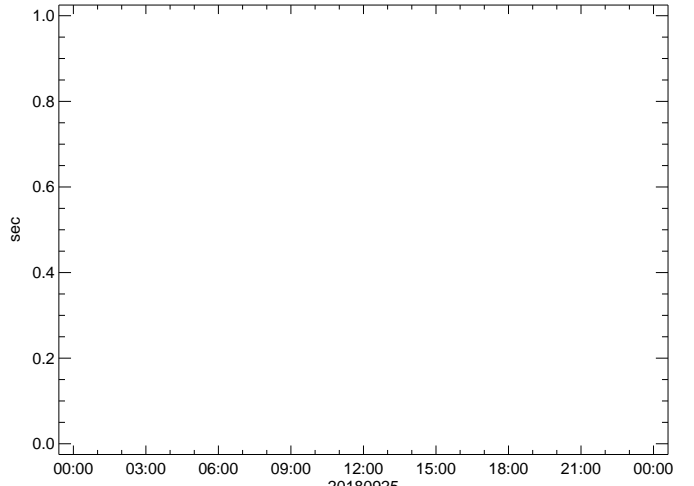
MDP_XRT_ARS_EXP_TIM



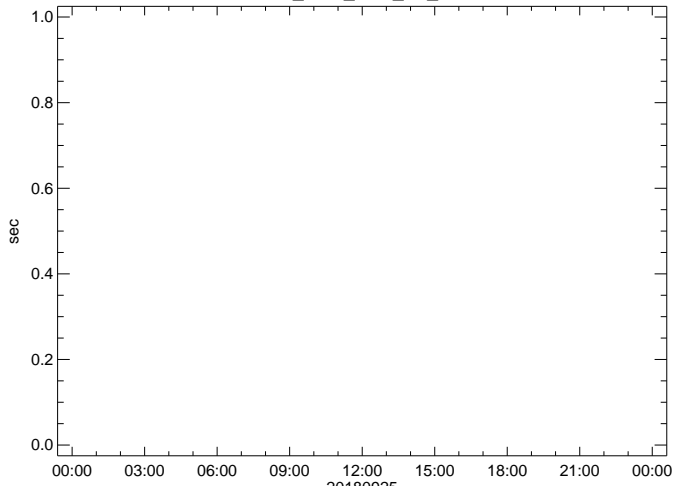
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_FLD_QT_ELP



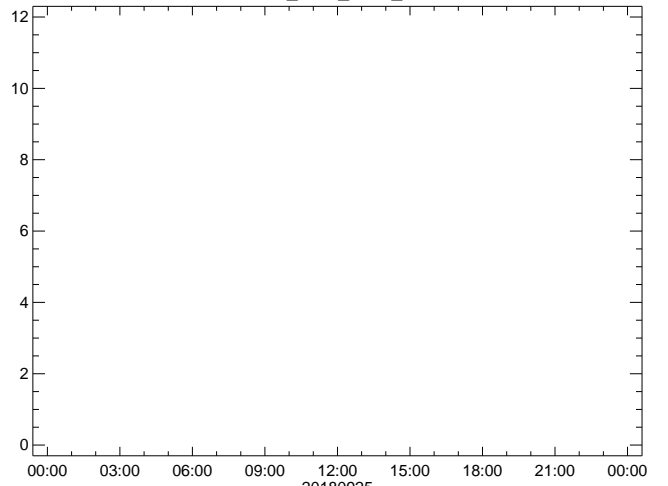
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_ARS_ELP



XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_FLD_FL_ELP

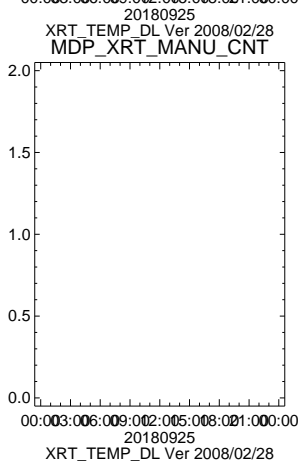
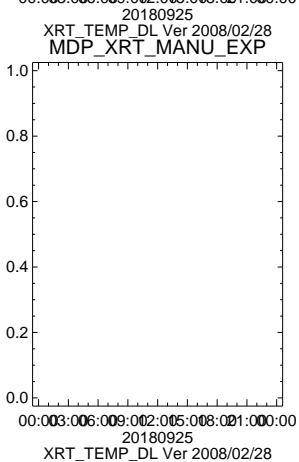
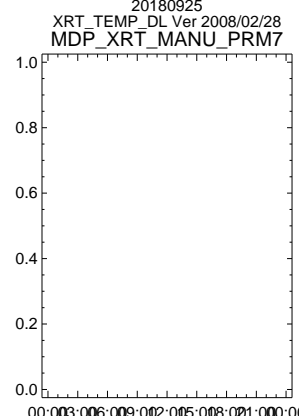
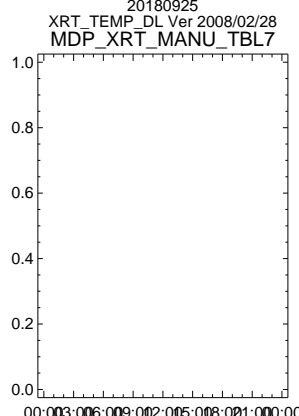
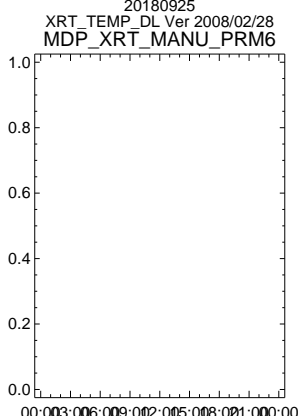
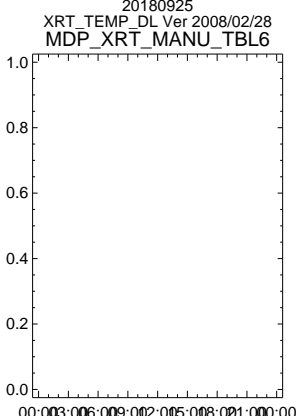
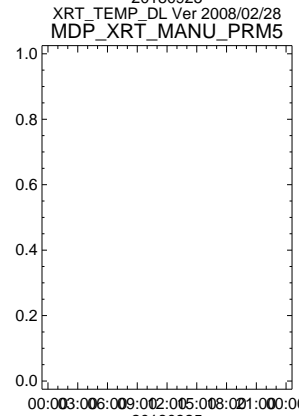
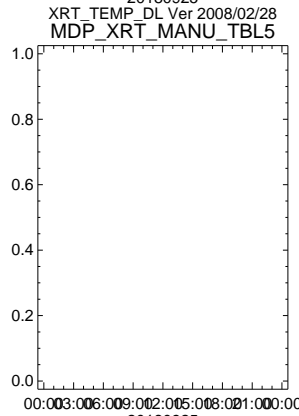
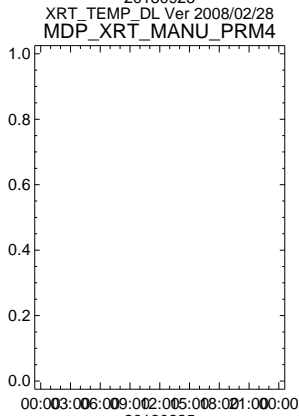
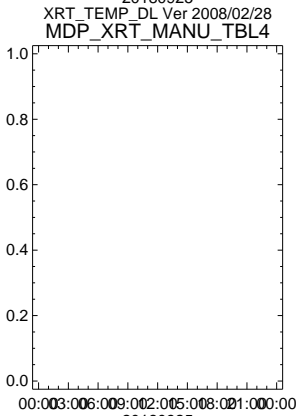
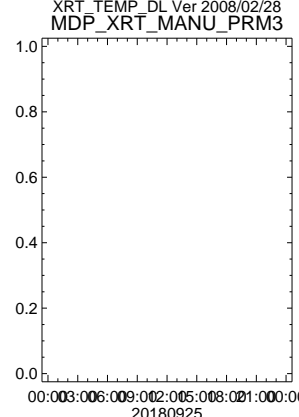
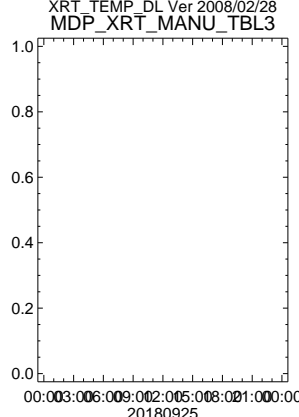
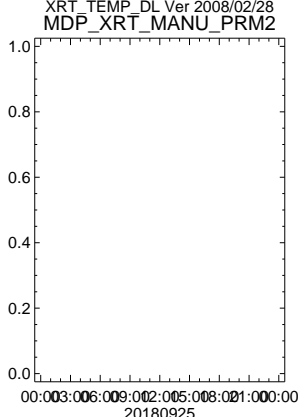
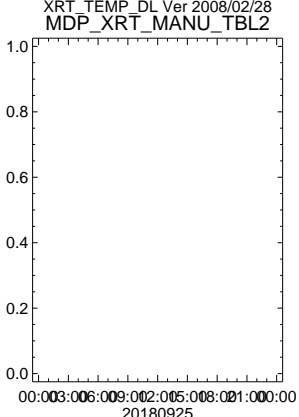
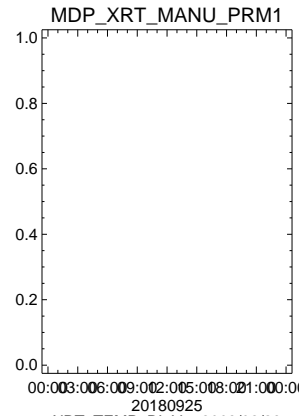
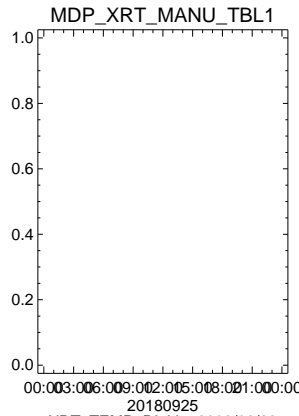
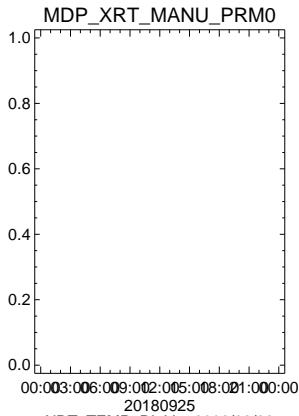
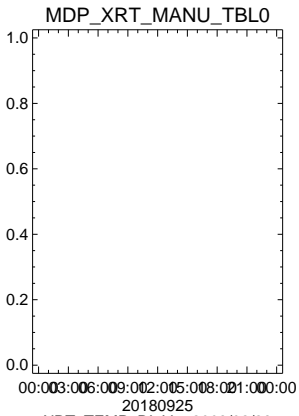


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MDP_XRT_ORT_NO



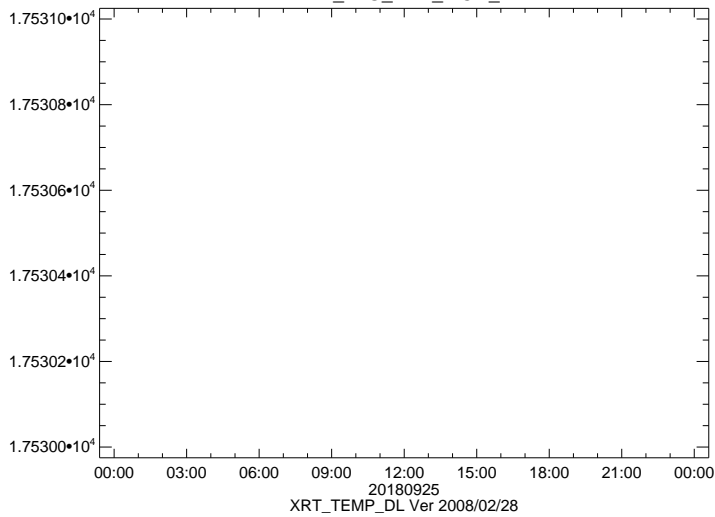
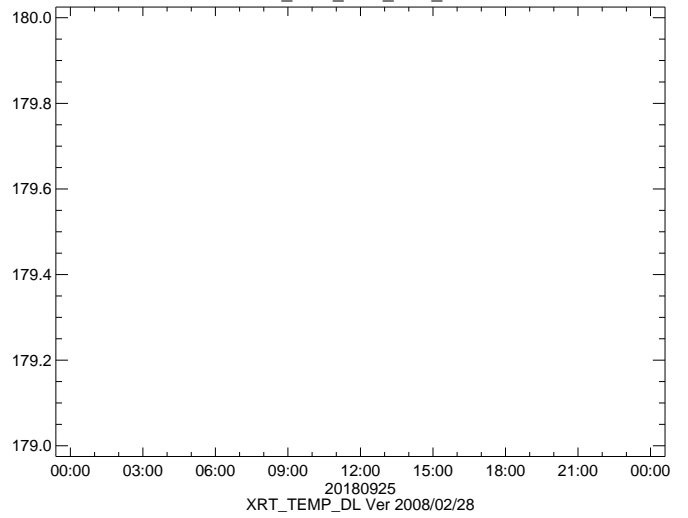
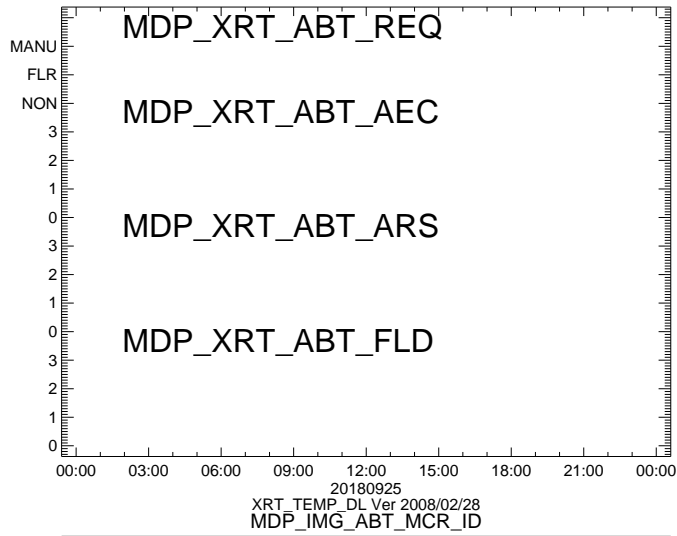
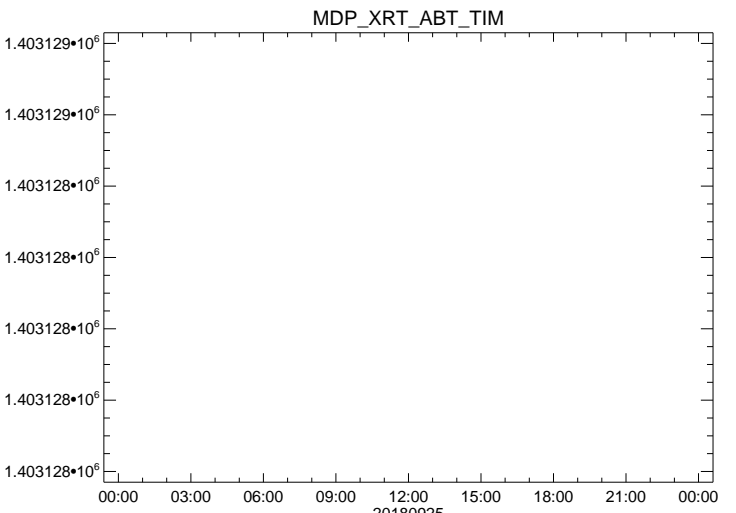
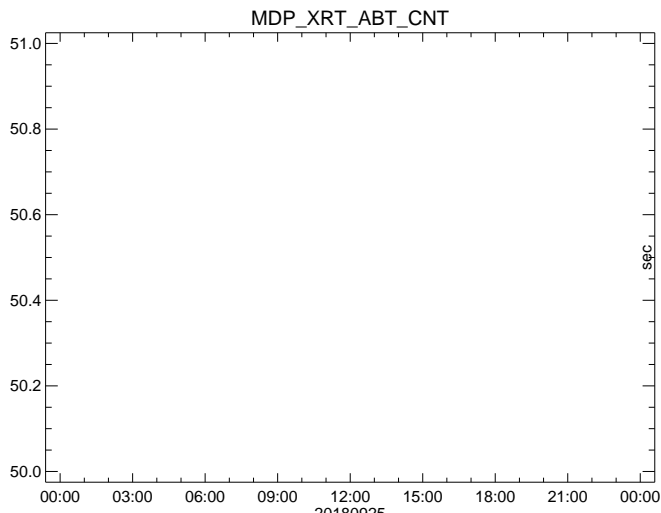
XRT_TEMP_DL Ver 2008/02/28

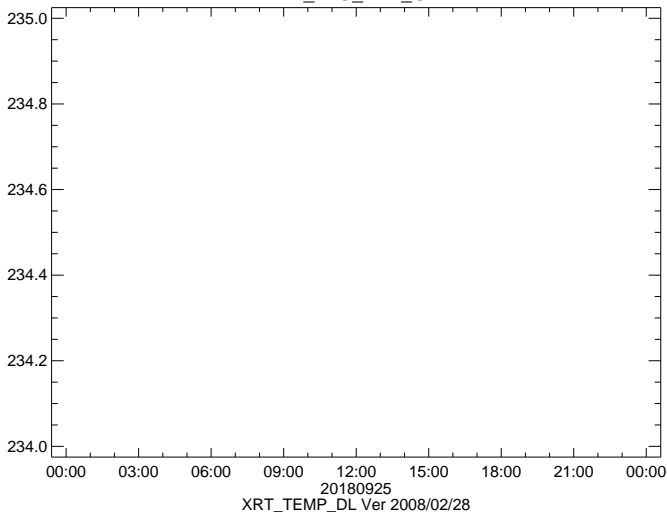
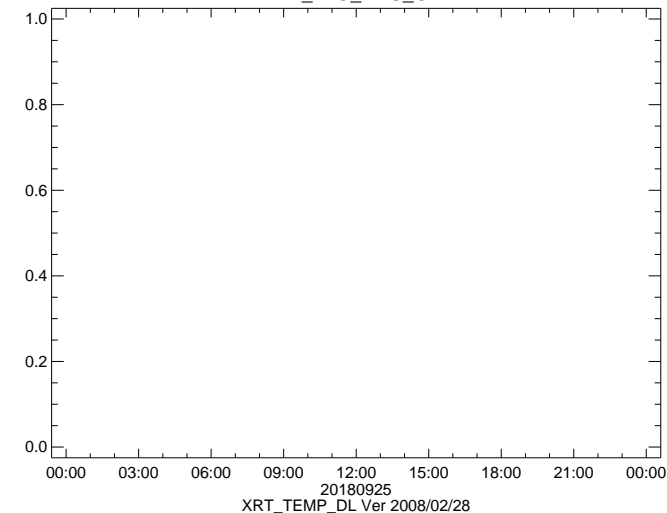
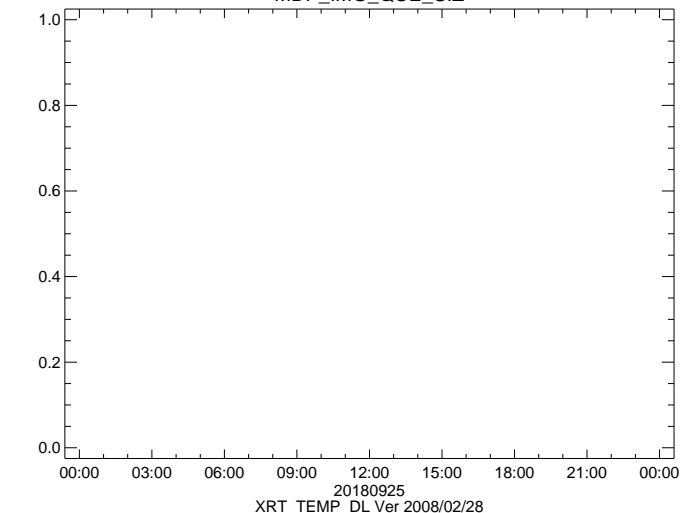
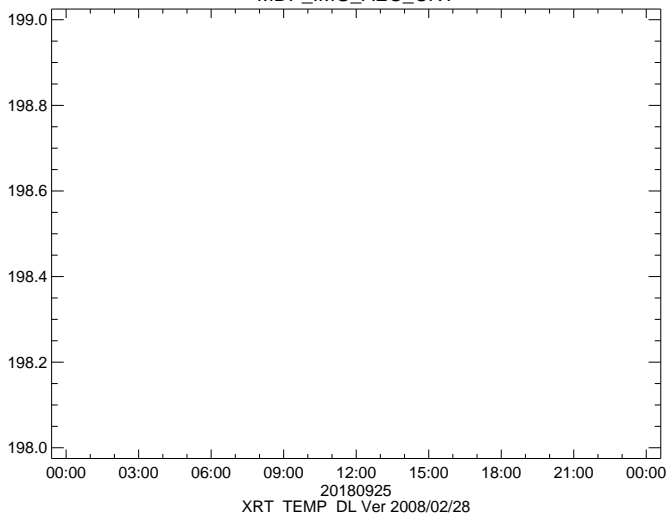
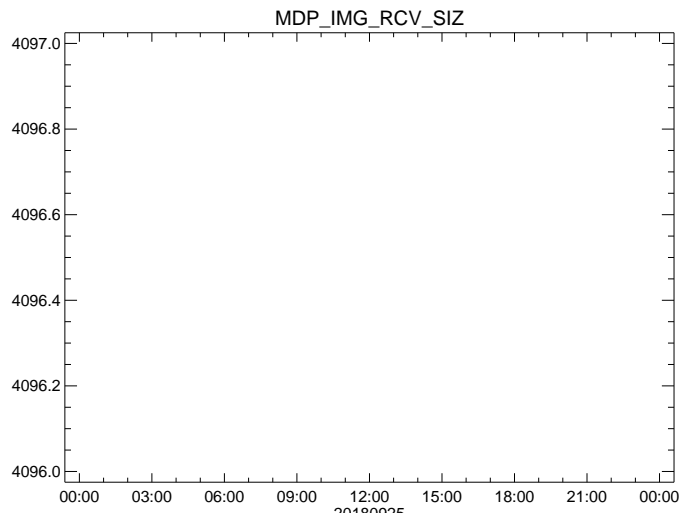
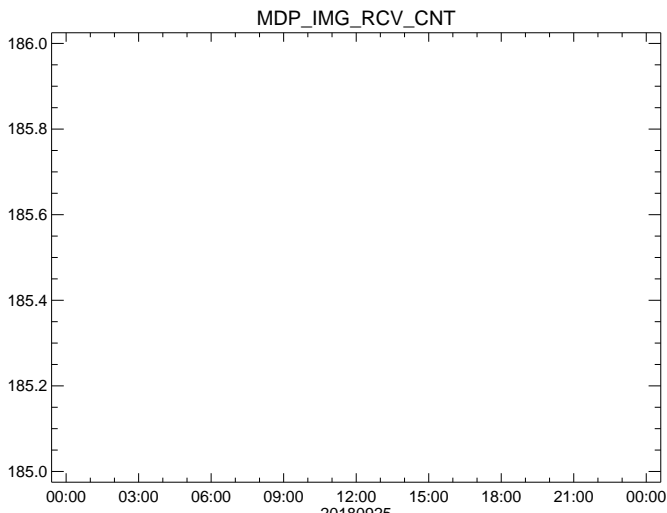
XRT_TEMP_DL Ver 2008/02/28

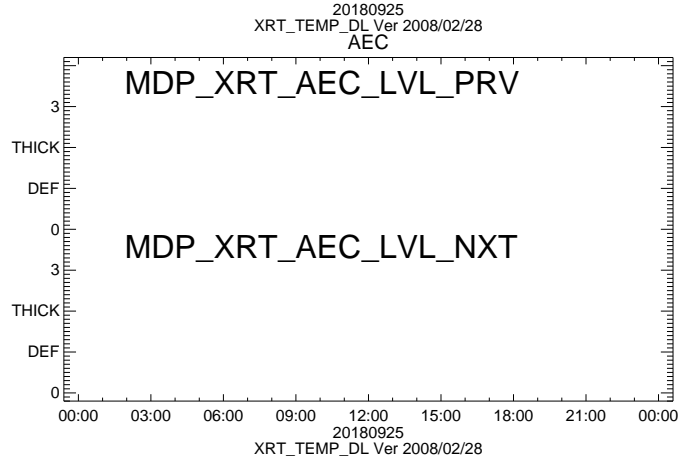
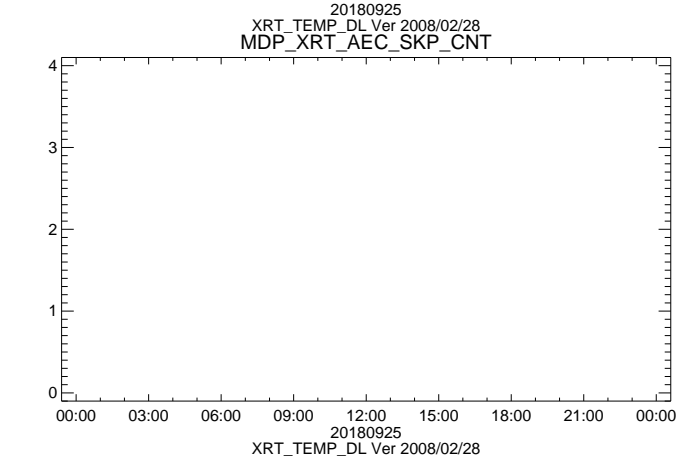
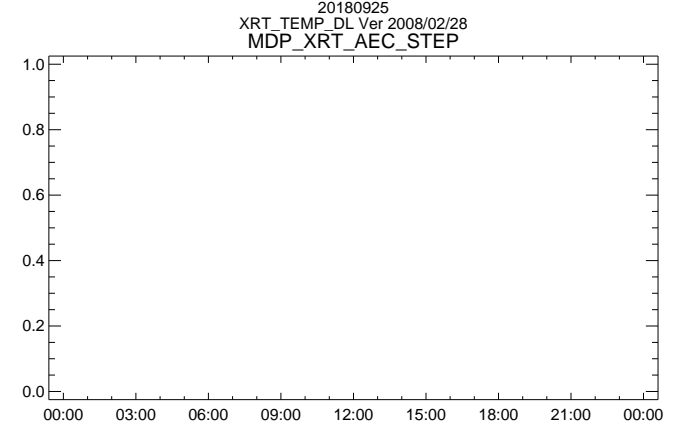
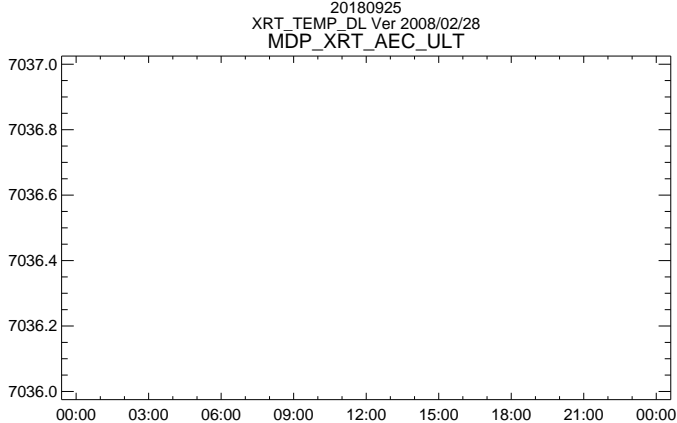
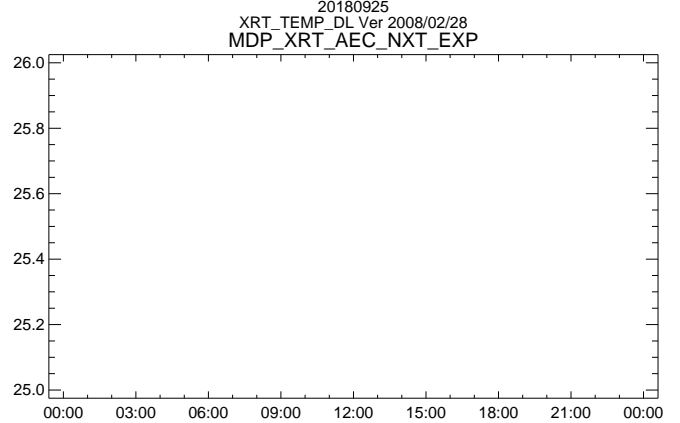
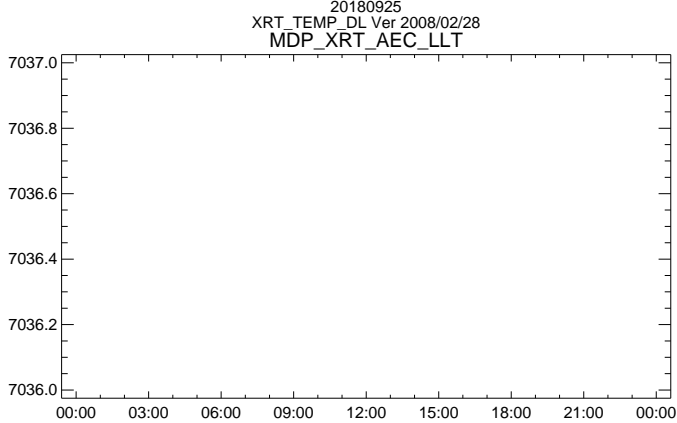
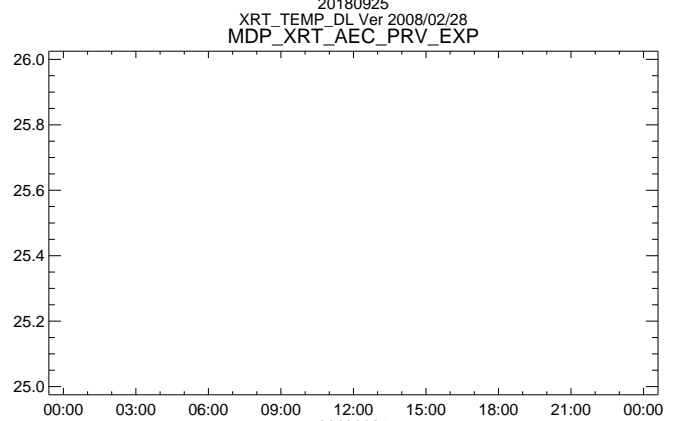
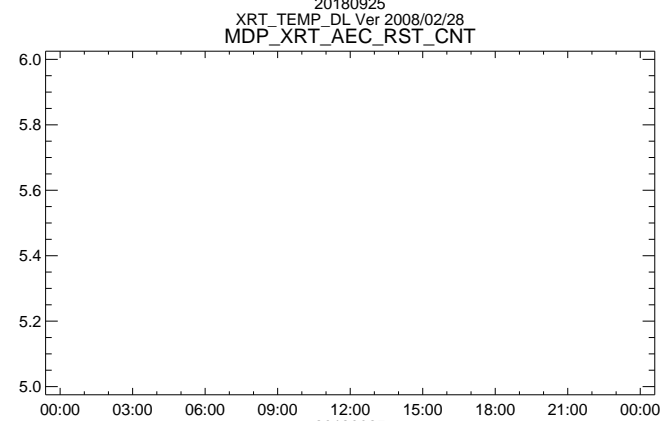
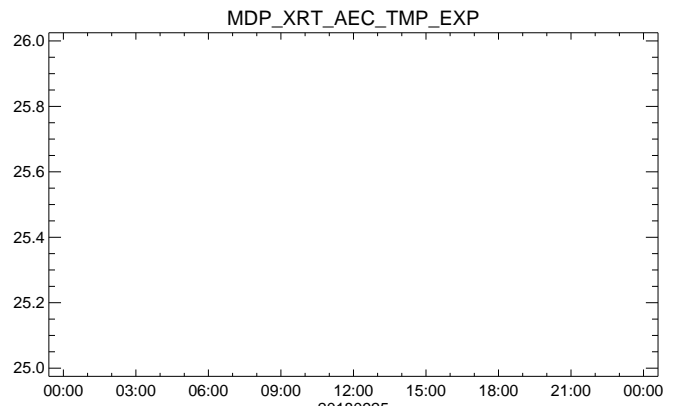
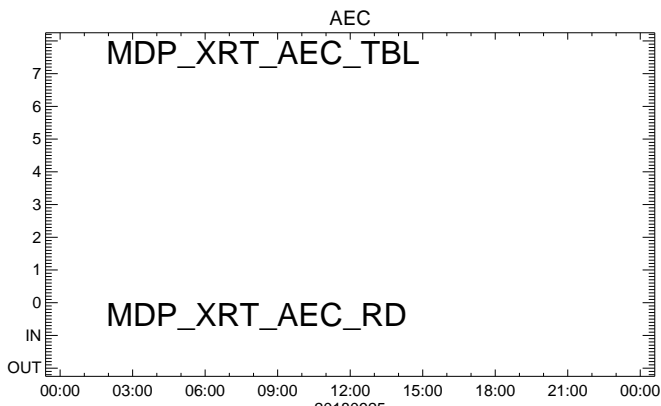


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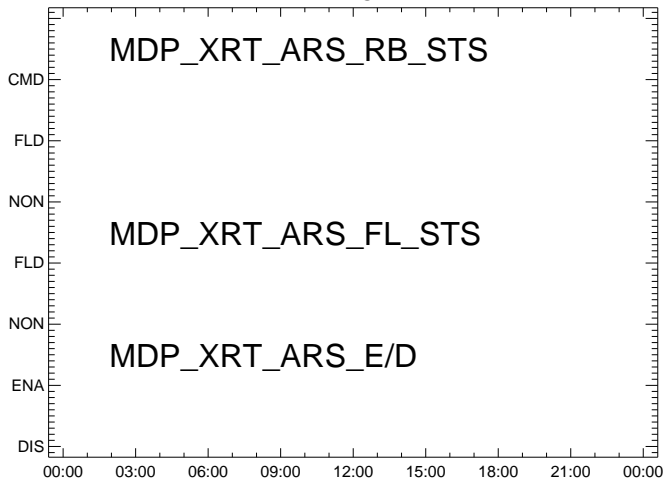
XRT_TEMP_DL Ver 2008/02/28



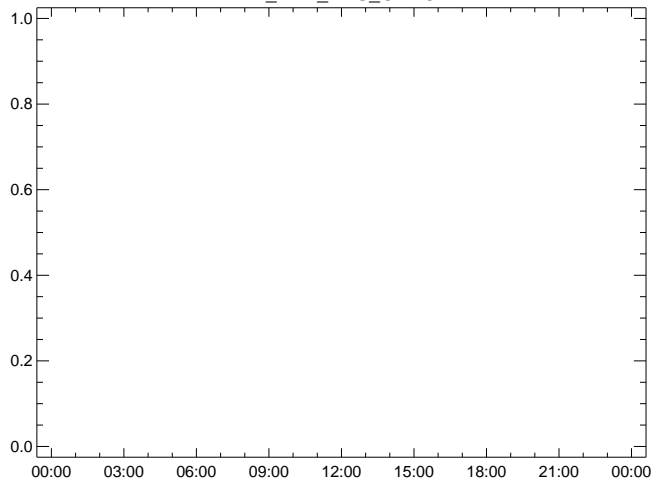




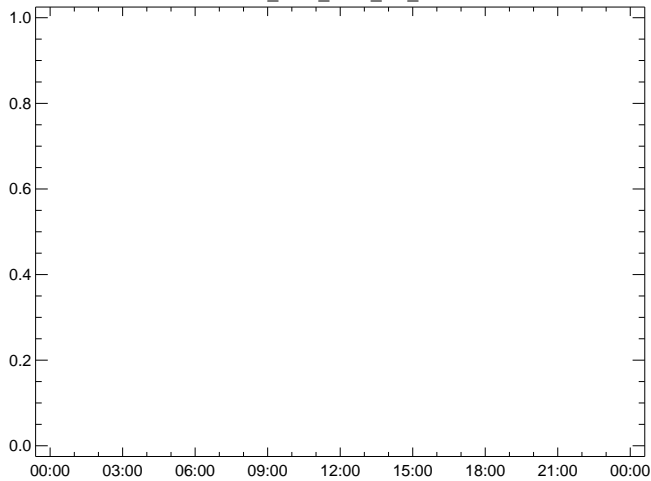
ARS



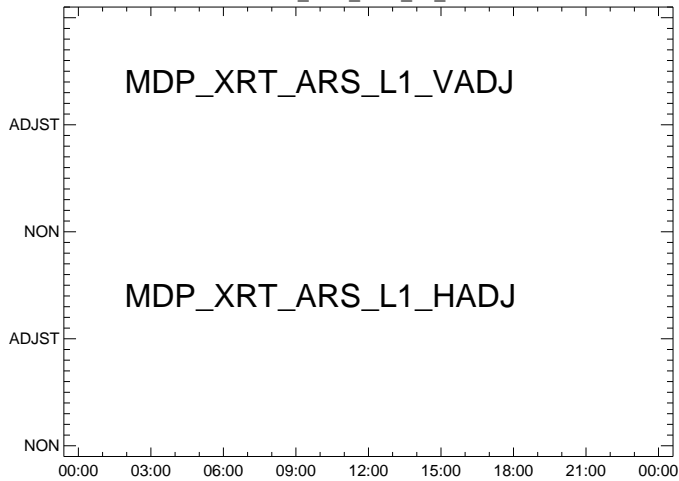
MDP_XRT_ARS_CANCEL



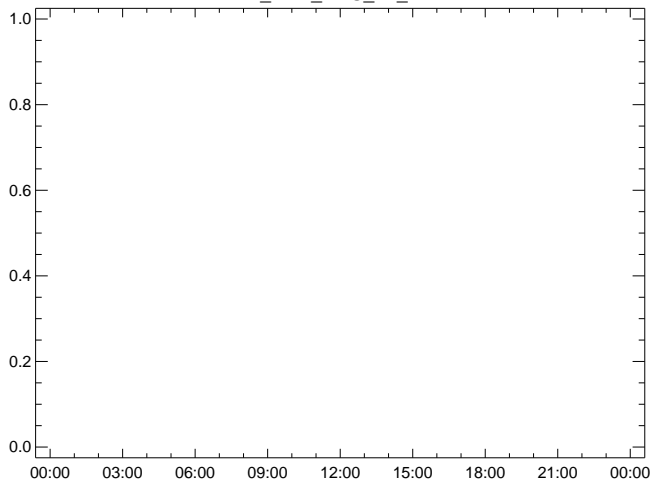
20180925
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_ARS_LC_STP



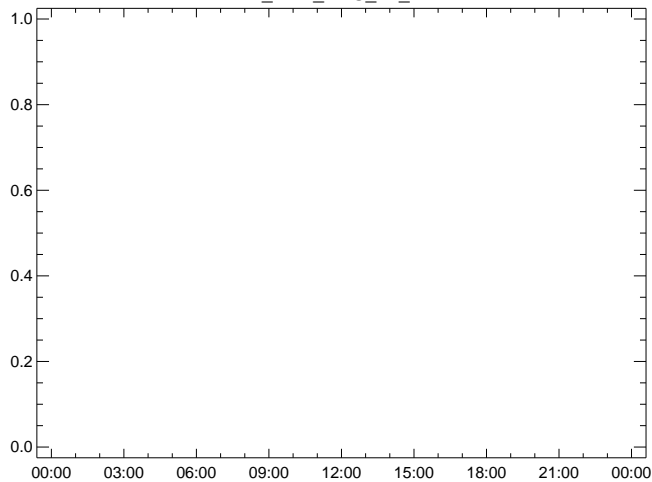
20180925
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_ARS_L1_ADJ



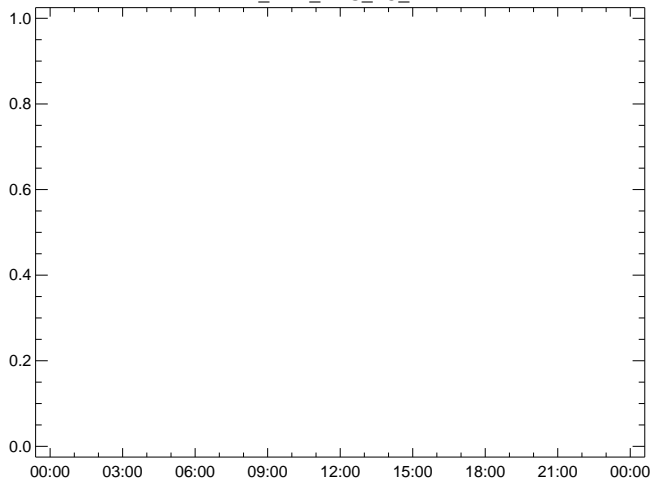
20180925
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_ARS_L2_HADR



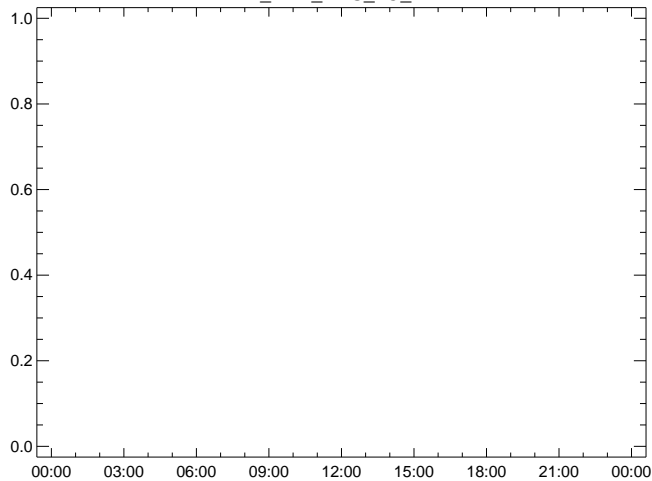
20180925
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_ARS_L2_VADR



20180925
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_ARS_LC_HADR



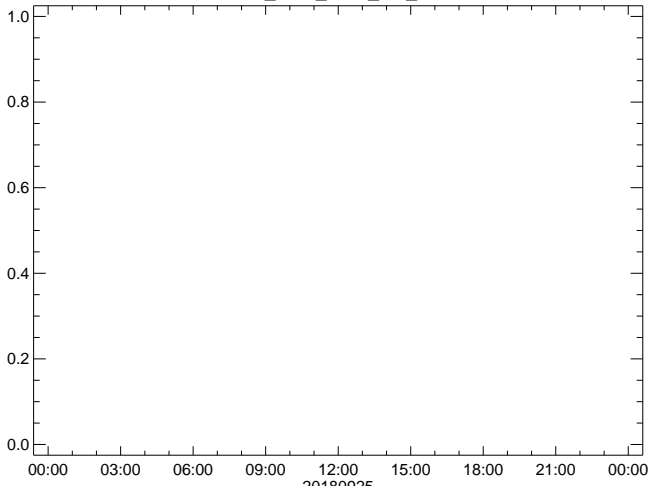
20180925
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_ARS_LC_VADR



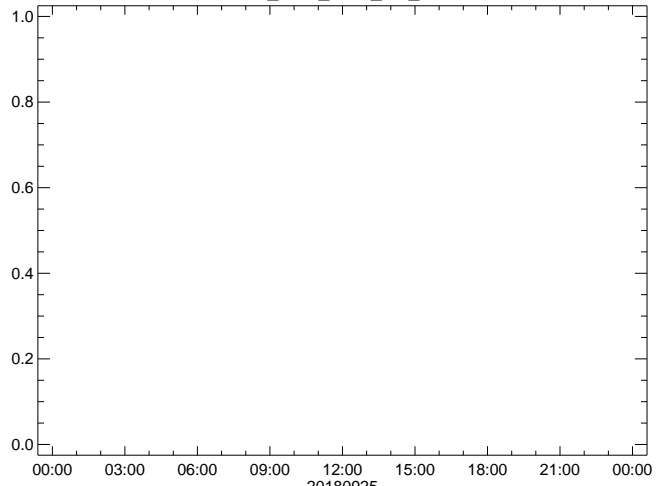
20180925
XRT_TEMP_DL Ver 2008/02/28

20180925
XRT_TEMP_DL Ver 2008/02/28

MDP_XRT_ARS_GL_STP



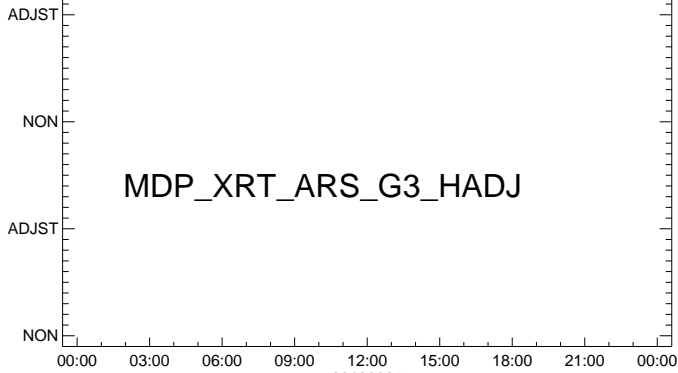
MDP_XRT_ARS_G2_ERR



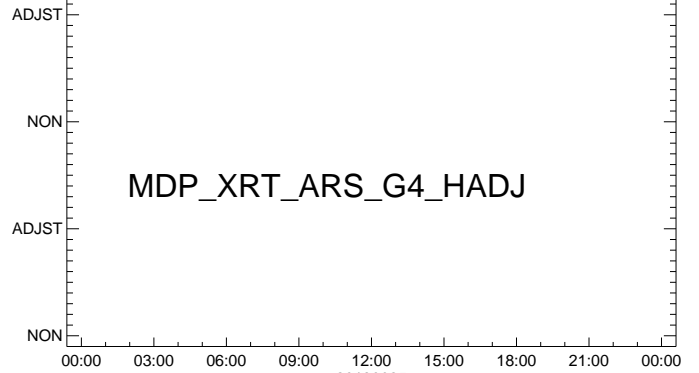
20180925
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_ARS_G3_ADJ

20180925
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_ARS_G4_ADJ

MDP_XRT_ARS_G3_VADJ



MDP_XRT_ARS_G4_VADJ

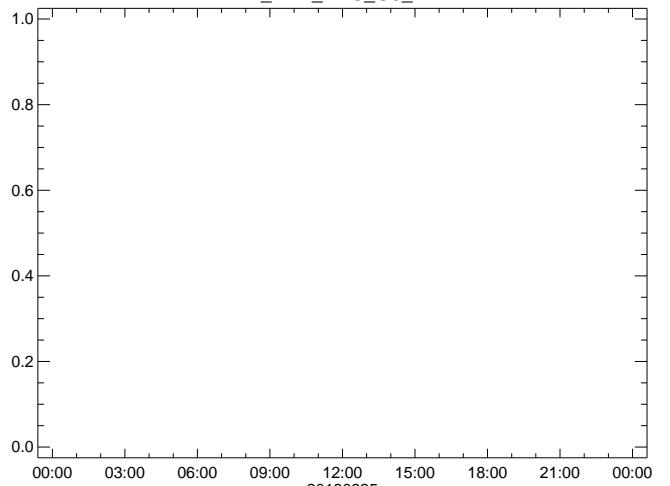
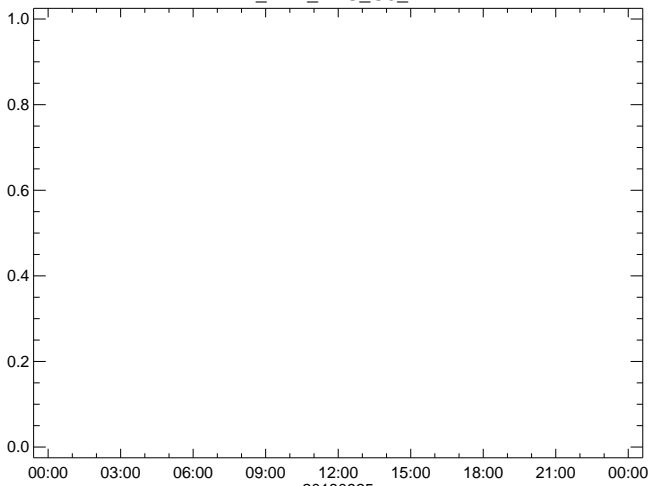


MDP_XRT_ARS_G3_HADJ

20180925
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_ARS_G3_HADR

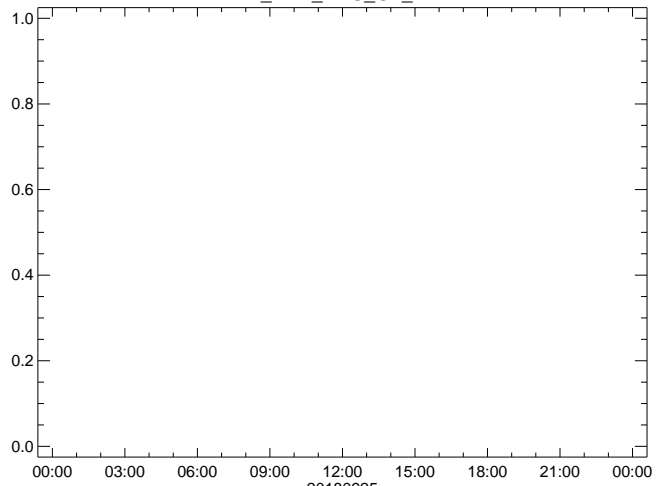
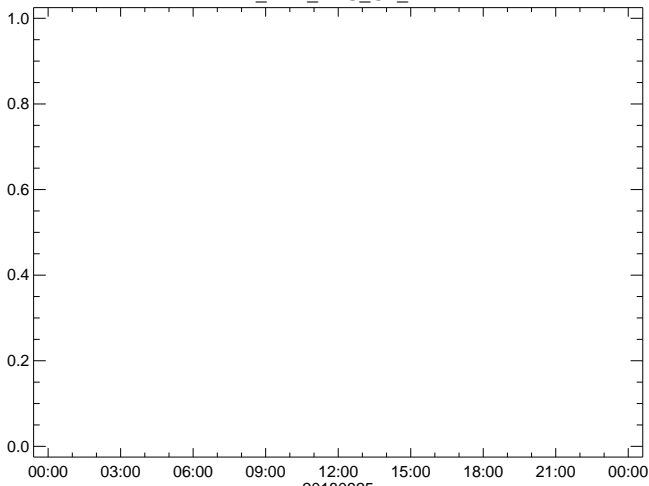
MDP_XRT_ARS_G4_HADJ

20180925
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_ARS_G3_VADR



20180925
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_ARS_GL_HADR

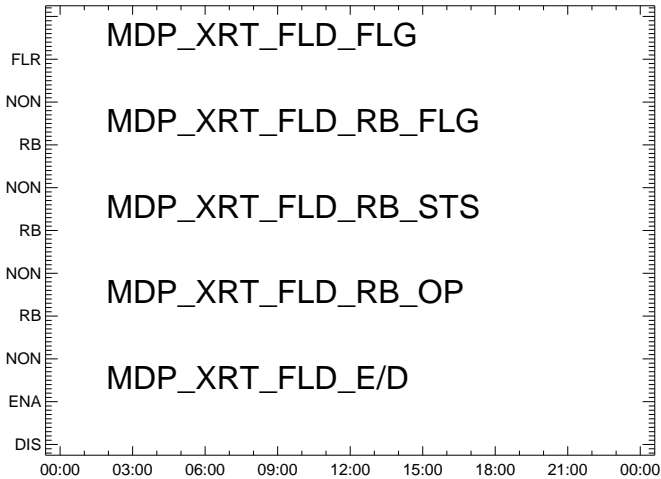
20180925
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_ARS_GL_VADR



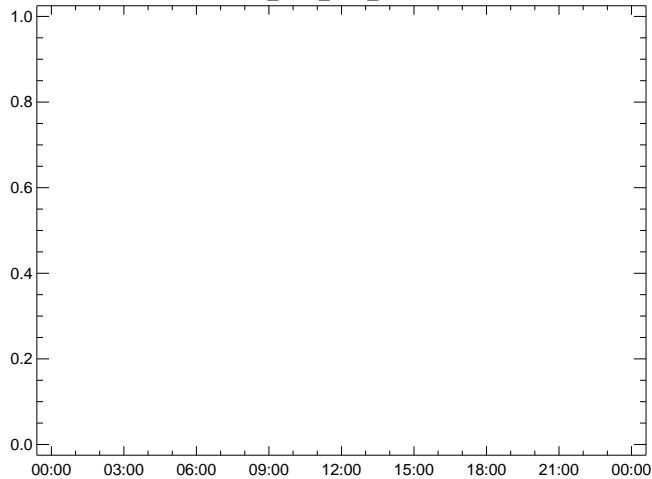
20180925
XRT_TEMP_DL Ver 2008/02/28

20180925
XRT_TEMP_DL Ver 2008/02/28

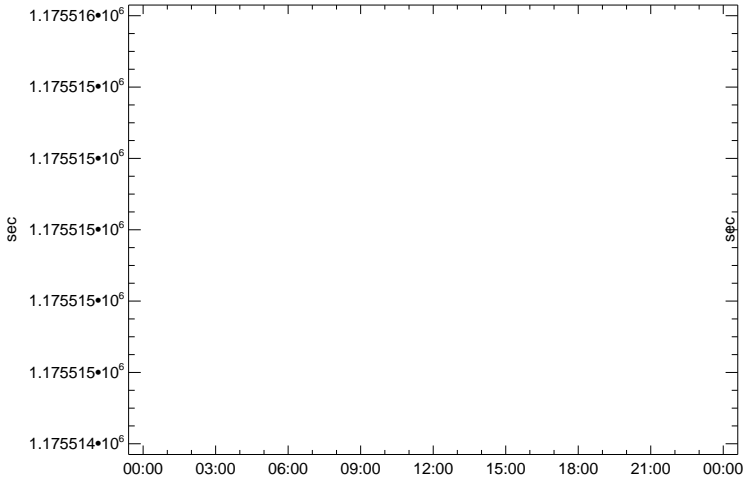
FLD



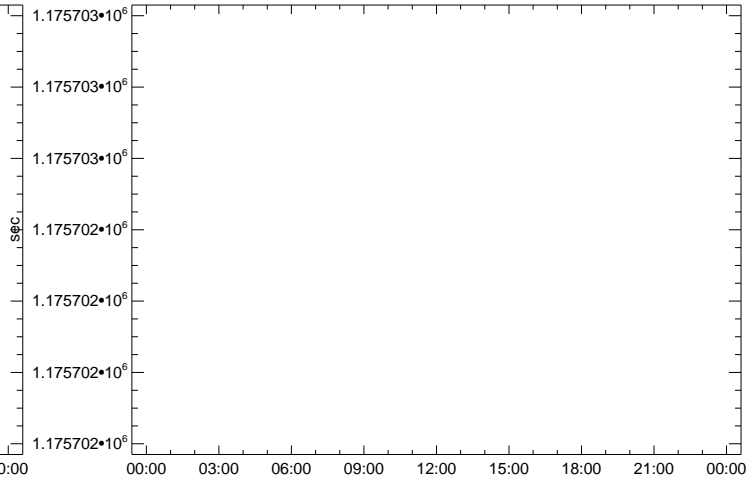
MDP_XRT_FLD_CANCEL



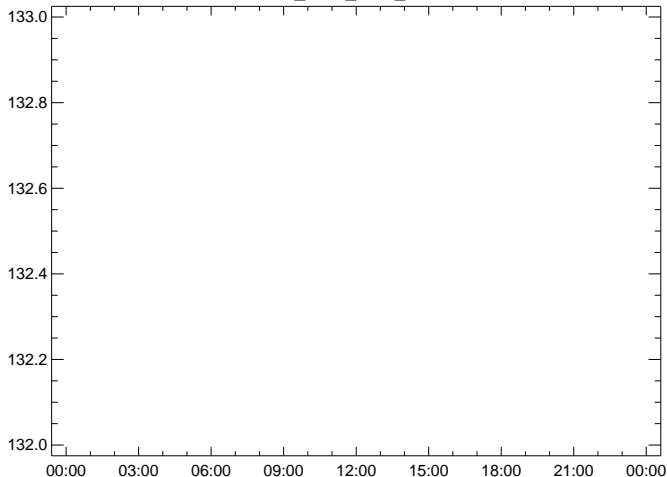
XRT_TEMP_DL Ver 2008/02/28
20180925
MDP_XRT_FLR_OCR_TIM



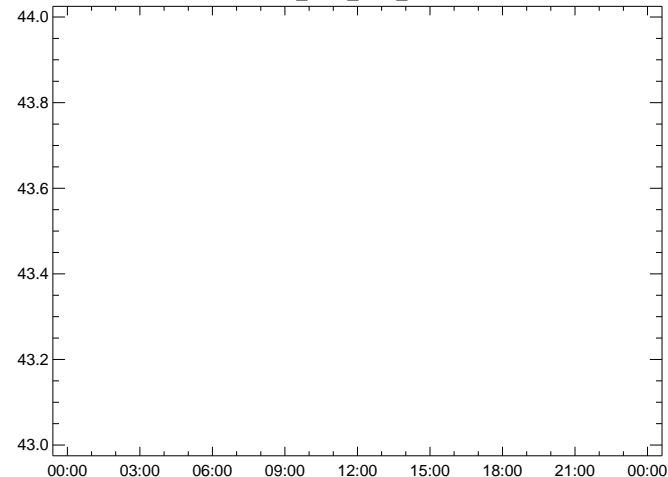
XRT_TEMP_DL Ver 2008/02/28
20180925
MDP_XRT_FLR_TRM_TIM



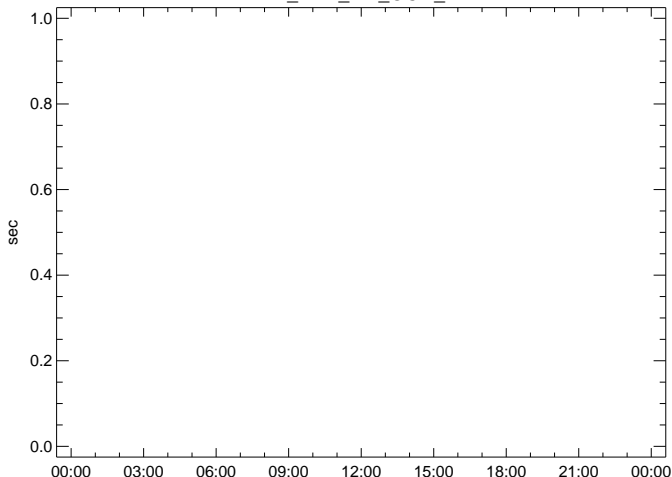
XRT_TEMP_DL Ver 2008/02/28
20180925
MDP_XRT_FLD_HADR



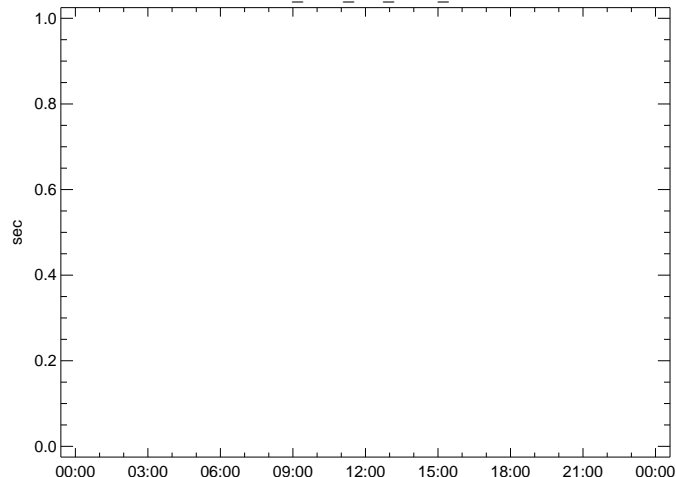
XRT_TEMP_DL Ver 2008/02/28
20180925
MDP_XRT_FLD_VADR



XRT_TEMP_DL Ver 2008/02/28
20180925
MDP_XRT_RD_OCR_TIM

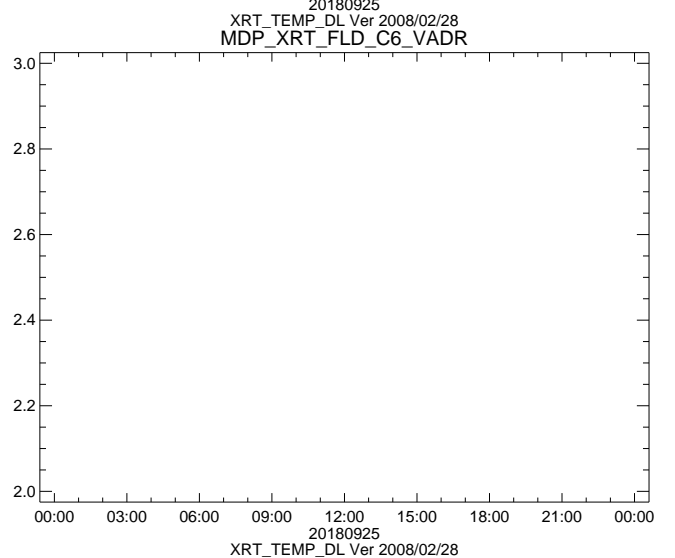
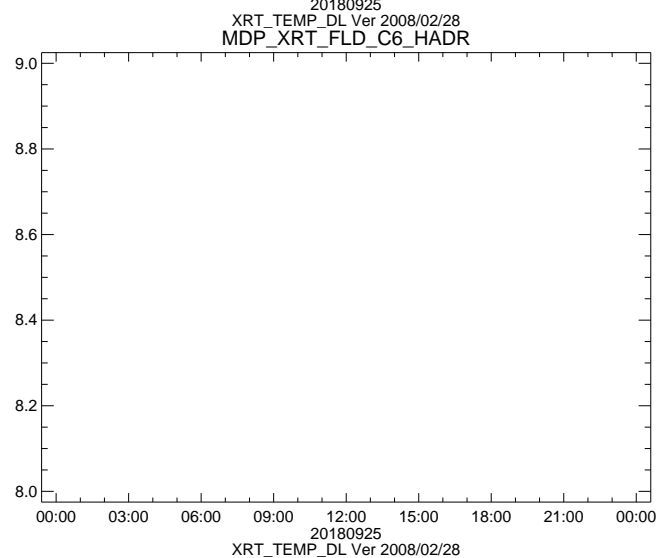
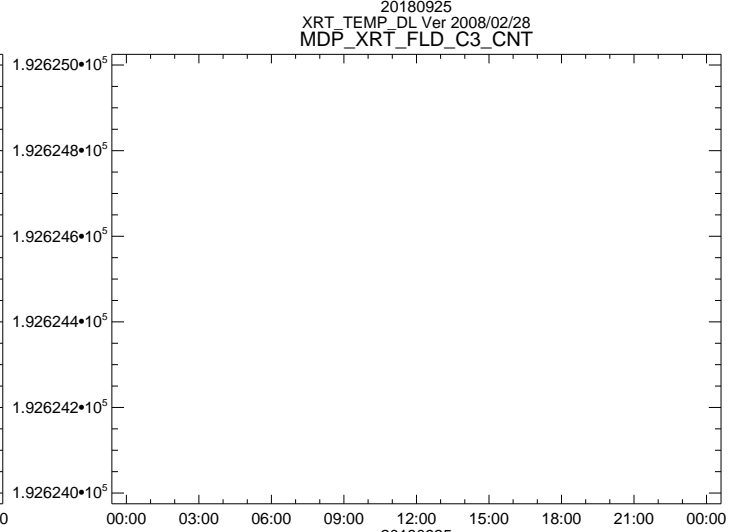
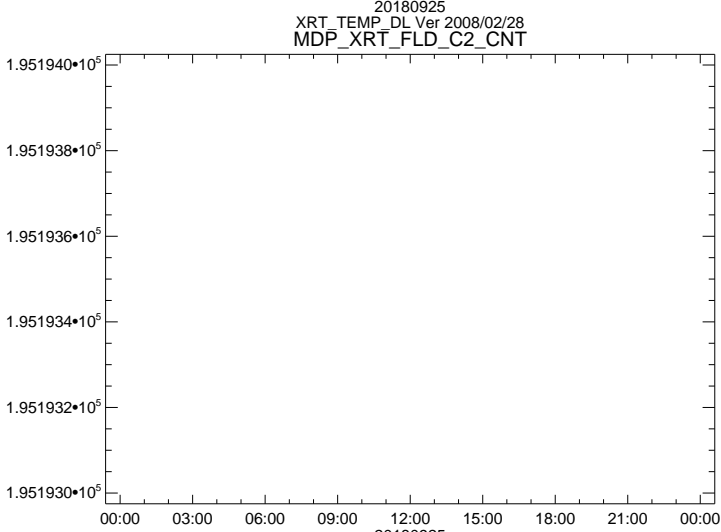
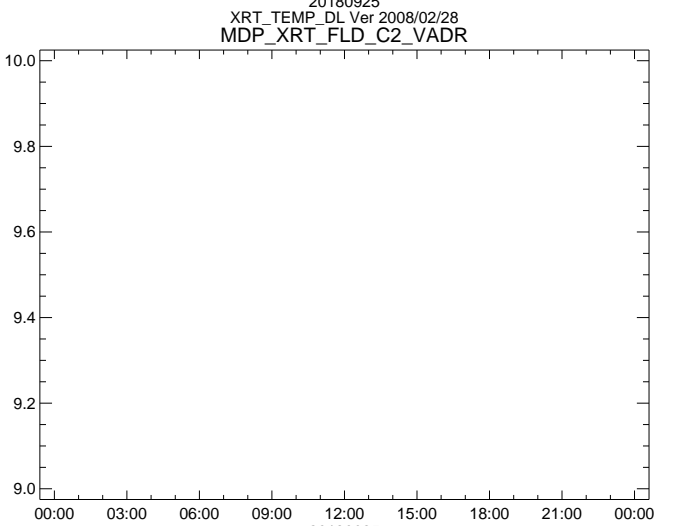
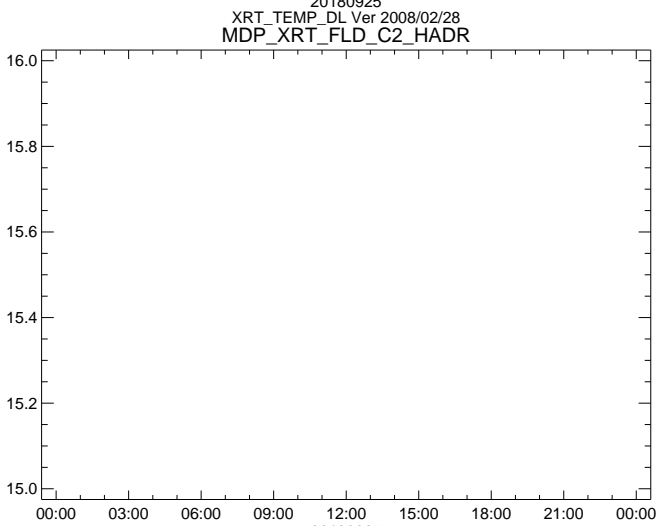
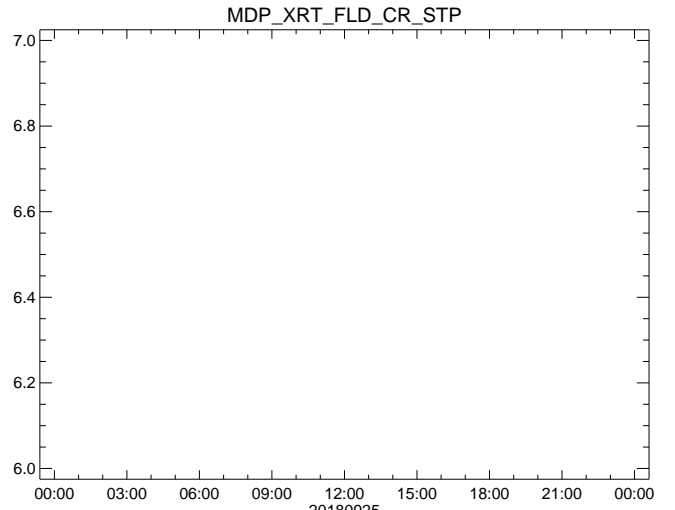
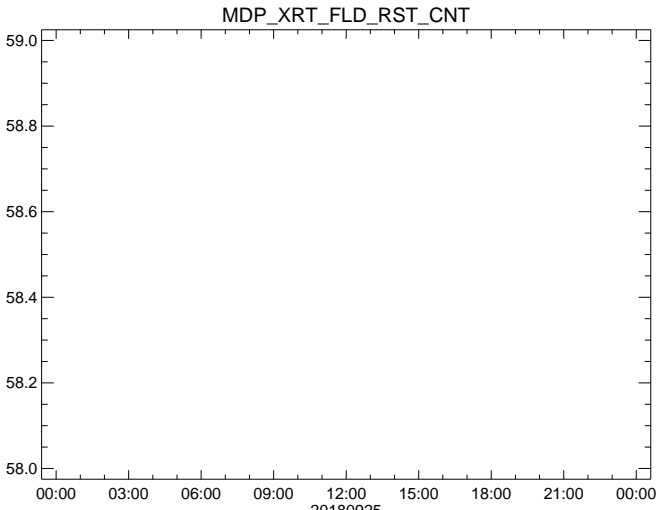


XRT_TEMP_DL Ver 2008/02/28
20180925
MDP_XRT_RD_TRM_TIM

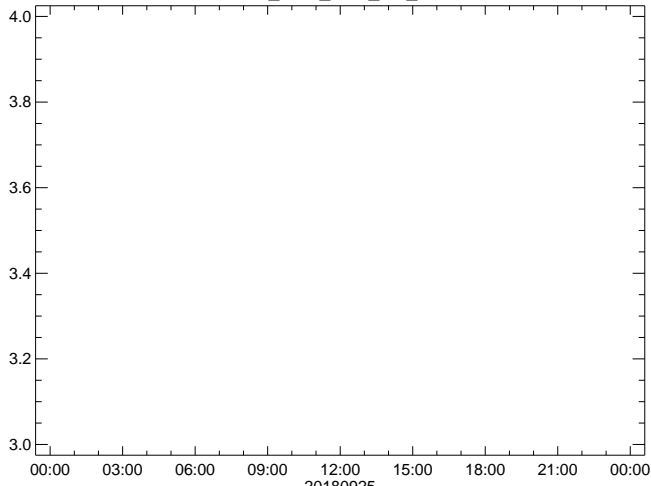


XRT_TEMP_DL Ver 2008/02/28
20180925

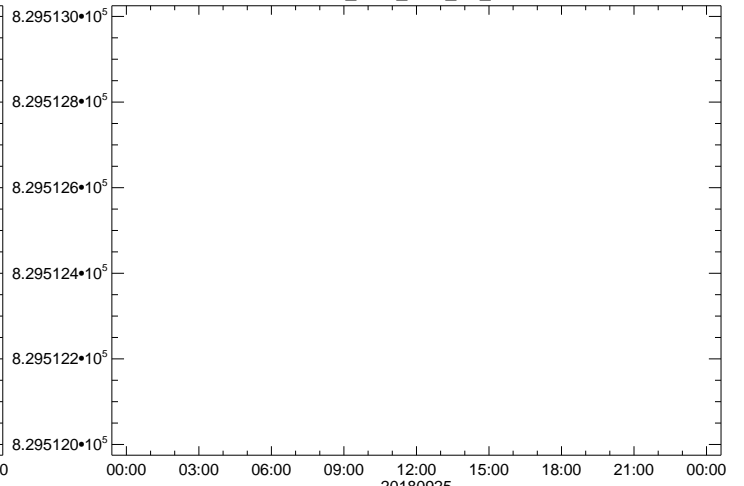
XRT_TEMP_DL Ver 2008/02/28
20180925



MDP_XRT_FLD_FN_STP

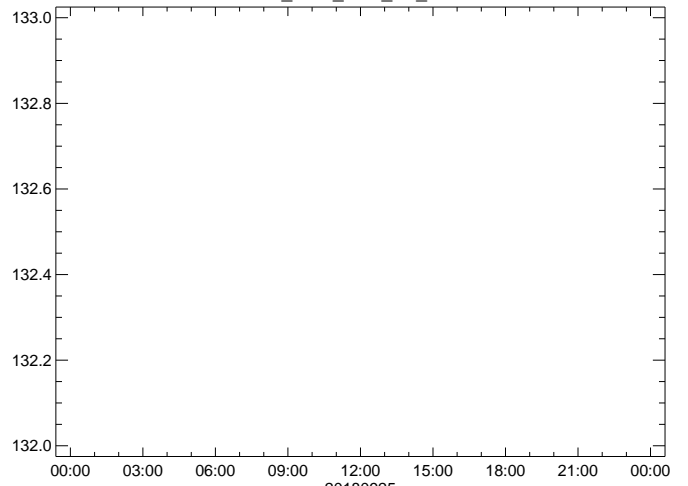
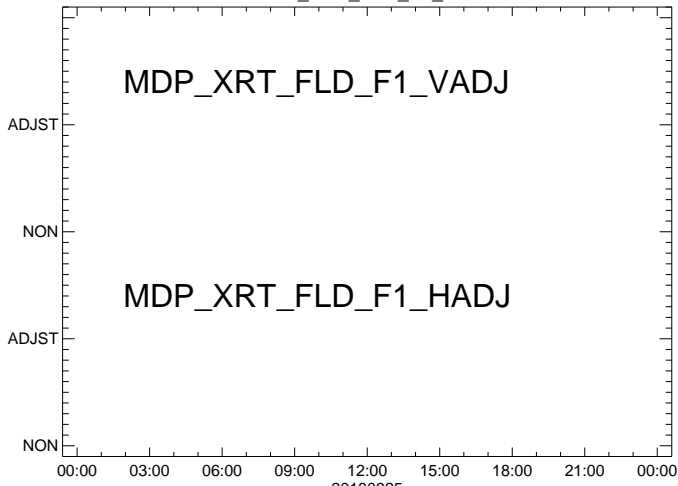


MDP_XRT_FLD_F1_CNT



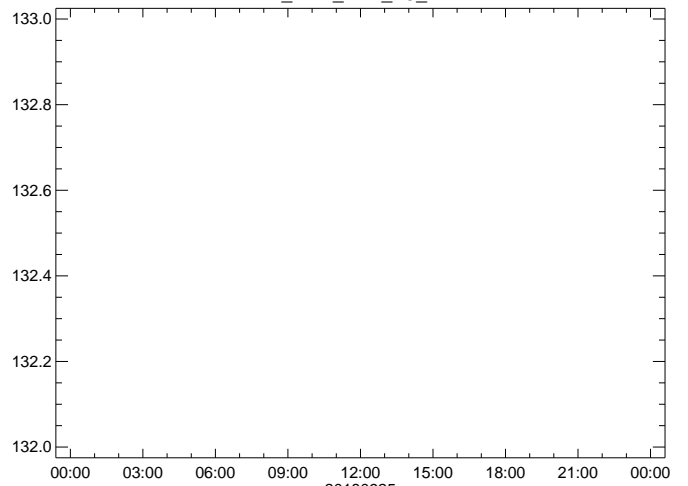
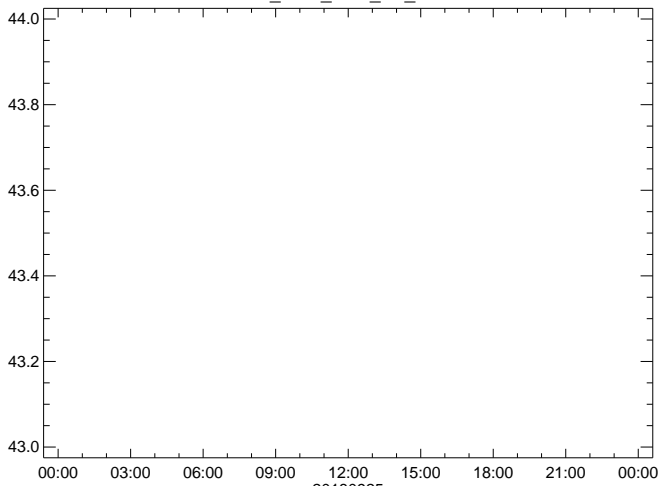
20180925
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_FLD_F1_ADJ

20180925
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_FLD_F2_HADR



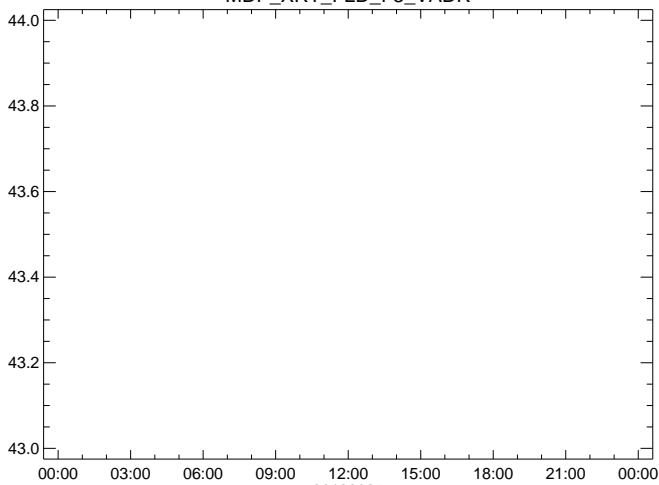
20180925
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_FLD_F2_VADR

20180925
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_FLD_F3_HADR



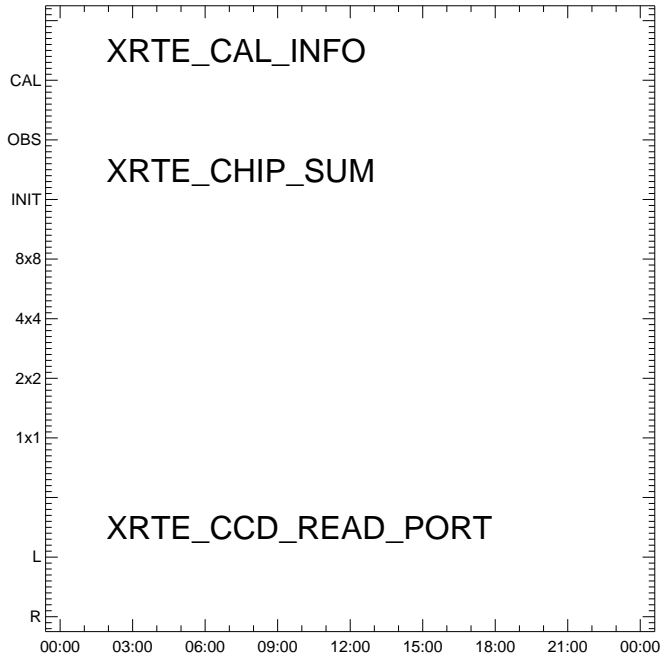
20180925
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_FLD_F3_VADR

20180925
XRT_TEMP_DL Ver 2008/02/28

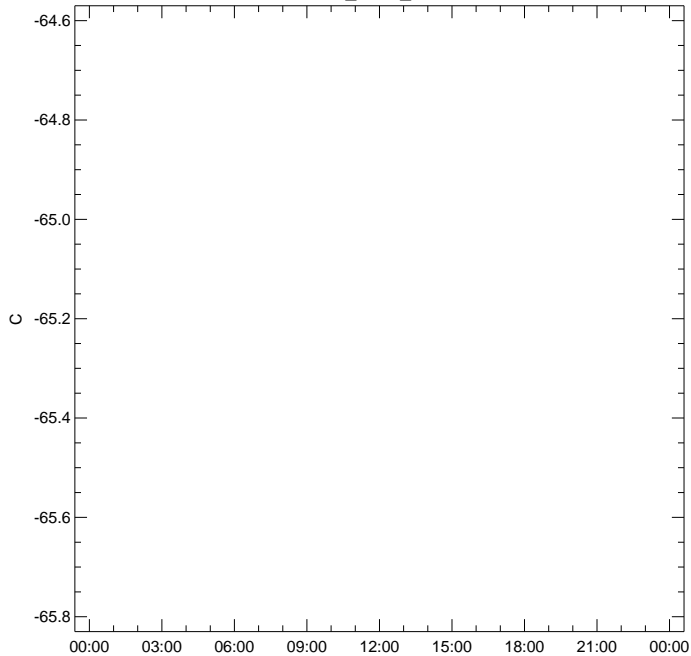


20180925
XRT_TEMP_DL Ver 2008/02/28

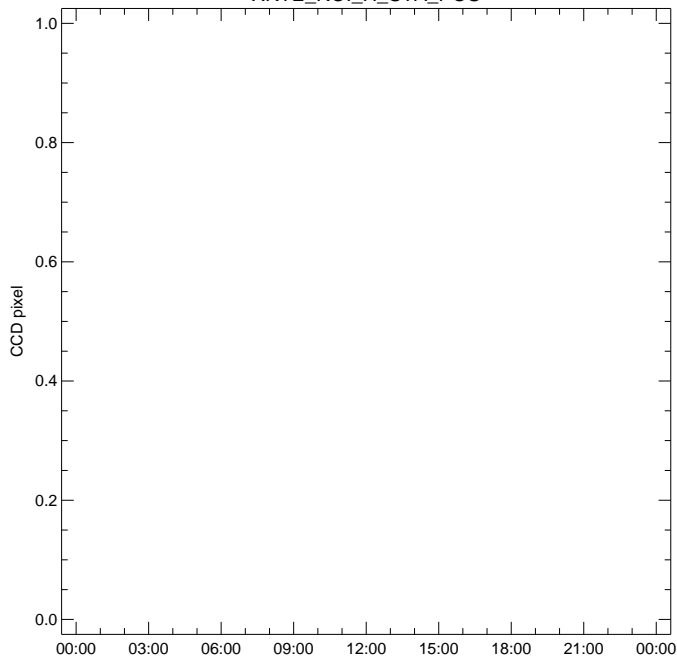
ON/OFF Statuses



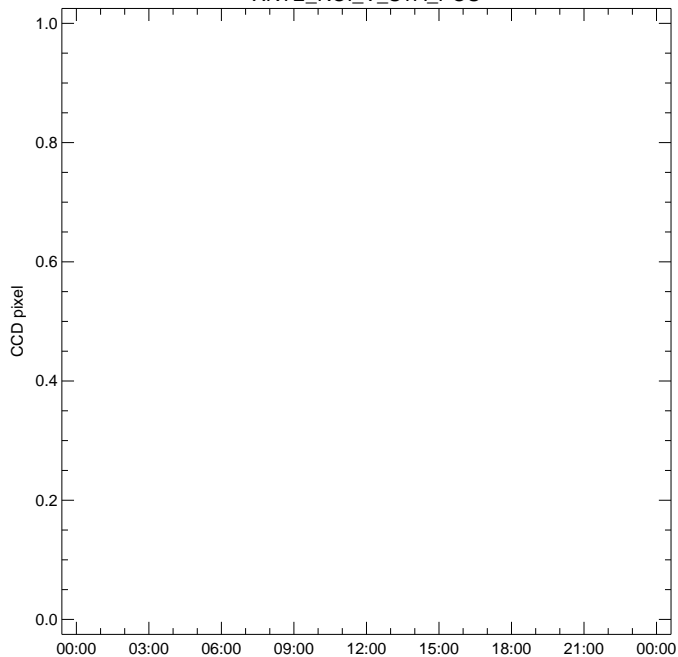
XRTE_CCD_TEMP



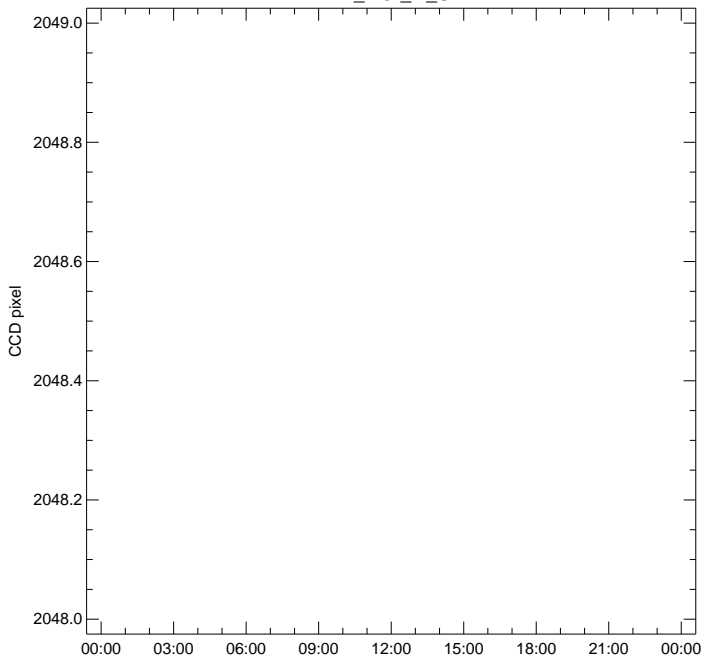
20180925
XRT_TEMP_DL Ver 2008/02/28
XRTE_ROI_H_STA_POS



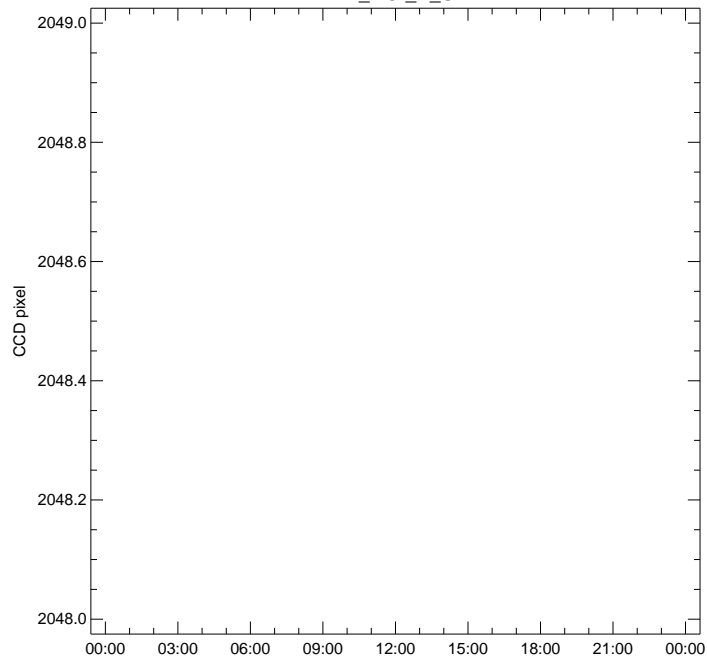
20180925
XRT_TEMP_DL Ver 2008/02/28
XRTE_ROI_V_STA_POS



20180925
XRT_TEMP_DL Ver 2008/02/28
XRTE_ROI_H_SIZE



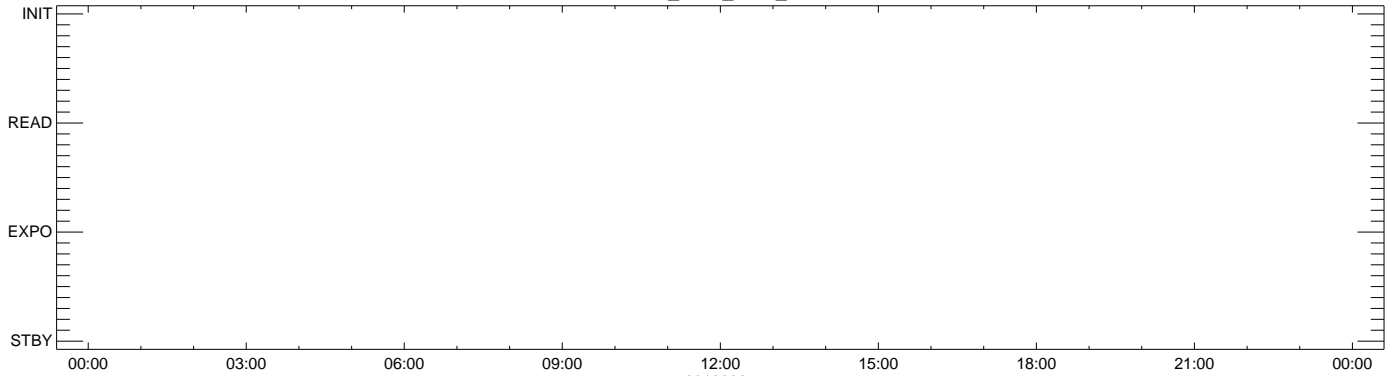
20180925
XRT_TEMP_DL Ver 2008/02/28
XRTE_ROI_V_SIZE



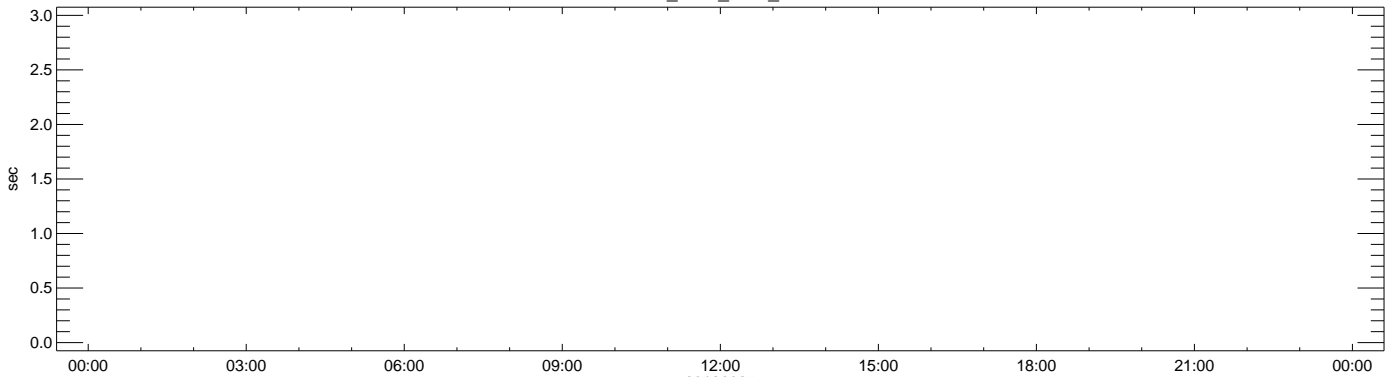
20180925
XRT_TEMP_DL Ver 2008/02/28

20180925
XRT_TEMP_DL Ver 2008/02/28

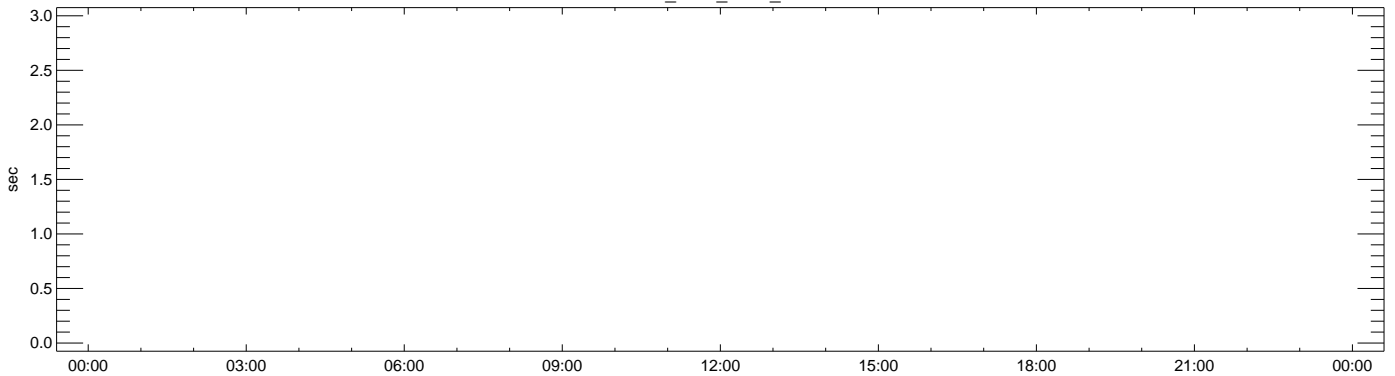
XRTE_CCD_OPE_STS



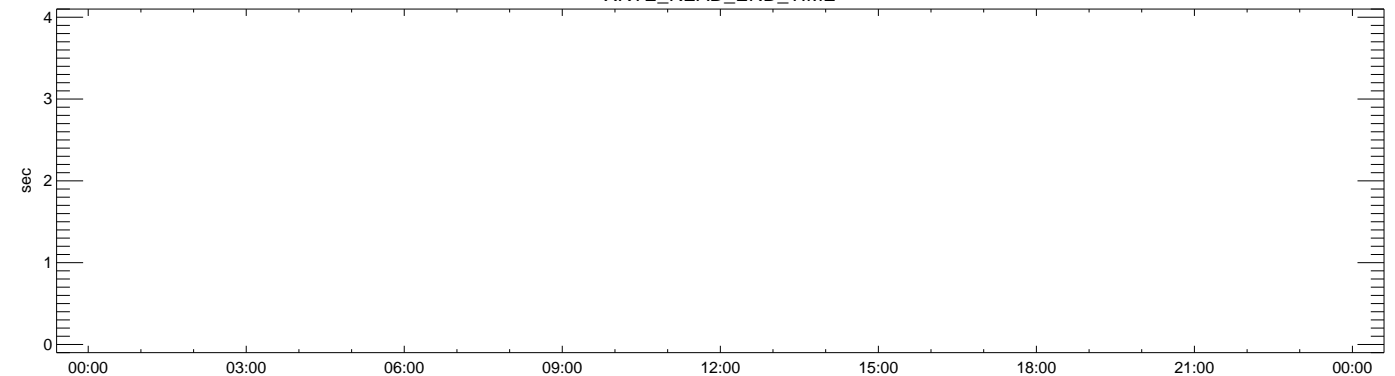
12:00
20180925
XRT_TEMP_DL Ver 2008/02/28
XRTE_EXP_STA_TIME



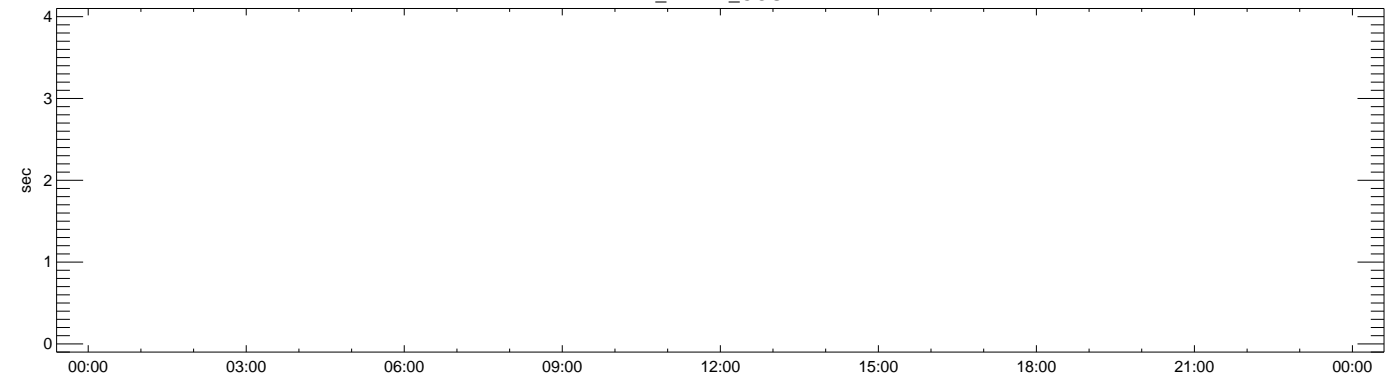
12:00
20180925
XRT_TEMP_DL Ver 2008/02/28
XRTE_EXP_END_TIME



12:00
20180925
XRT_TEMP_DL Ver 2008/02/28
XRTE_READ_END_TIME

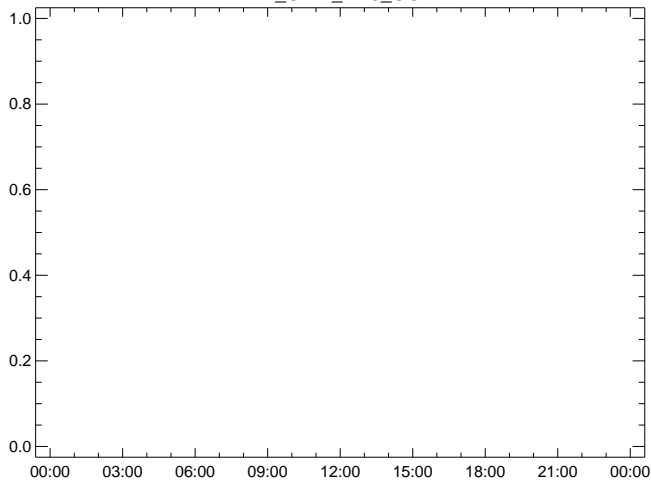


12:00
20180925
XRT_TEMP_DL Ver 2008/02/28
XRTE_TIMER_COUNTER

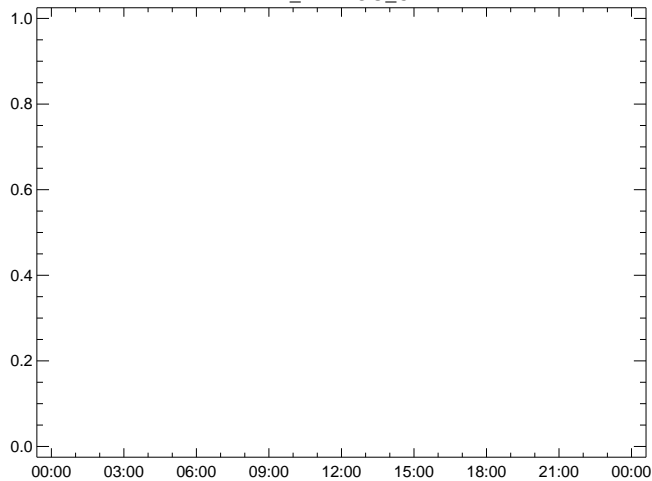


12:00
20180925
XRT_TEMP_DL Ver 2008/02/28

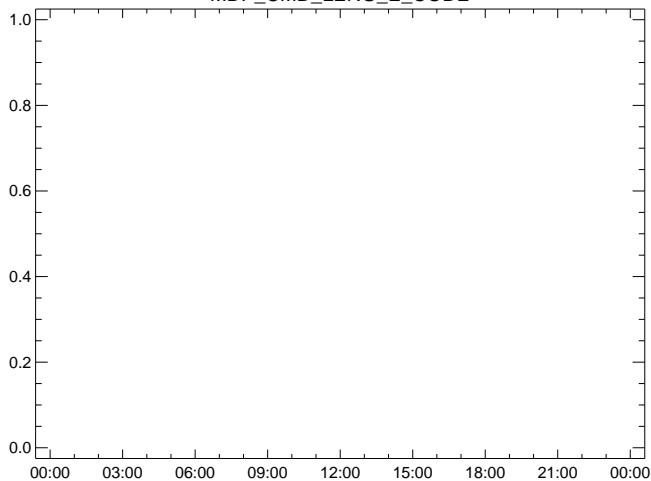
MDP_CMD_REJ_CODE



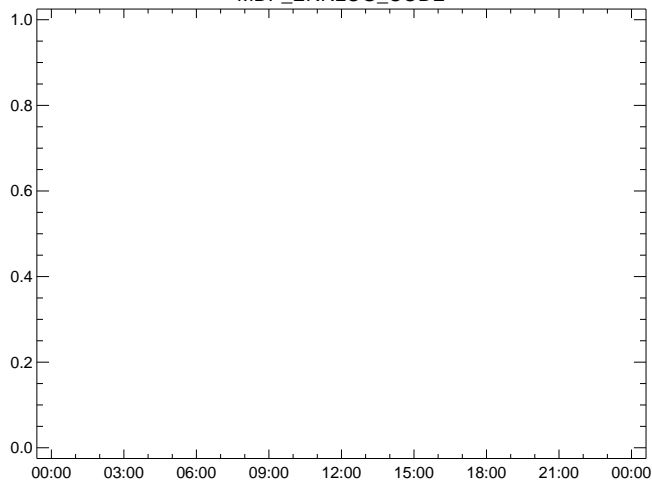
MDP_ERRLOG_CNT



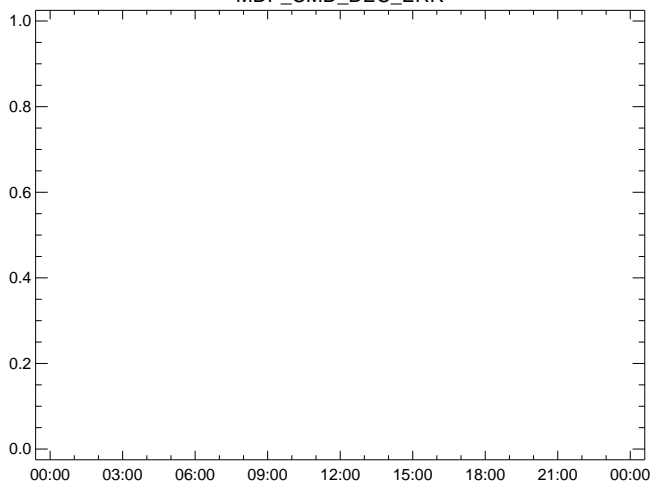
XRT_TEMP_DL Ver 2008/02/28
20180925
MDP_CMD LENG E_CODE



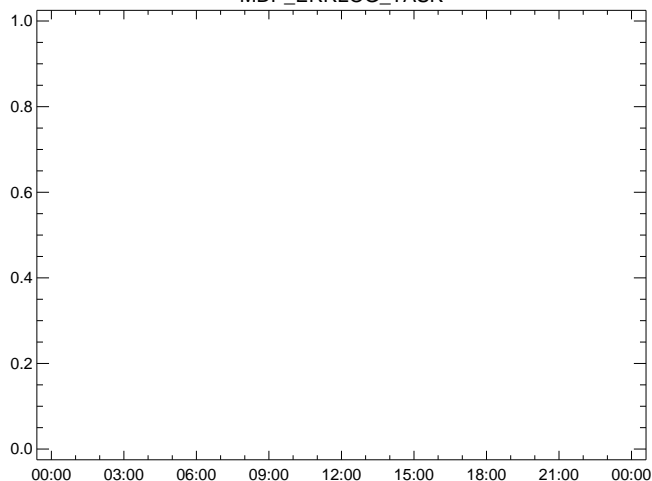
XRT_TEMP_DL Ver 2008/02/28
20180925
MDP_ERRLOG_CODE



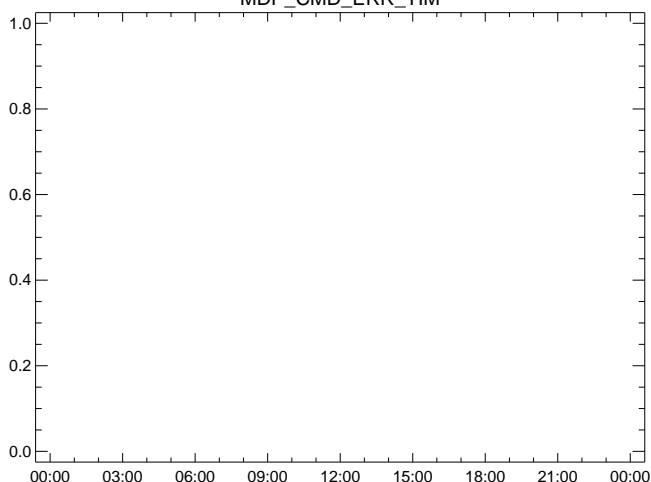
XRT_TEMP_DL Ver 2008/02/28
20180925
MDP_CMD DEC_ERR



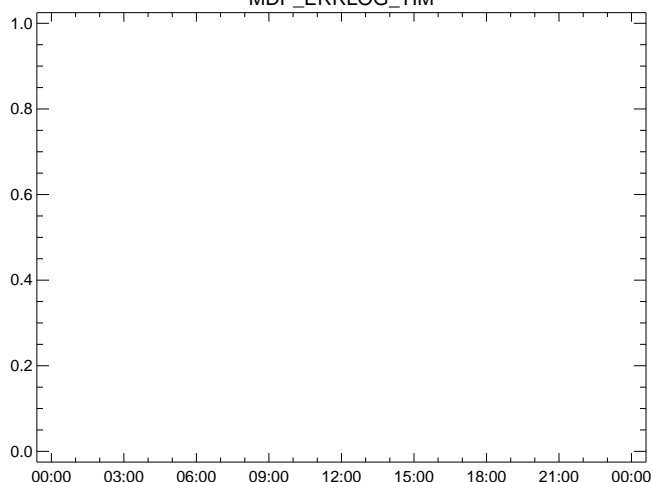
XRT_TEMP_DL Ver 2008/02/28
20180925
MDP_ERRLOG_TASK



XRT_TEMP_DL Ver 2008/02/28
20180925
MDP_CMD_ERR_TIM



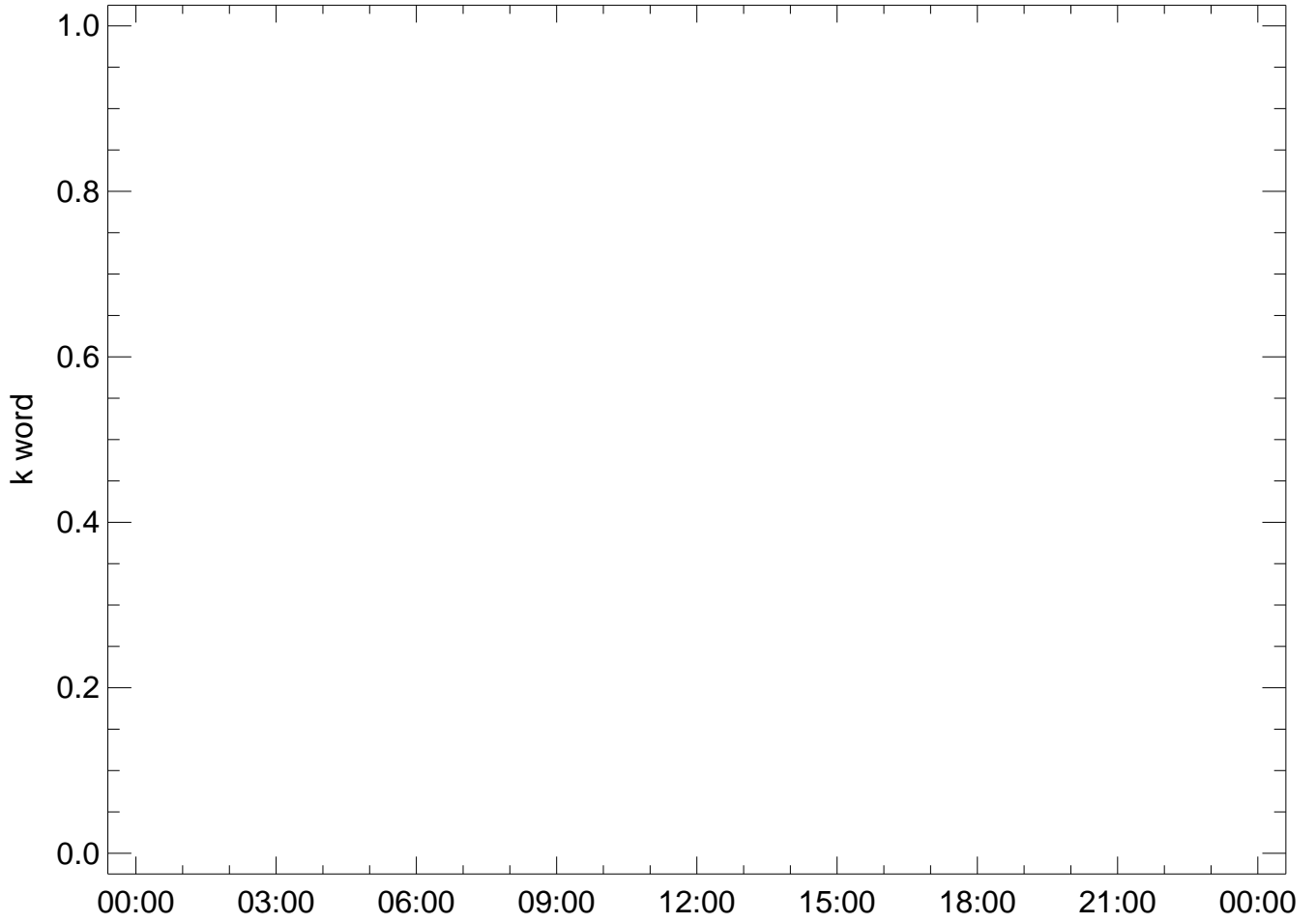
XRT_TEMP_DL Ver 2008/02/28
20180925
MDP_ERRLOG_TIM



XRT_TEMP_DL Ver 2008/02/28

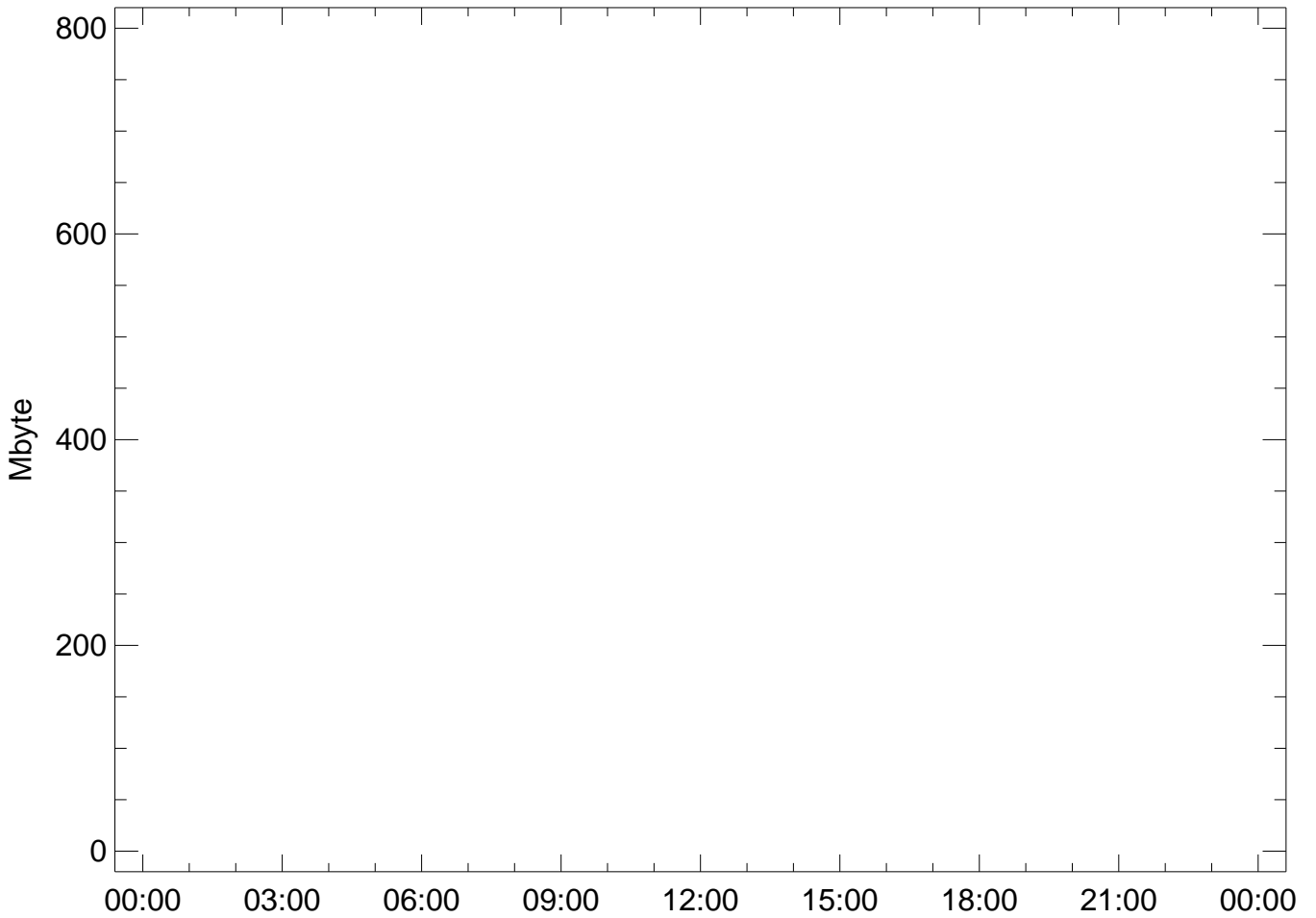
XRT_TEMP_DL Ver 2008/02/28

MDP_PST_CMP_XRT_ACM



20180925

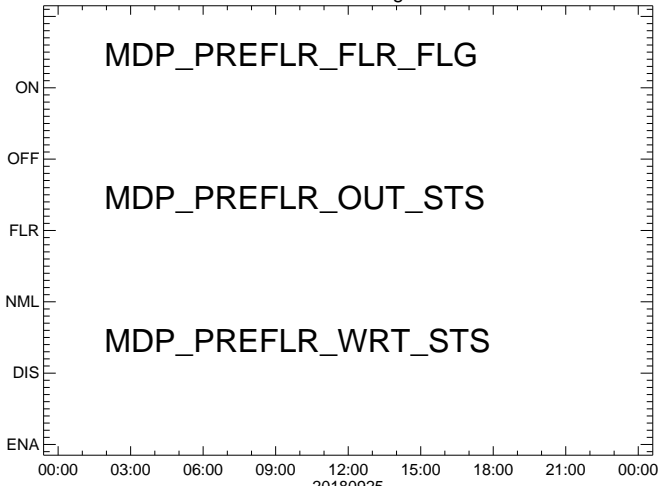
XRT_TEMP_DL Ver 2008/02/28 MDP_XRT_TLM_LIM & CNT



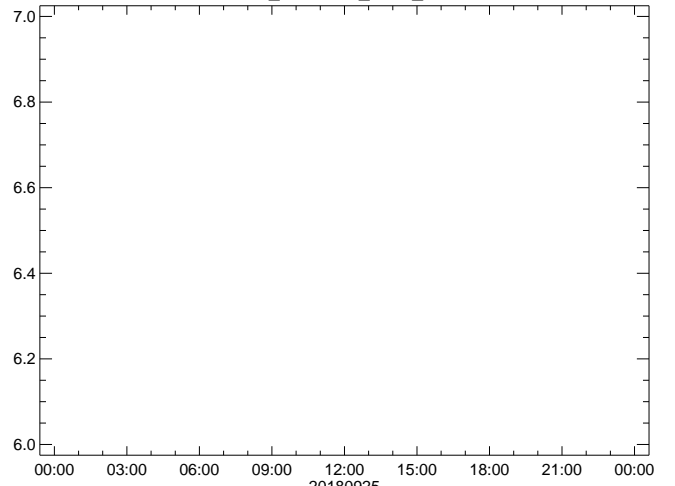
20180925

XRT_TEMP_DL Ver 2008/02/28

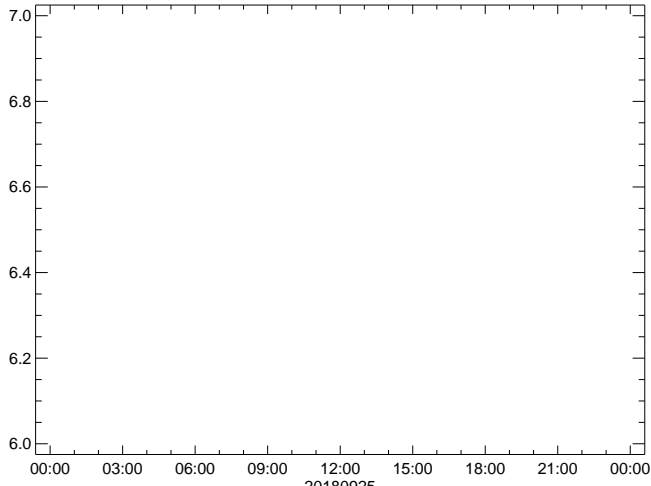
PreFlare Flags



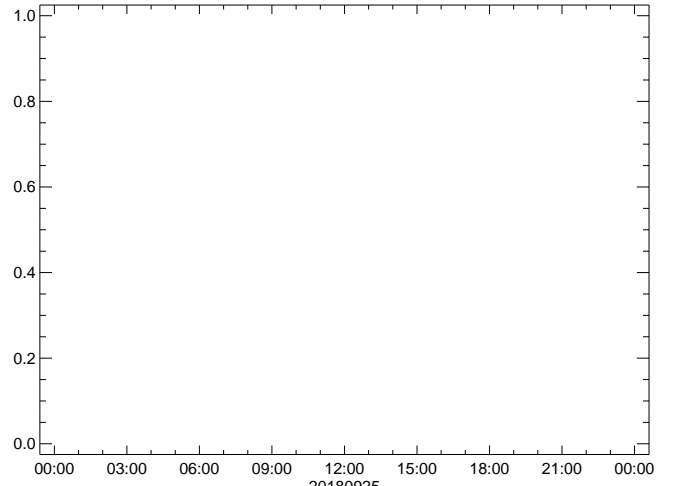
MDP_PREFLR_OUT_CNT



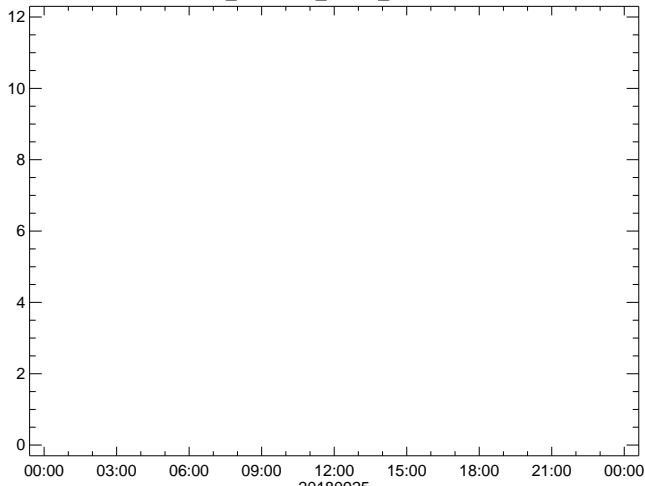
MDP_PREFLR_STP_CNT



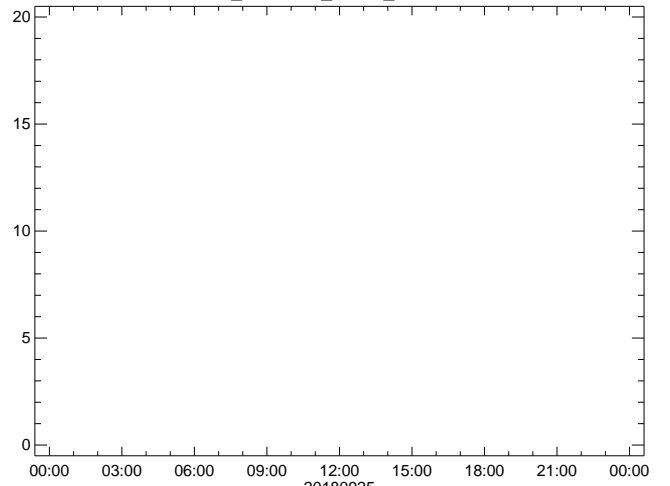
MDP_PREFLR_RST_CNT



MDP_PREFLR_BUF1_CNT & WP



MDP_PREFLR_BUF2_CNT & WP



MDP_PREFLR_BUF3_CNT & WP

