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VL Shutter Statuses

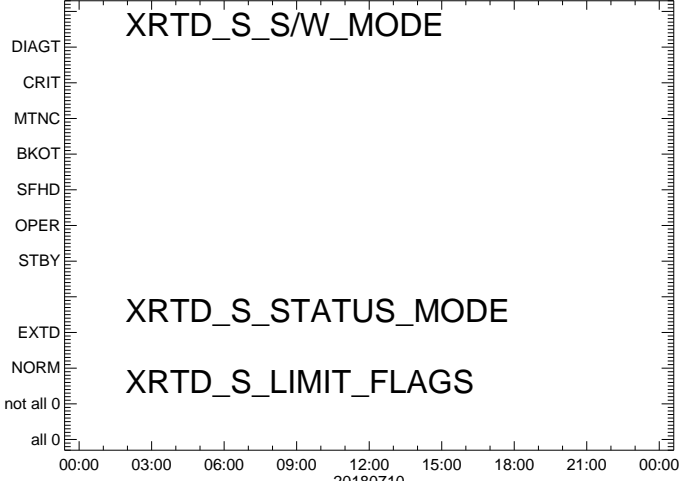
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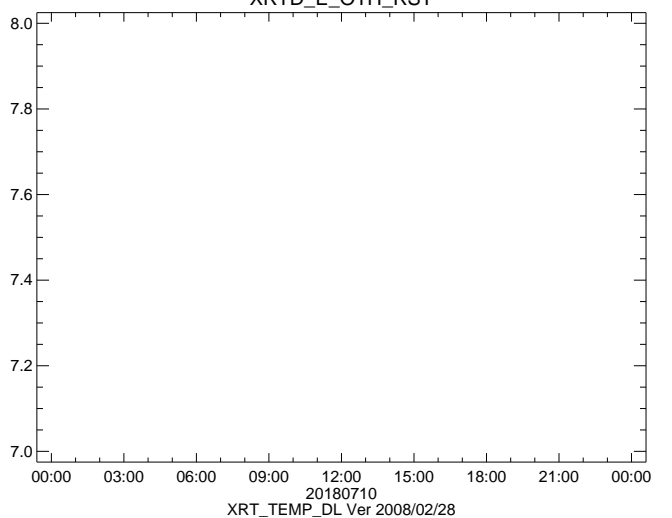
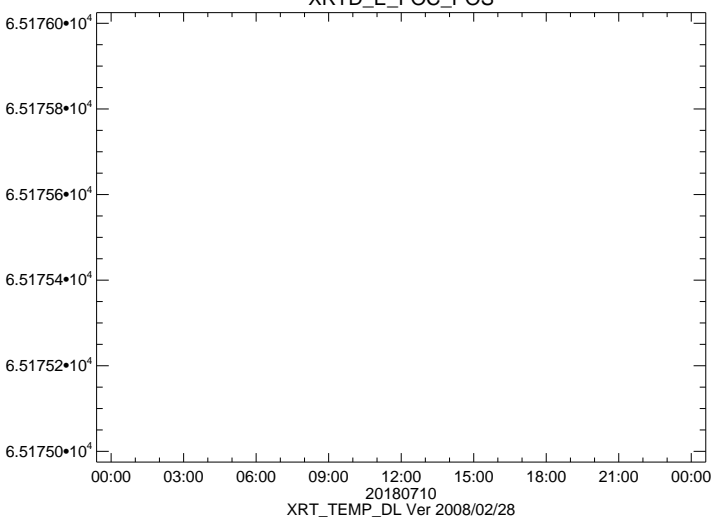
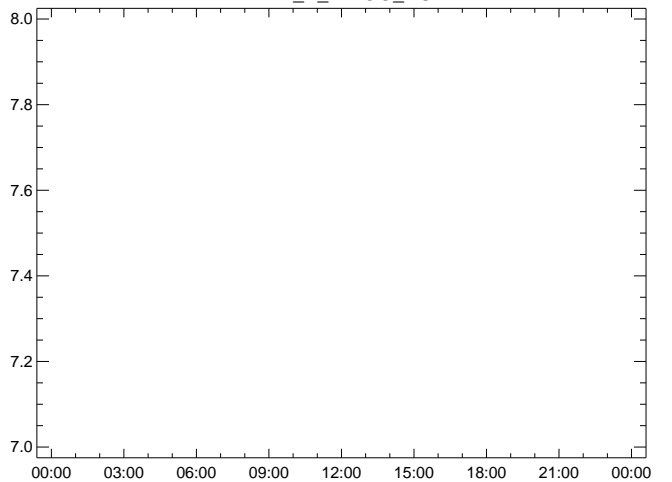
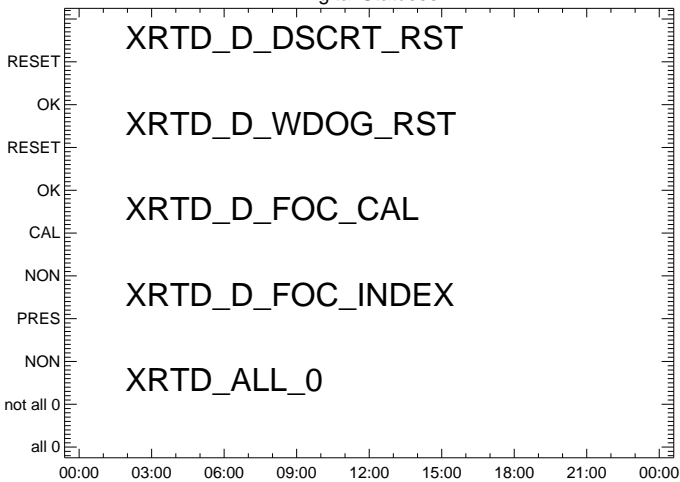
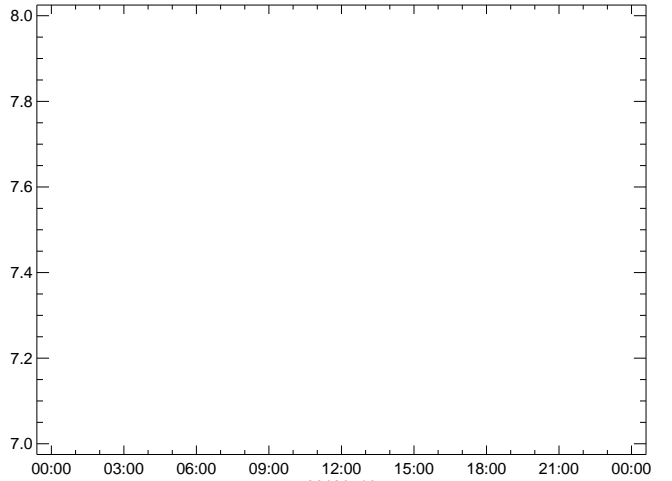
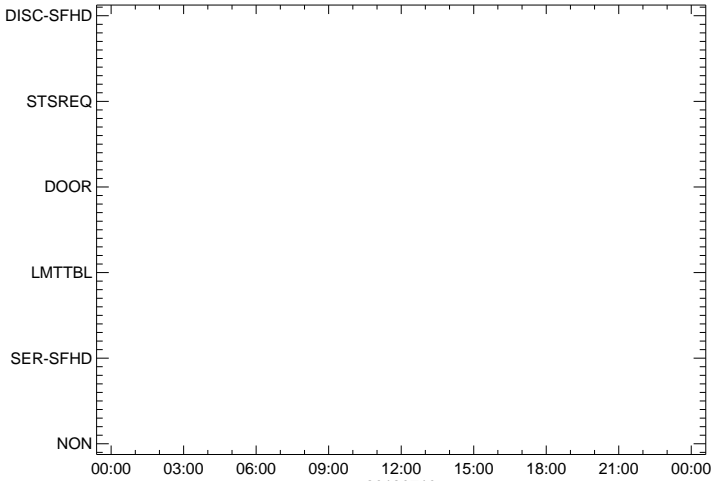
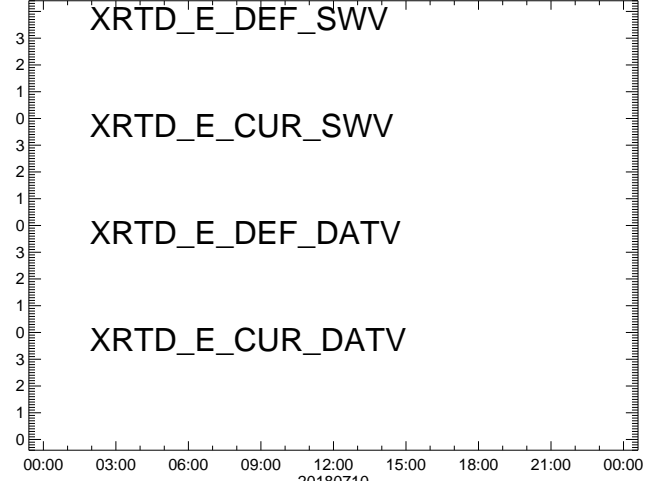
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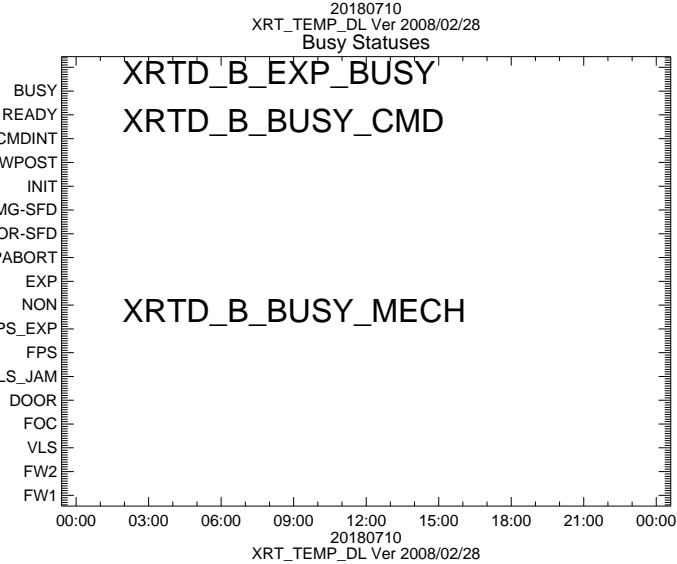
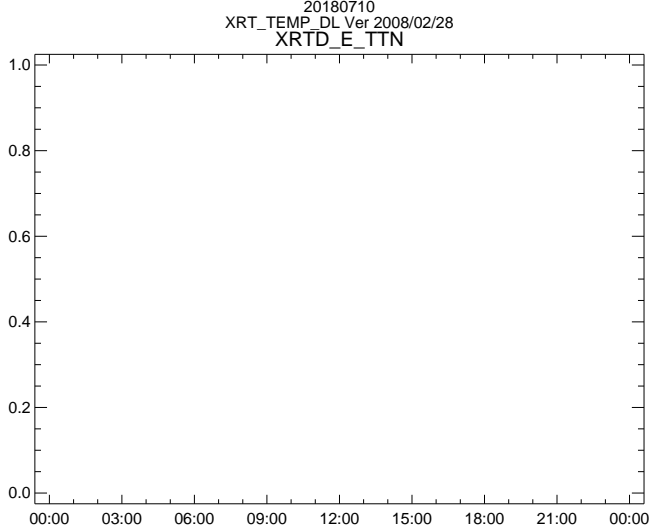
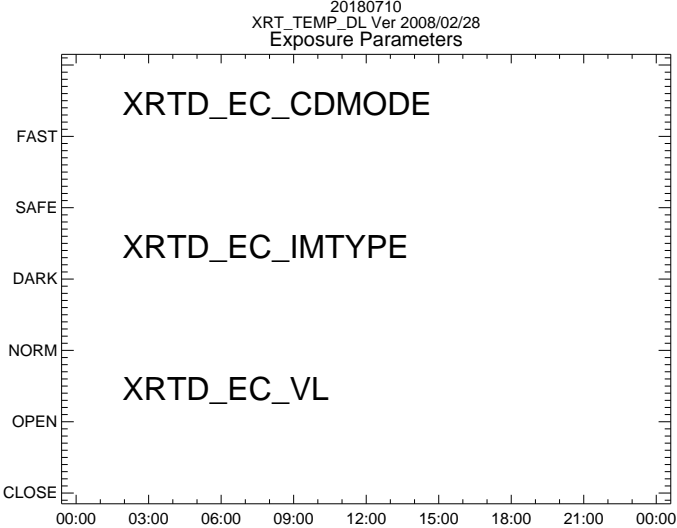
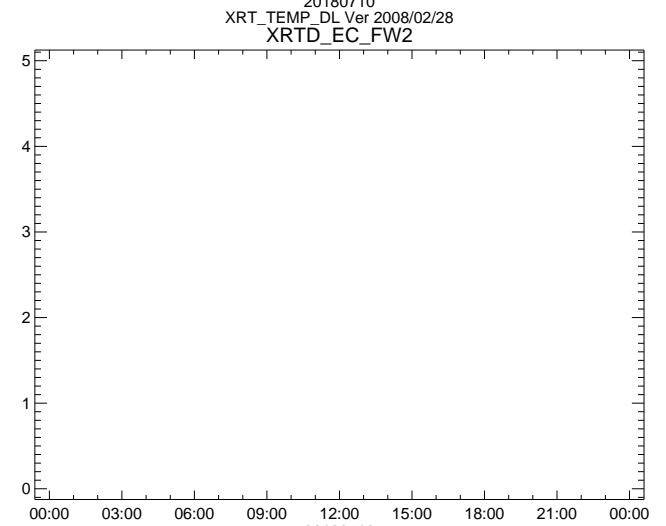
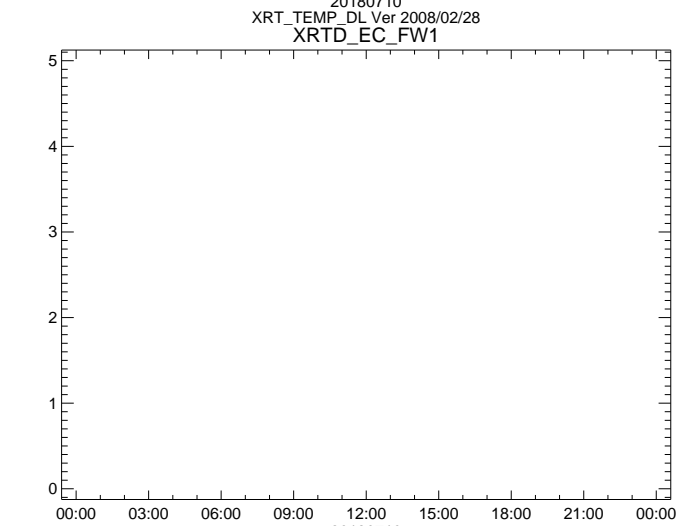
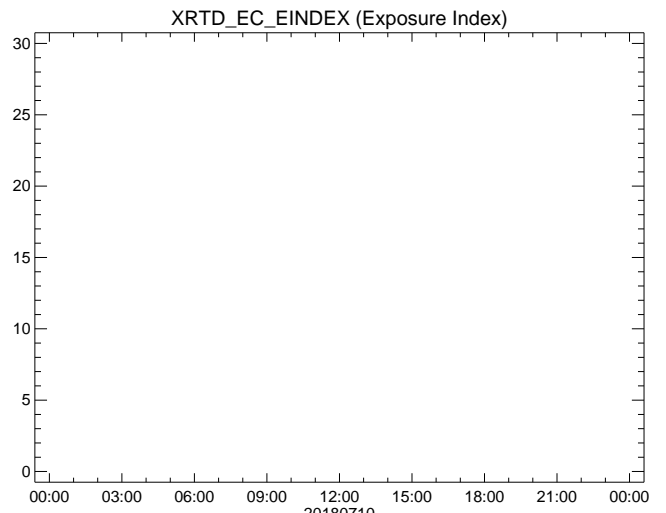
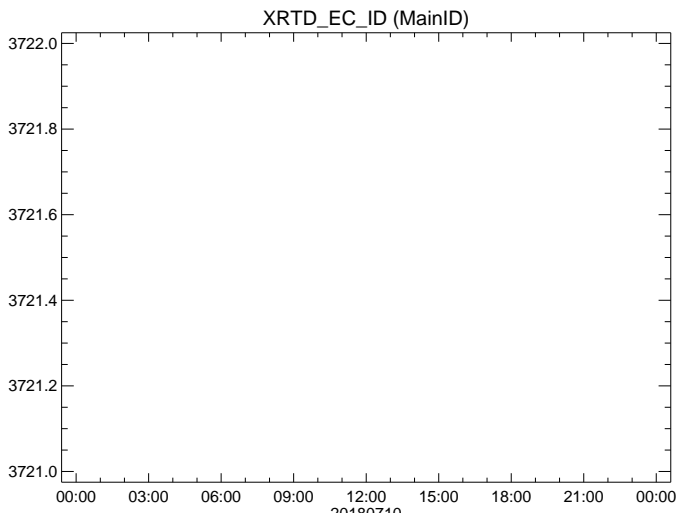
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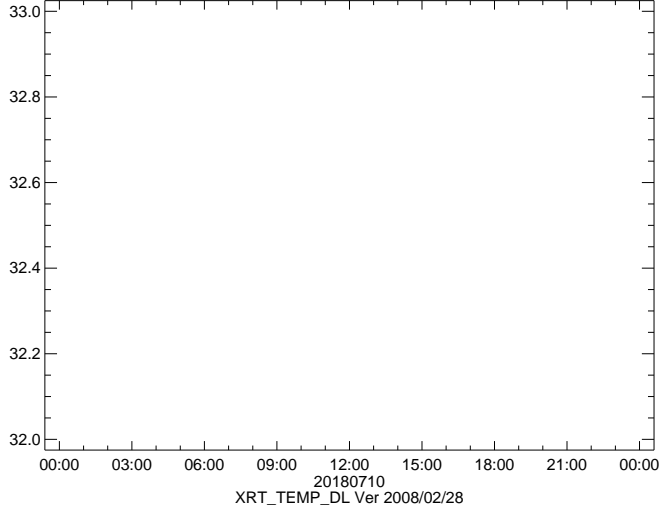
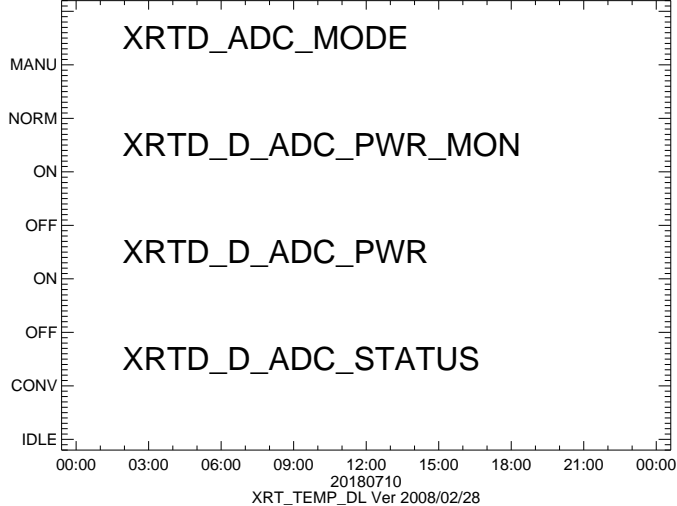
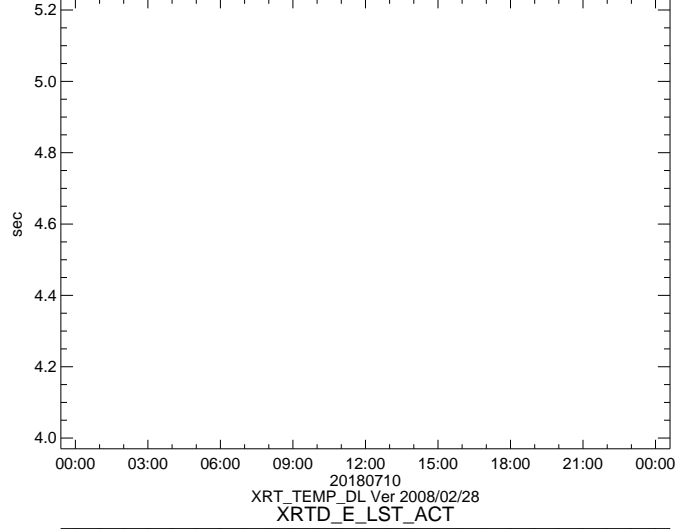
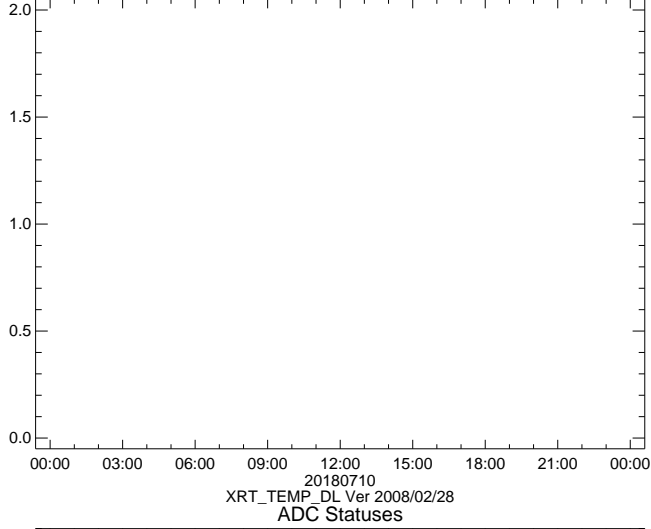
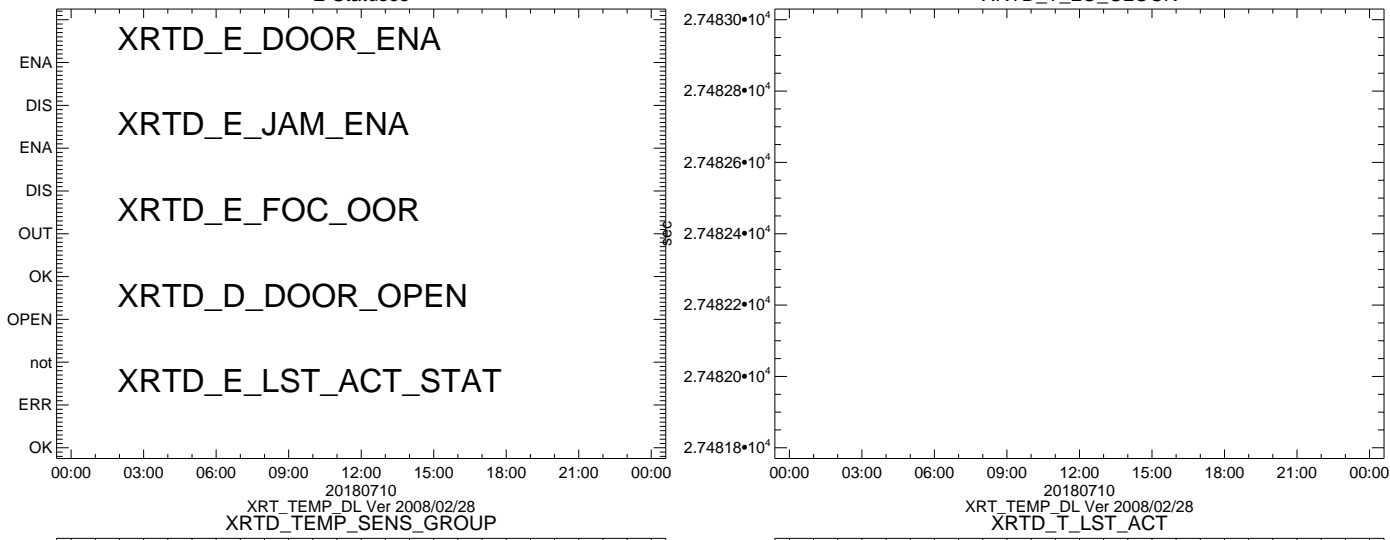
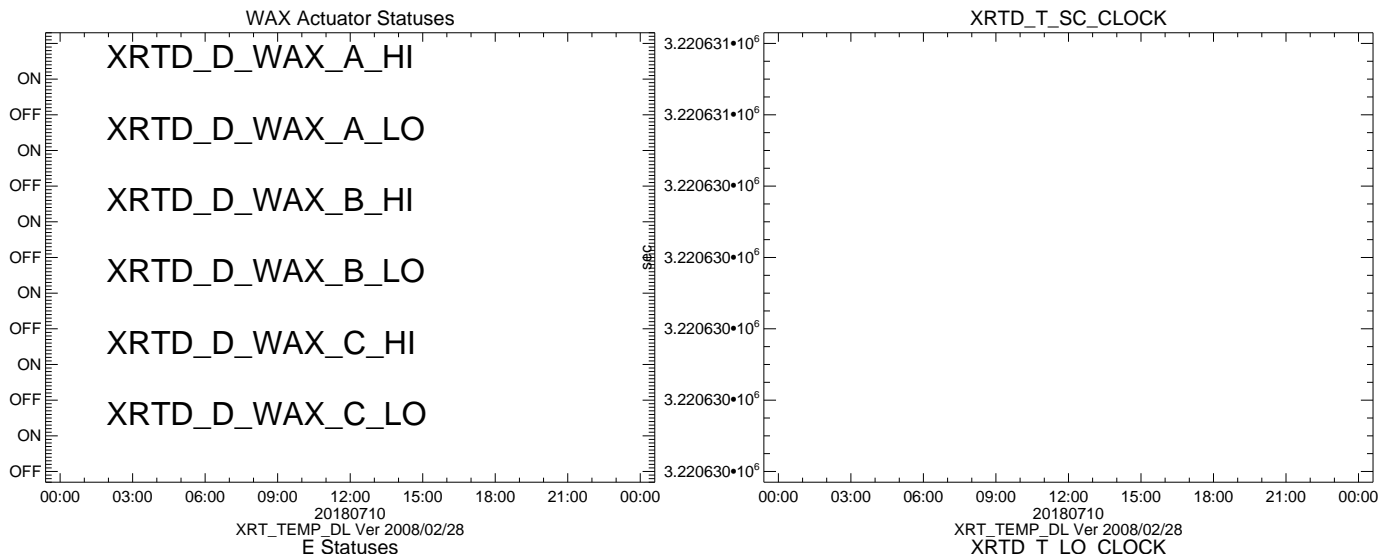
Software Statuses

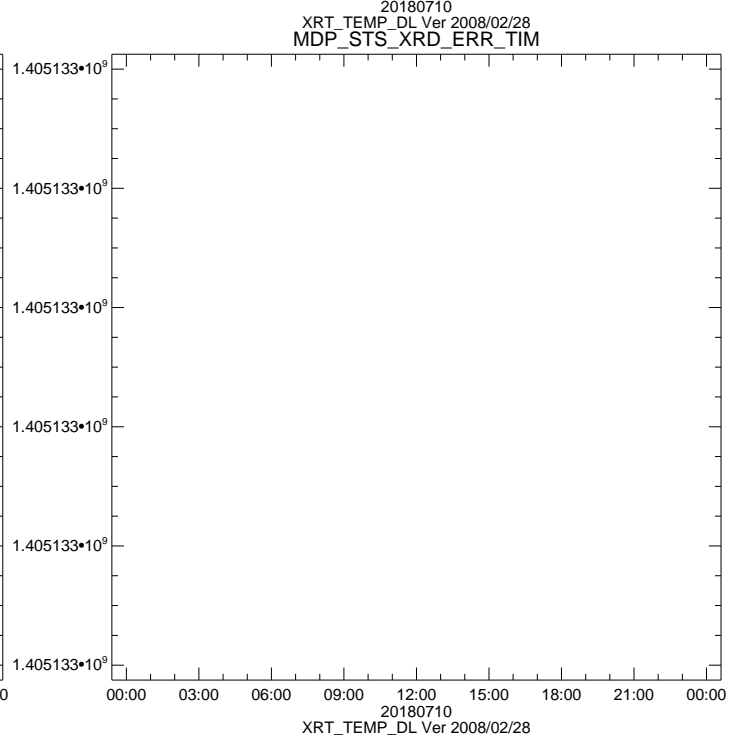
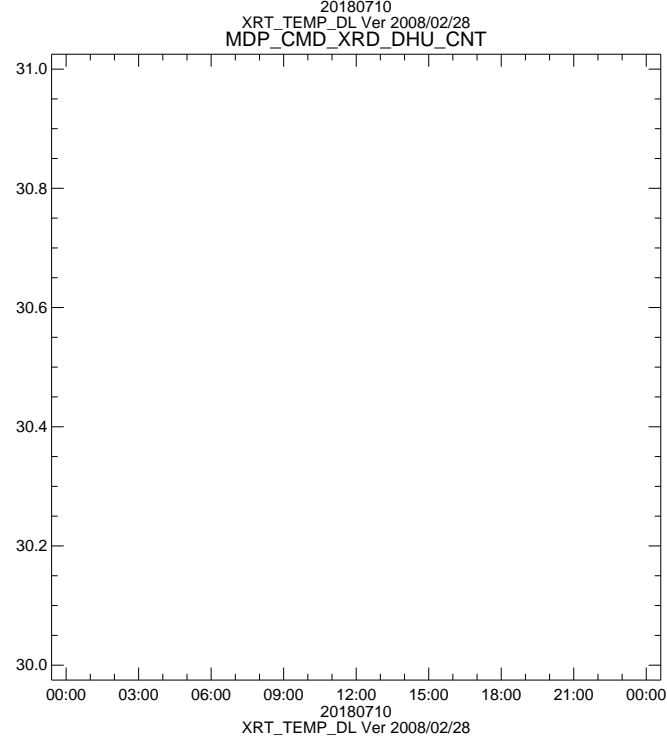
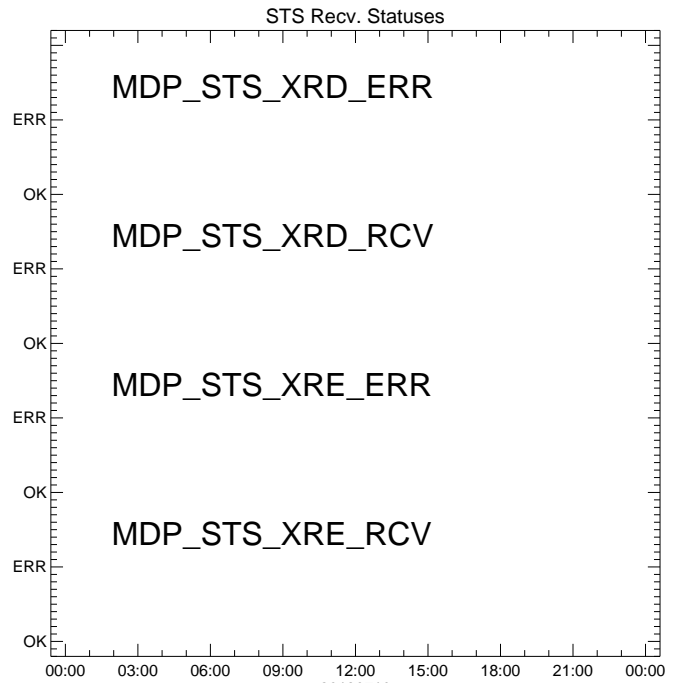
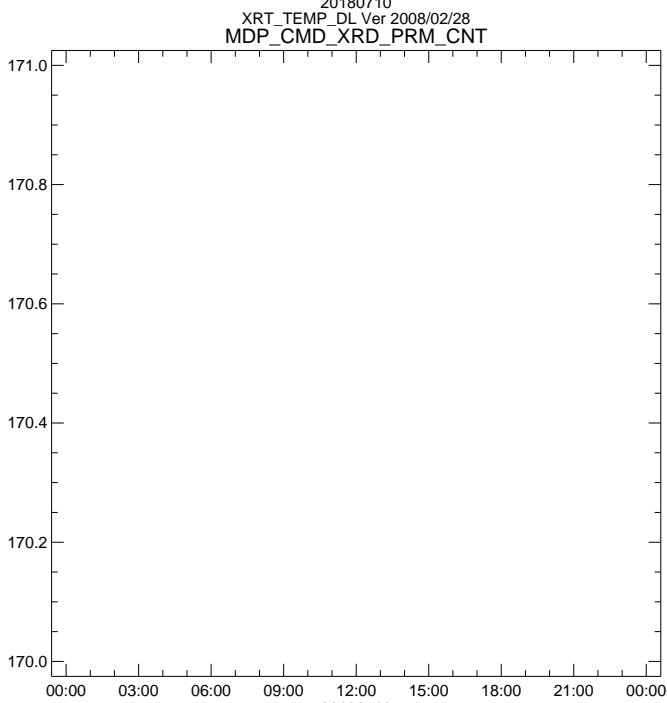
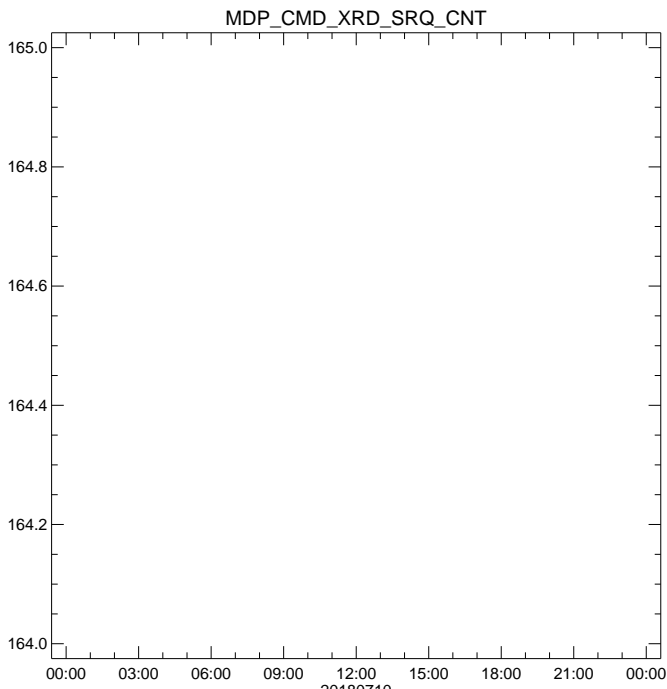


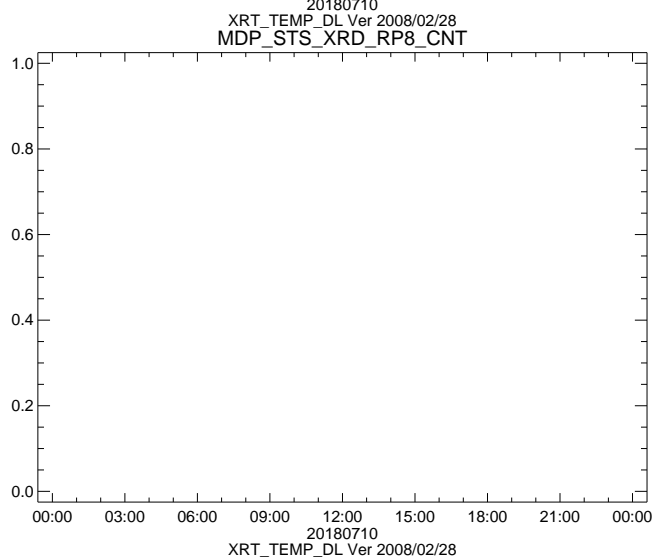
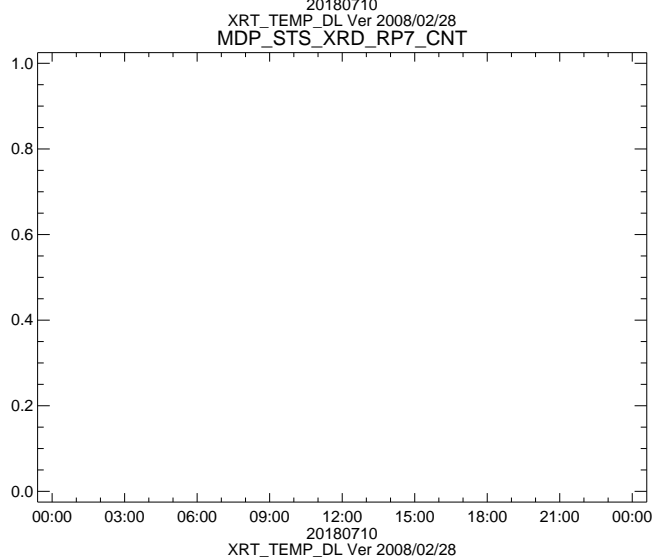
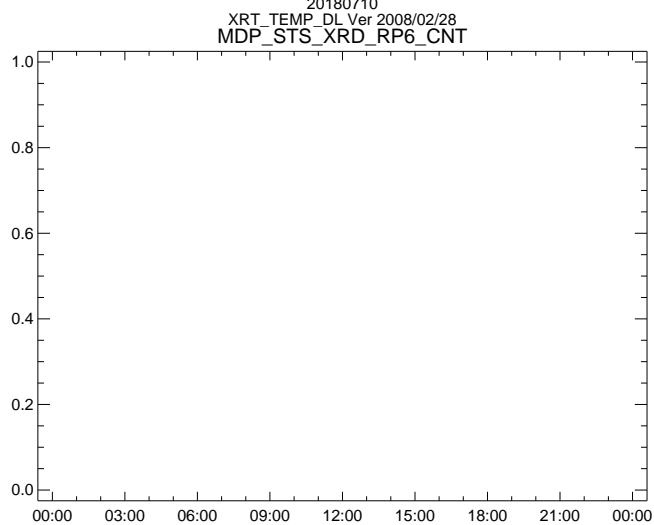
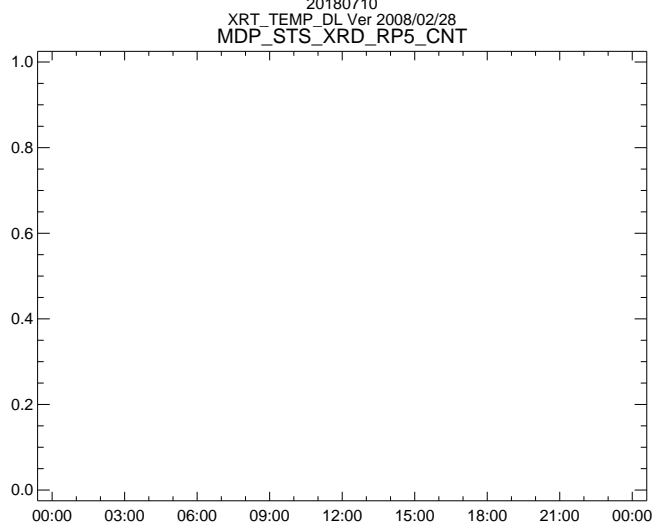
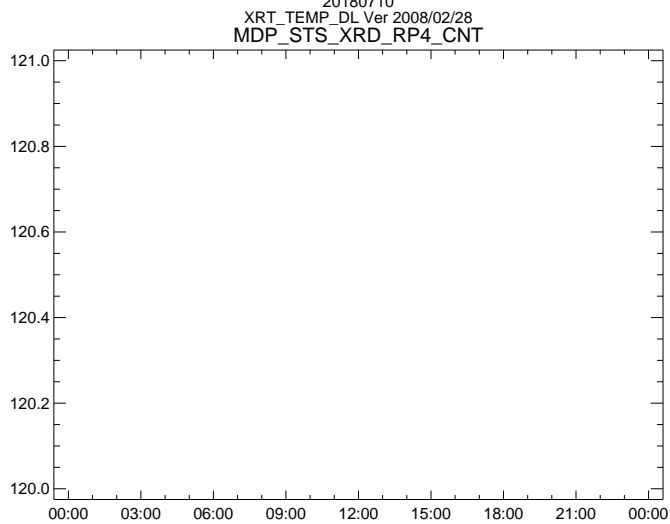
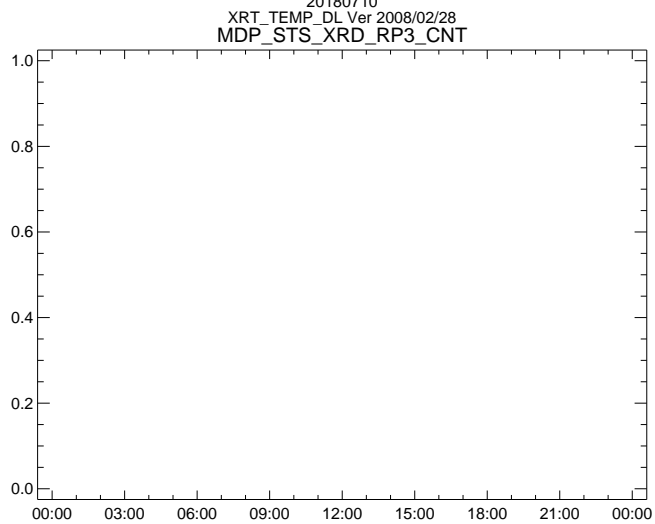
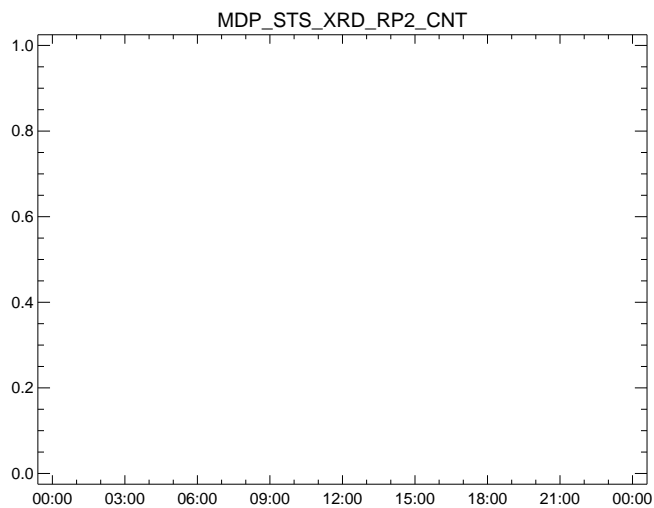
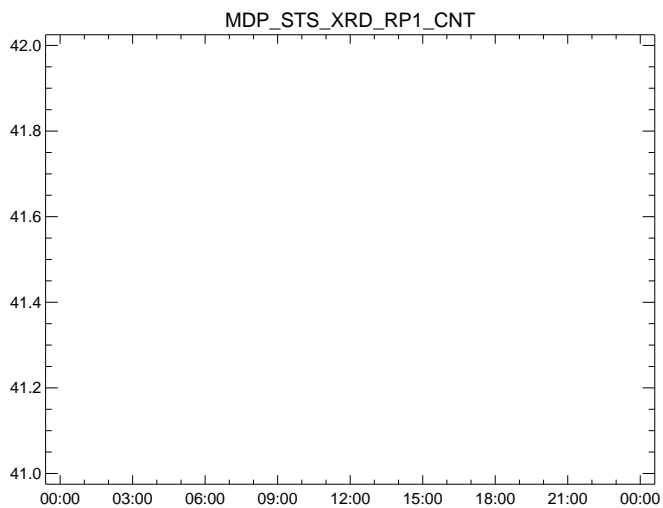
SW/DATA Version

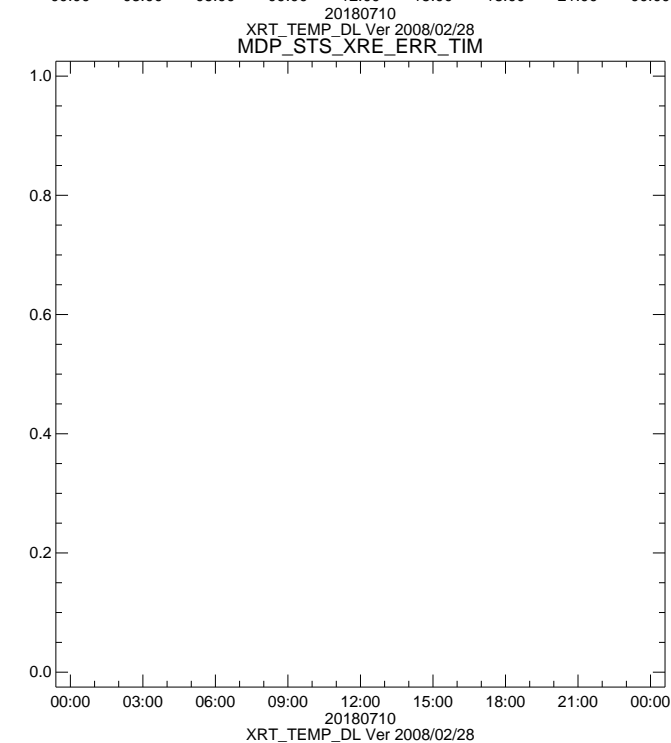
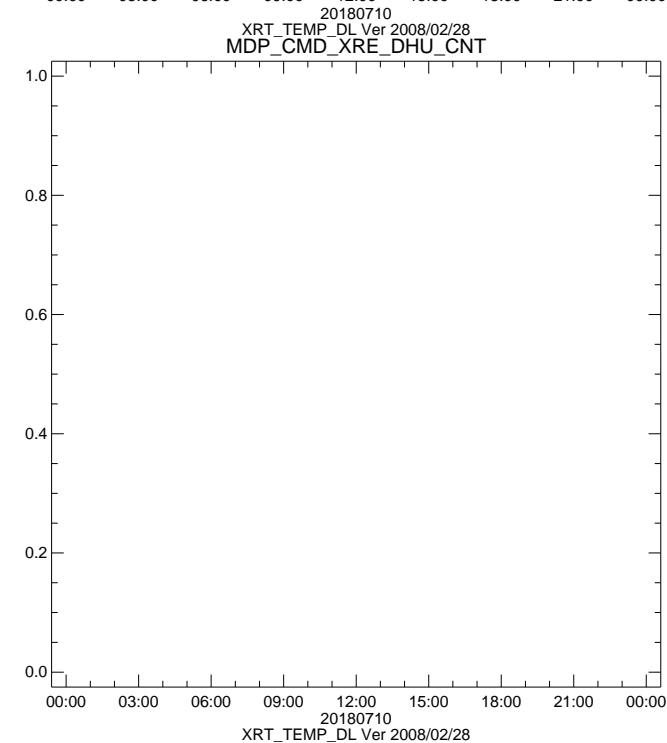
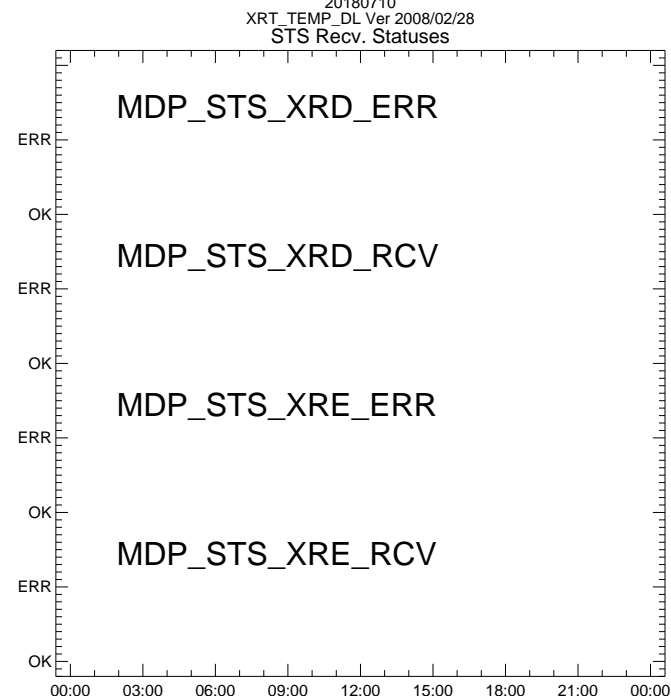
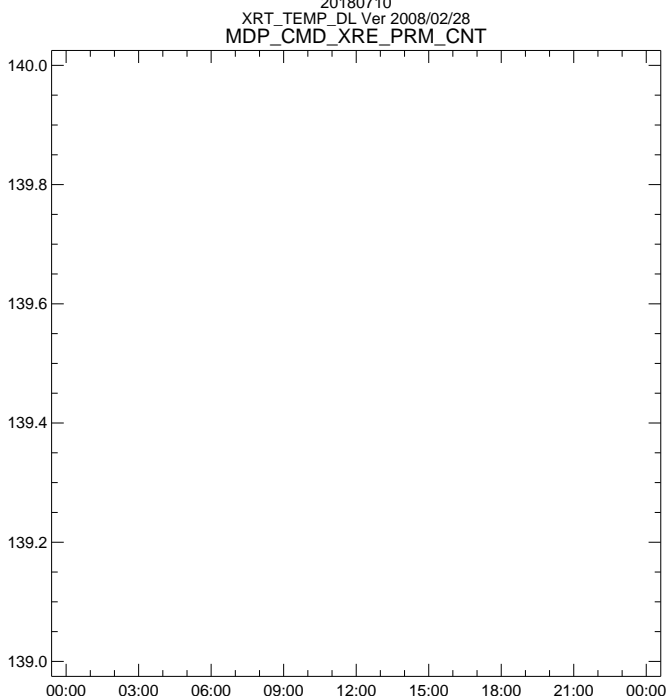
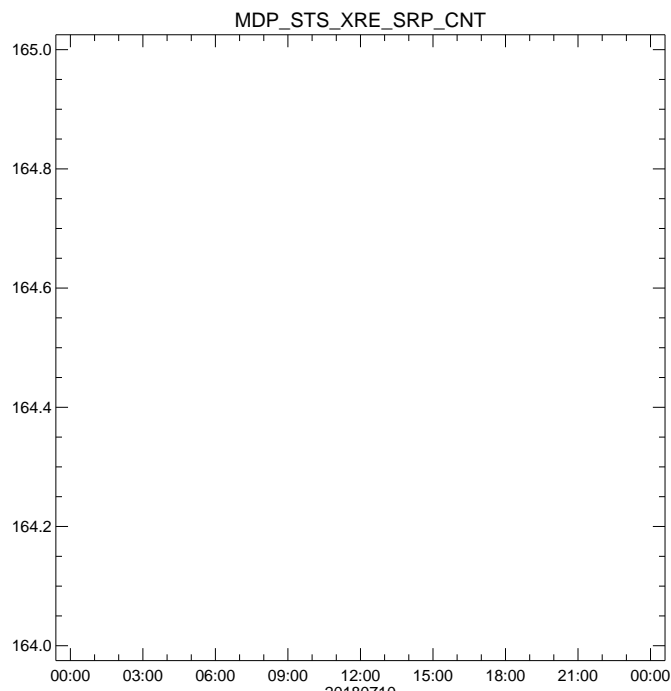
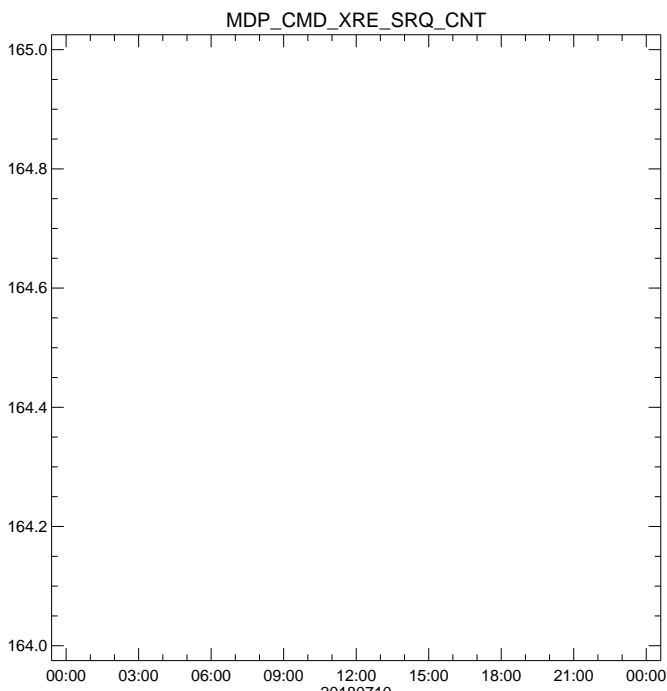


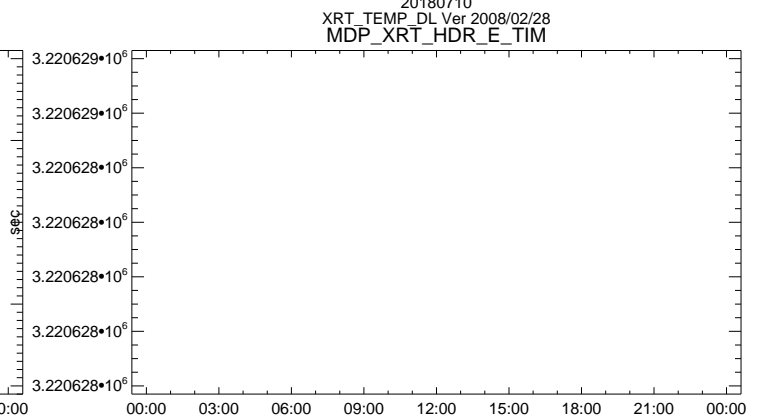
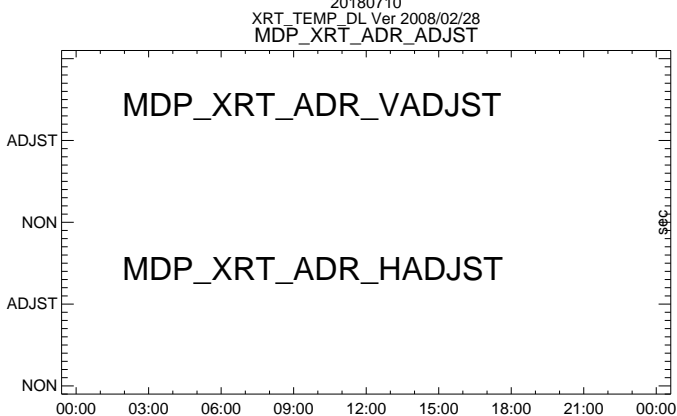
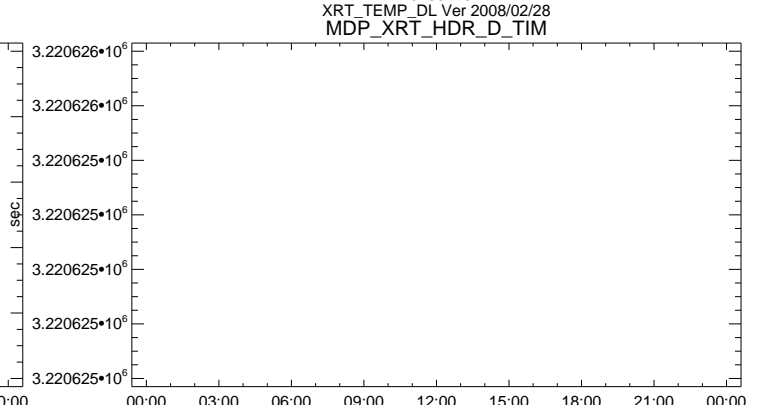
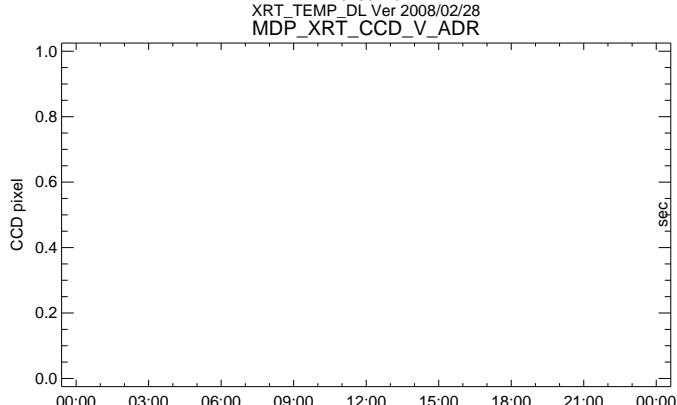
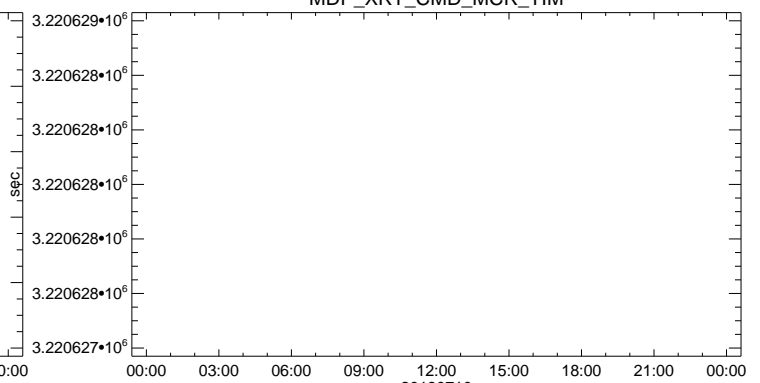
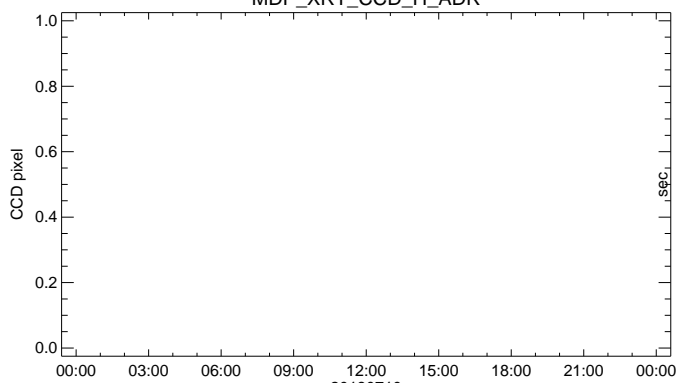
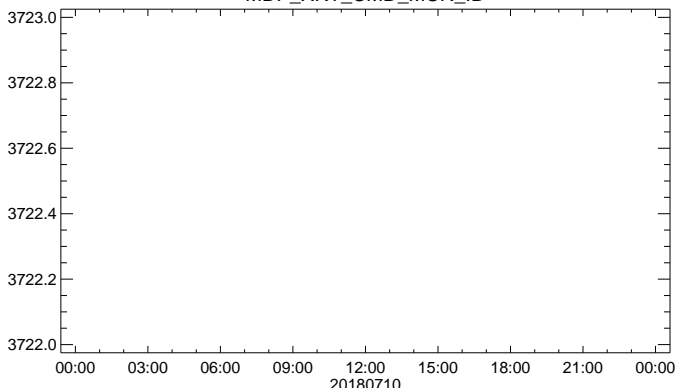
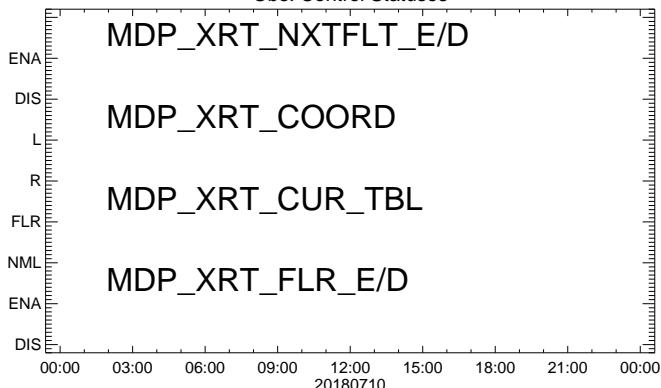
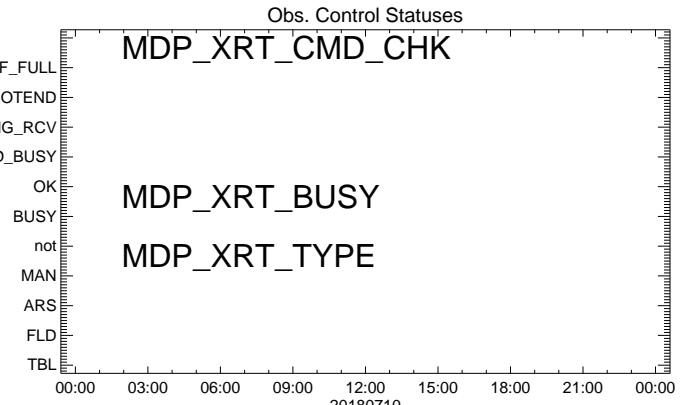
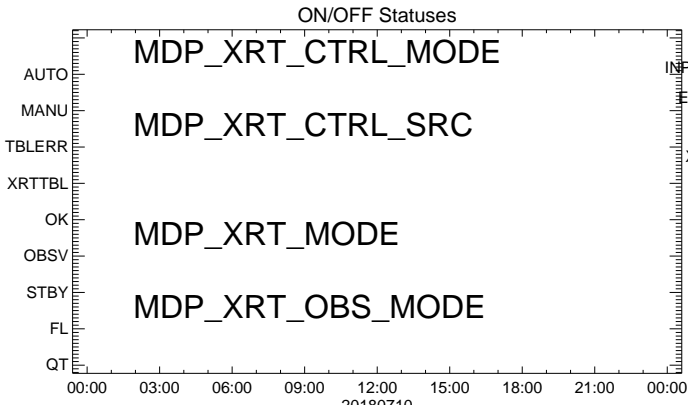


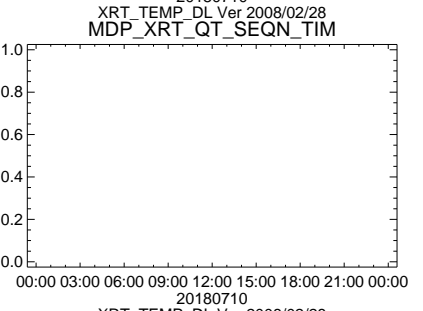
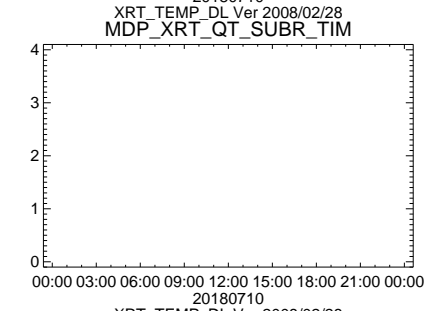
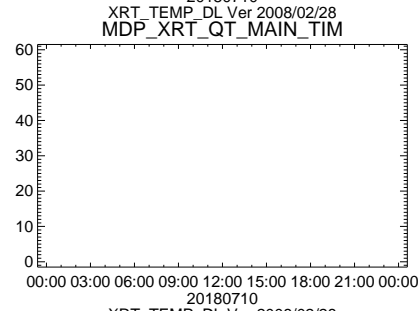
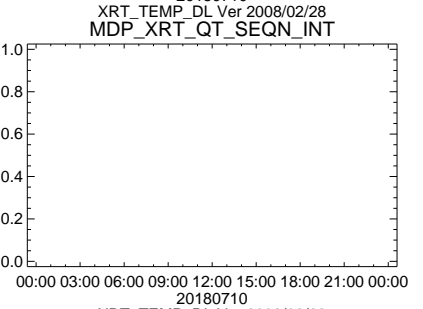
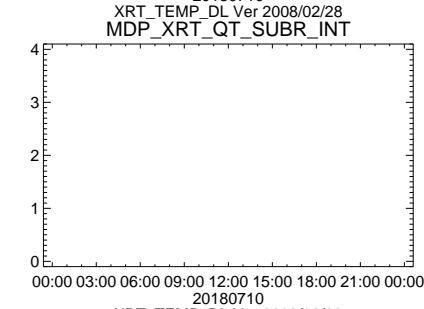
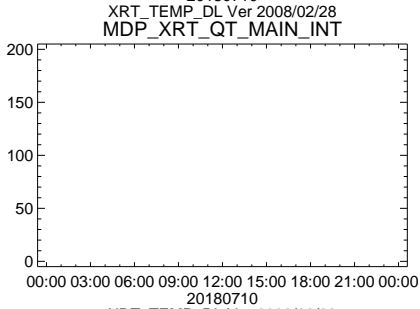
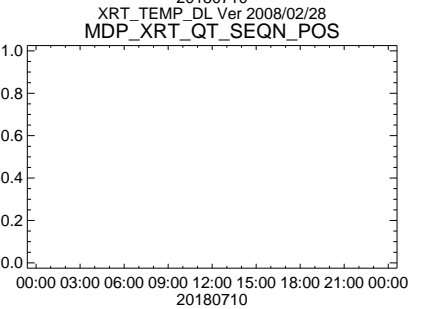
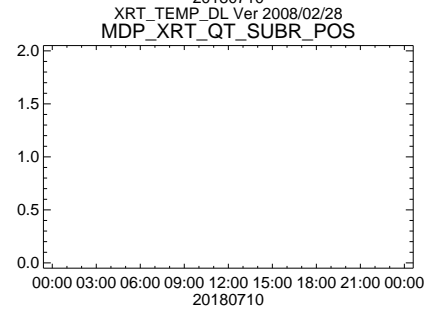
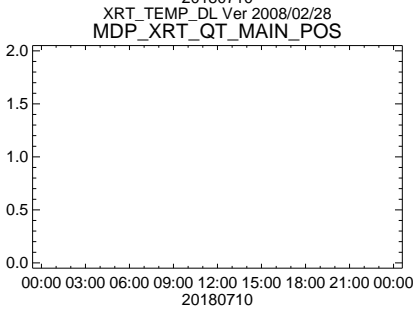
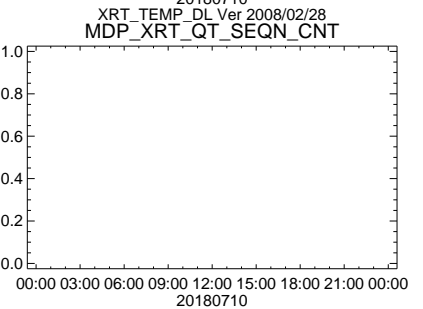
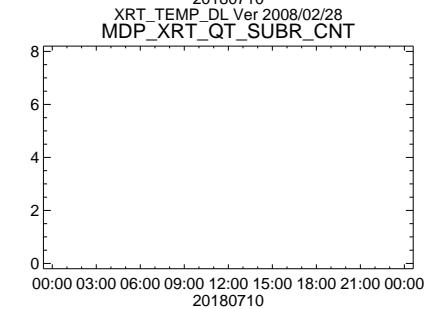
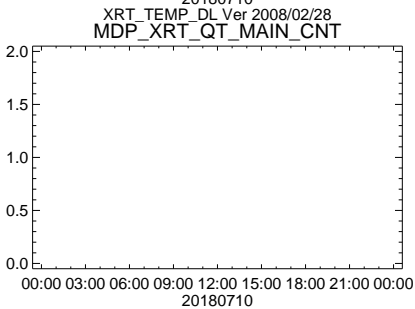
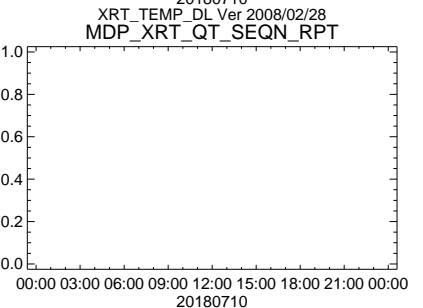
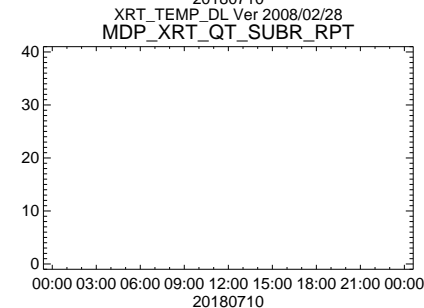
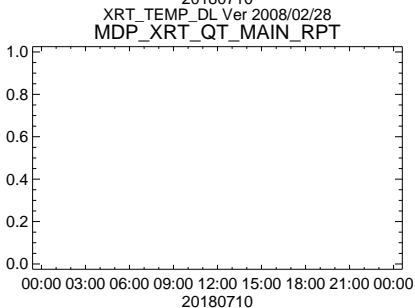
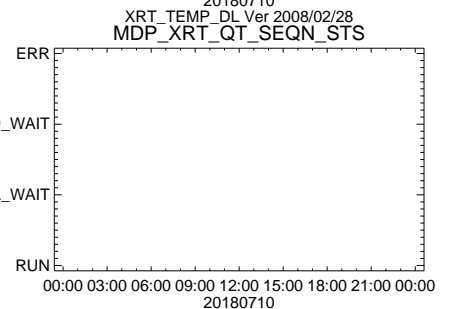
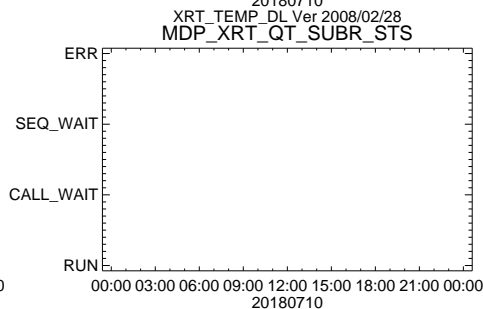
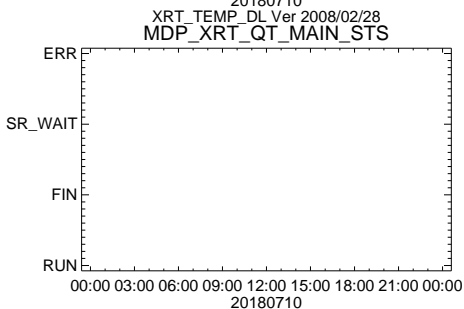
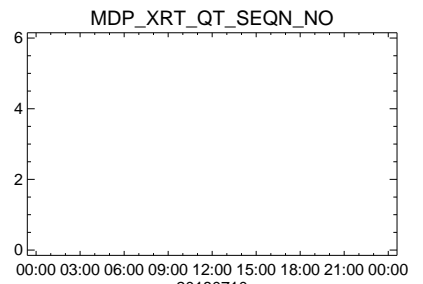
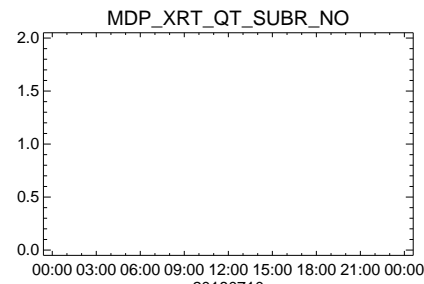
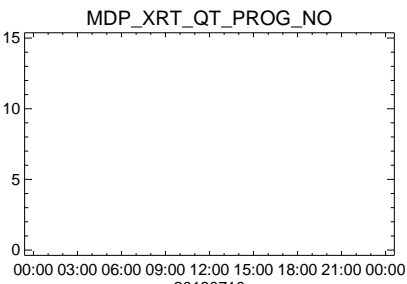


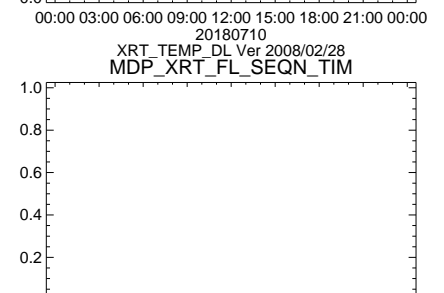
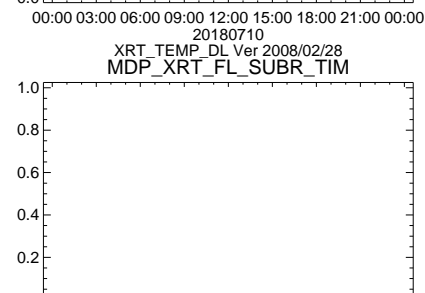
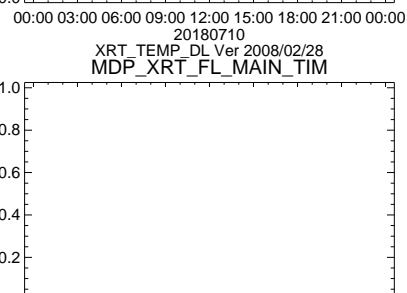
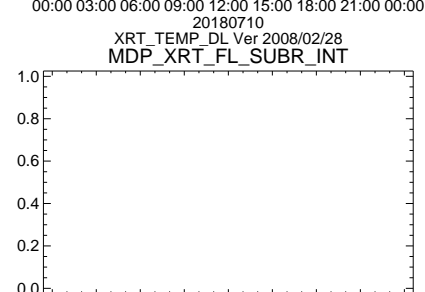
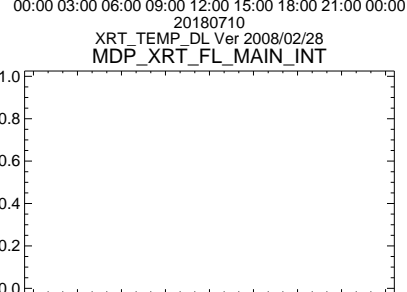
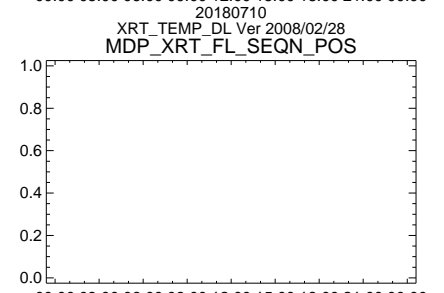
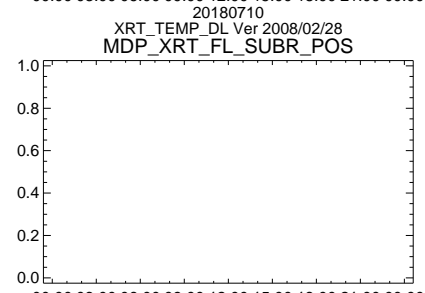
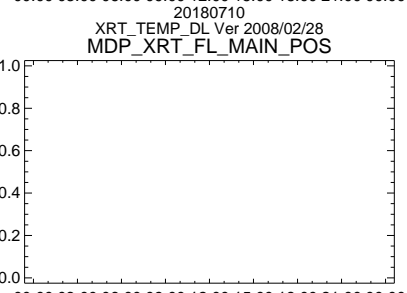
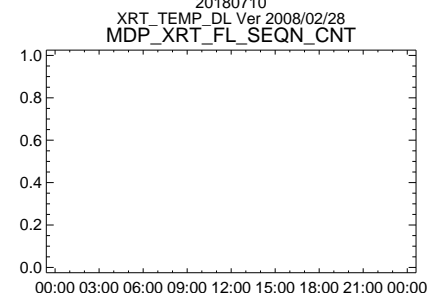
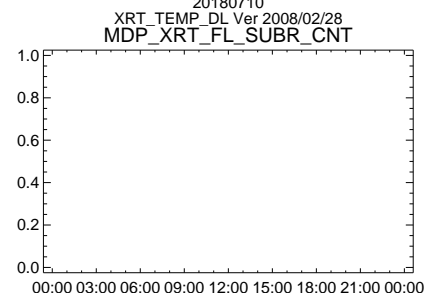
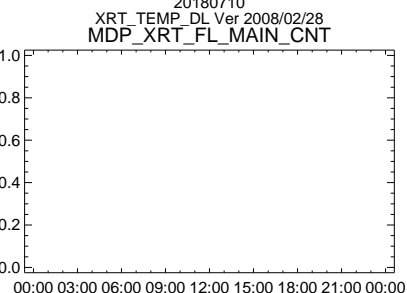
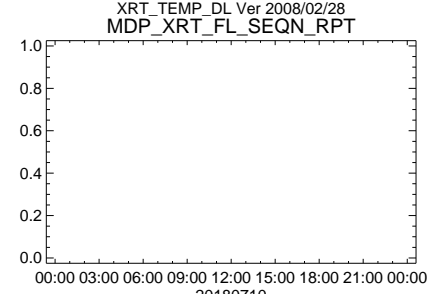
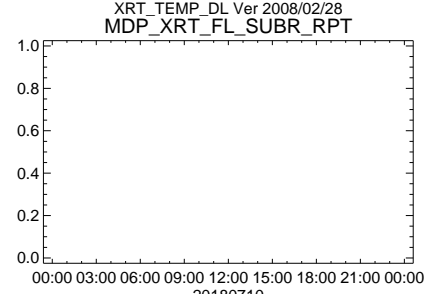
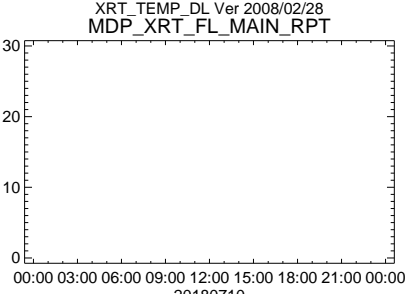
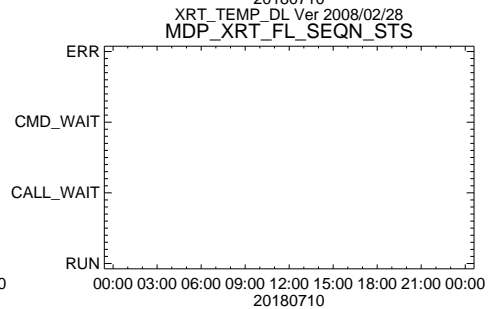
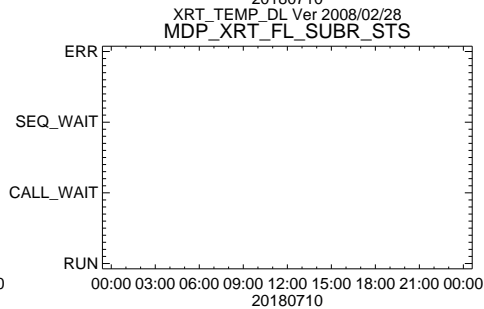
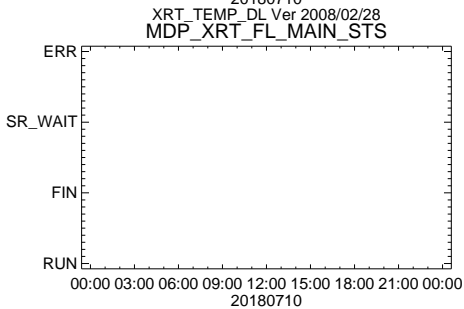
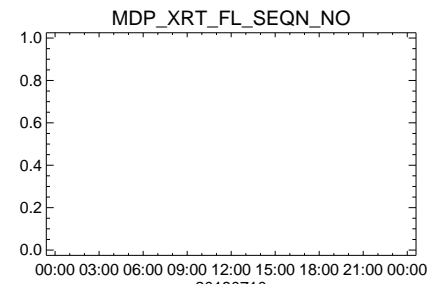
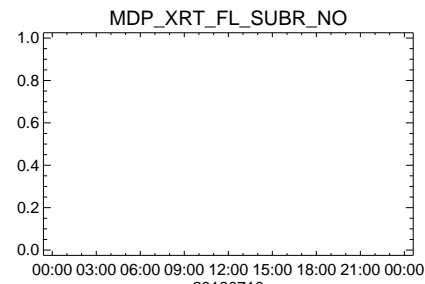
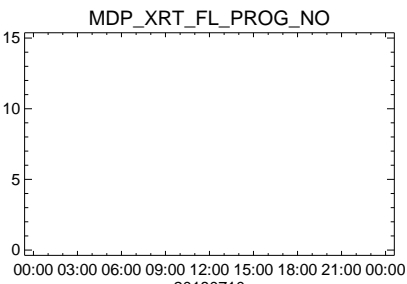




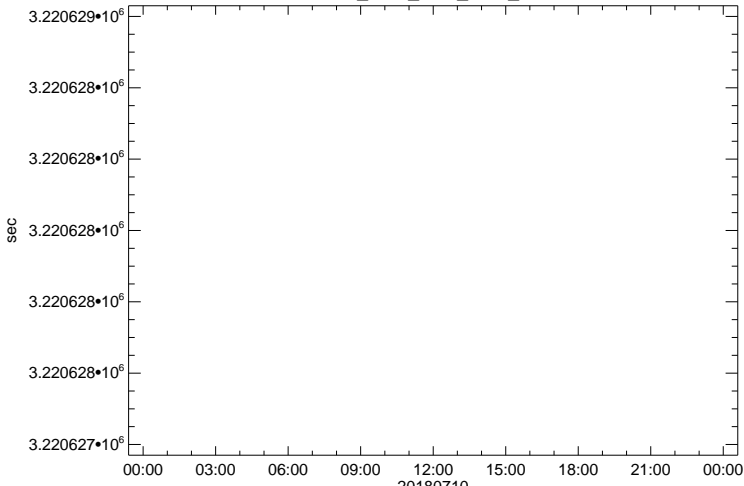




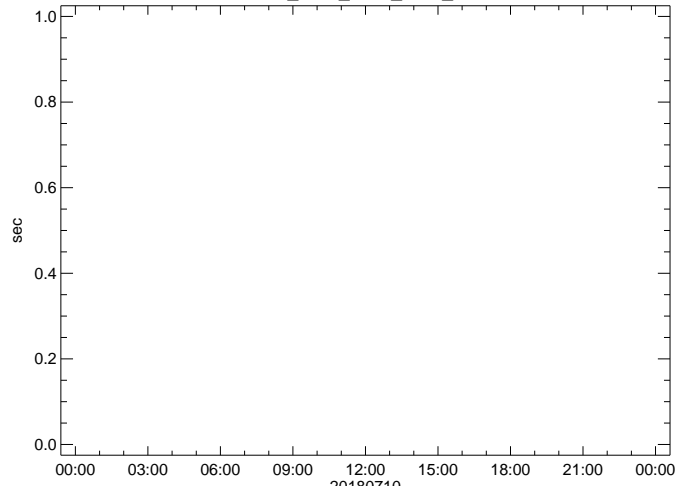




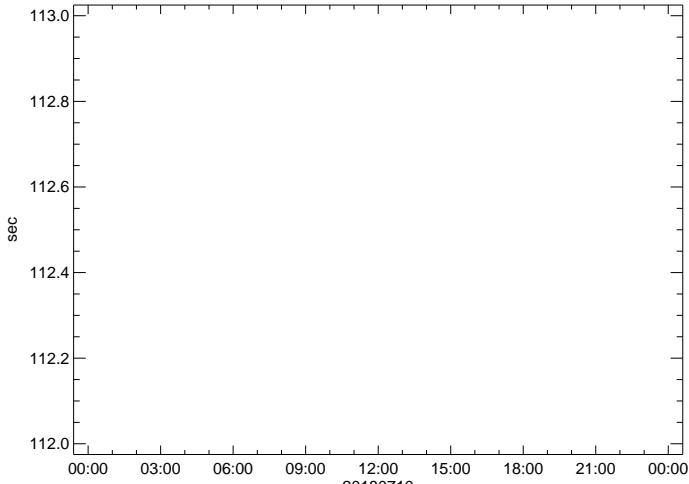
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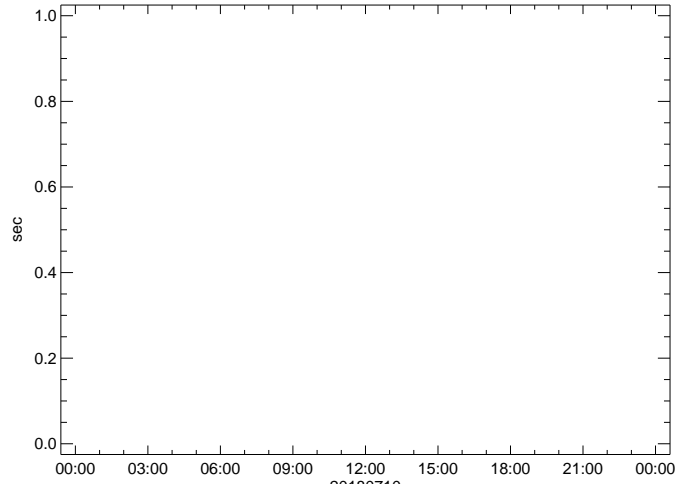
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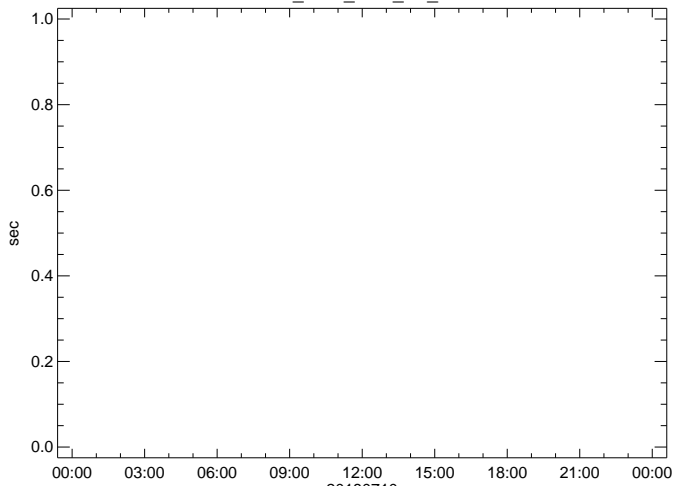
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MDP_XRT_FLD_QT_ELP



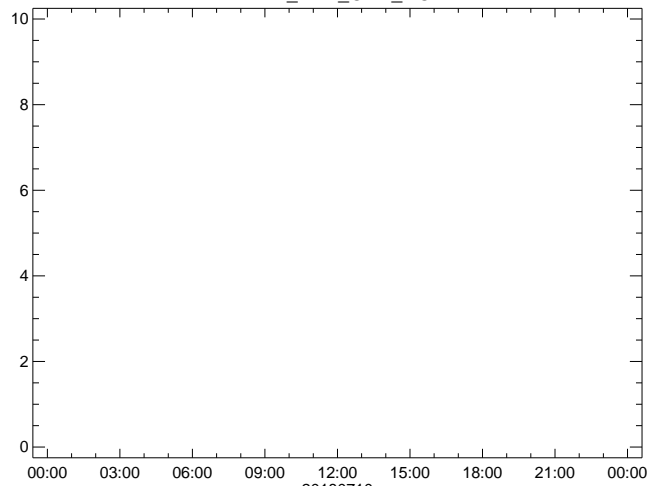
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MDP_XRT_ARS_ELP



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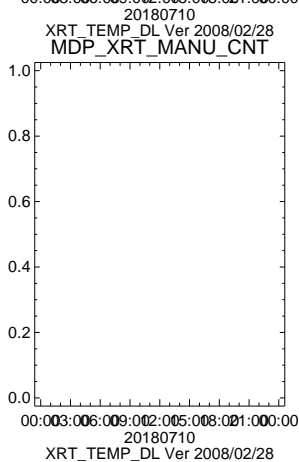
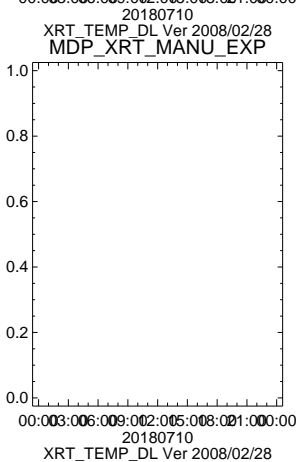
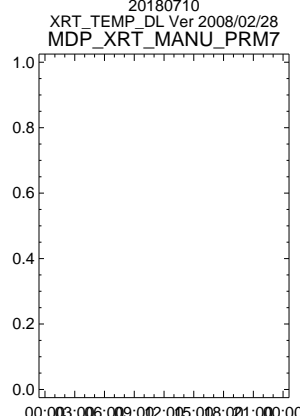
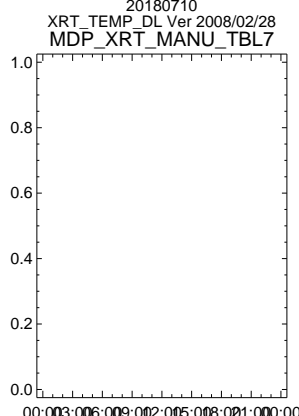
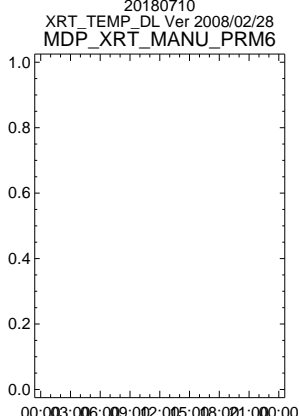
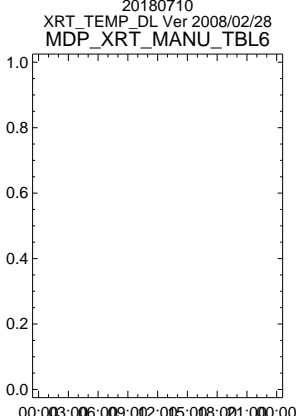
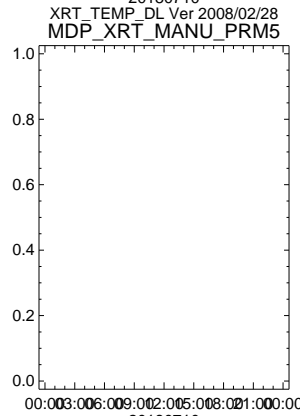
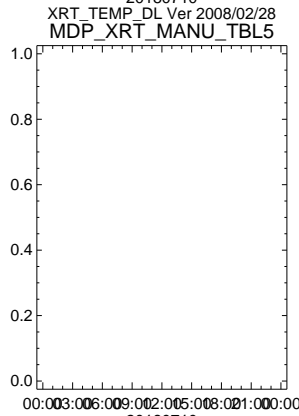
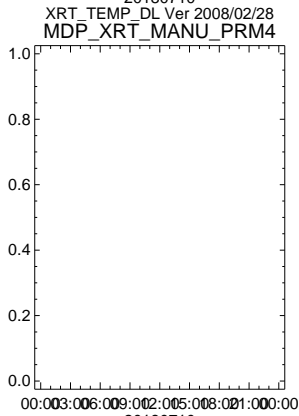
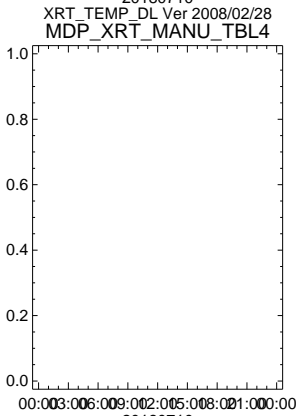
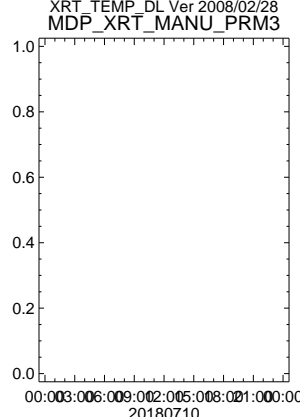
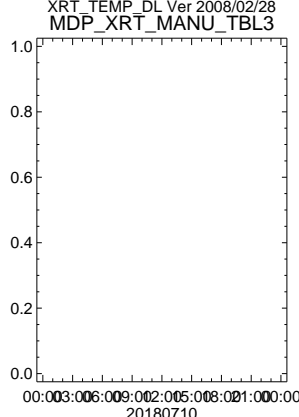
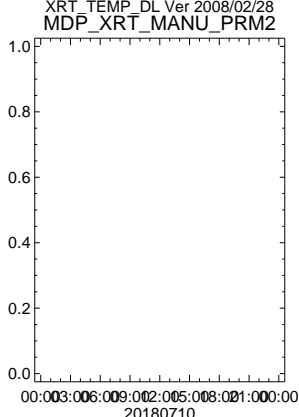
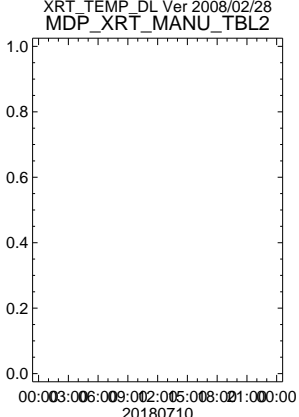
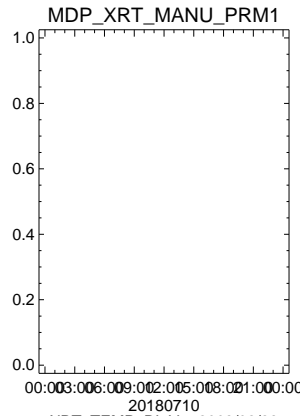
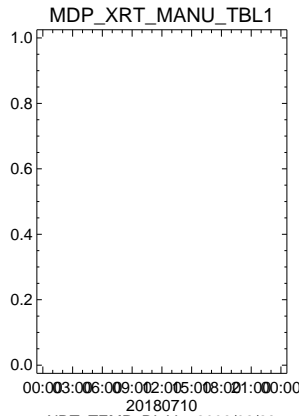
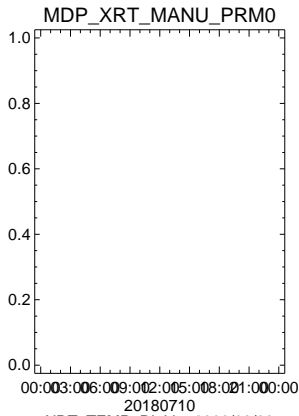
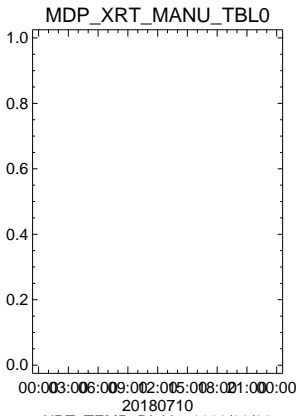


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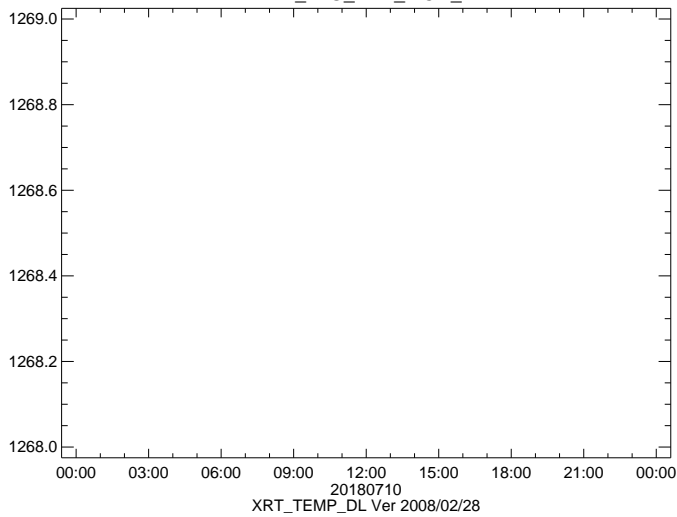
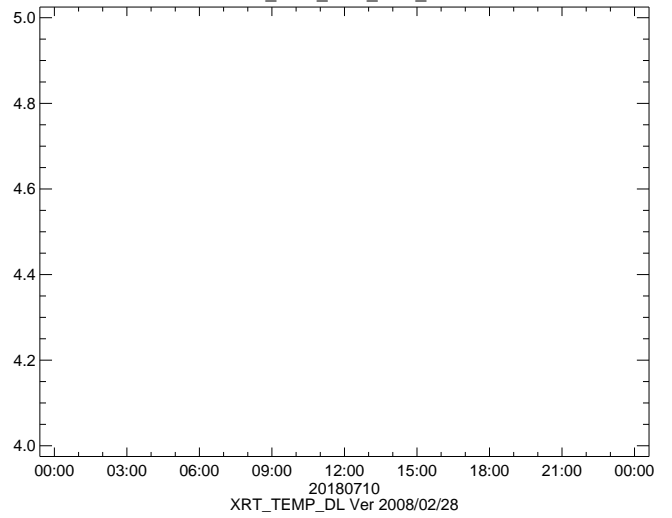
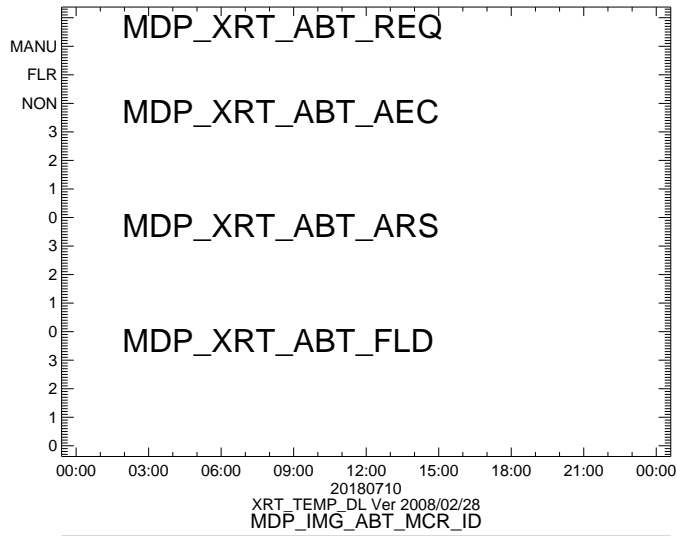
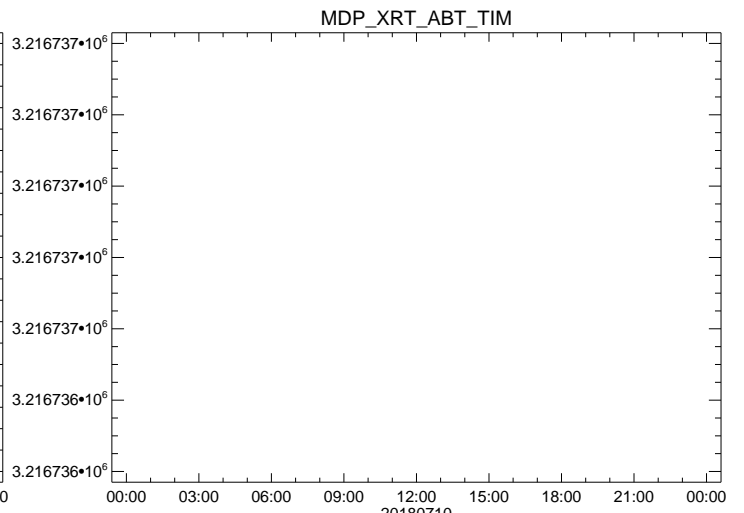
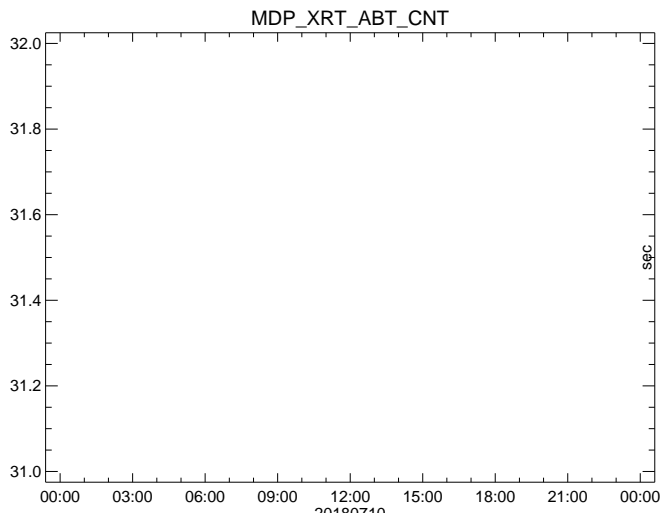
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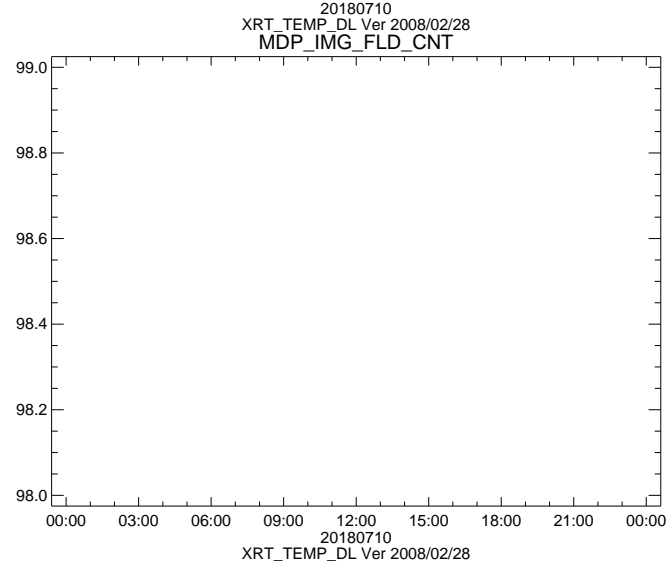
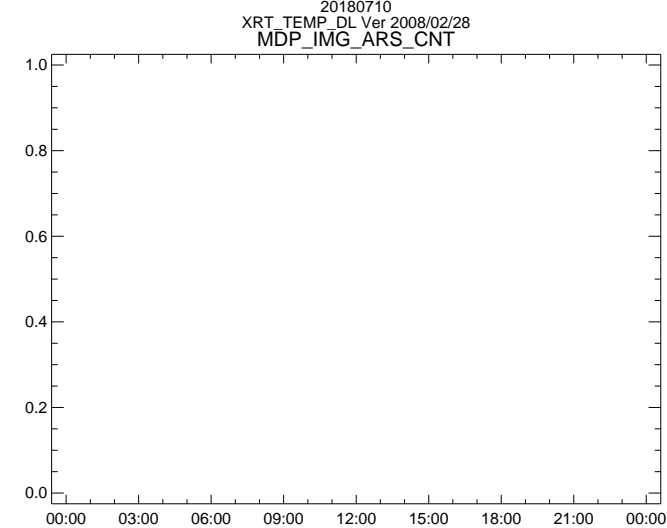
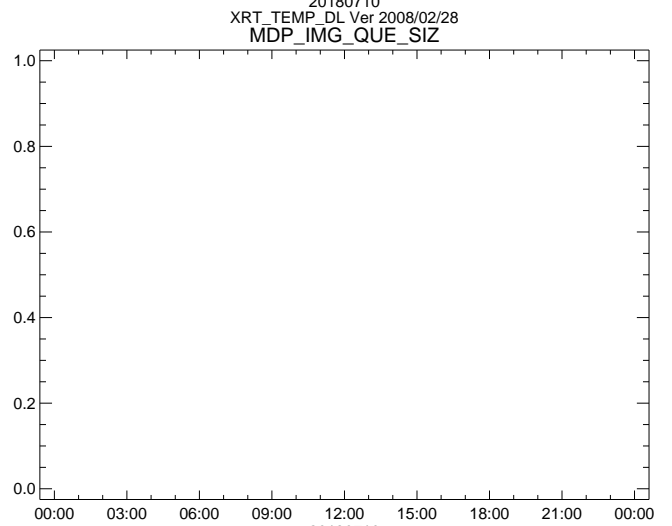
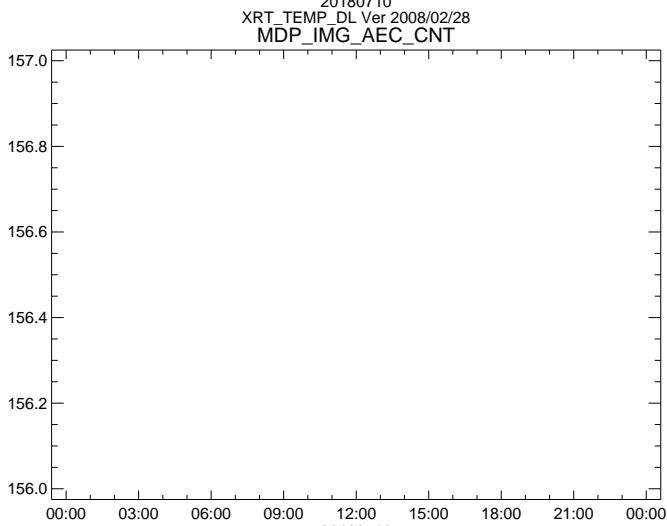
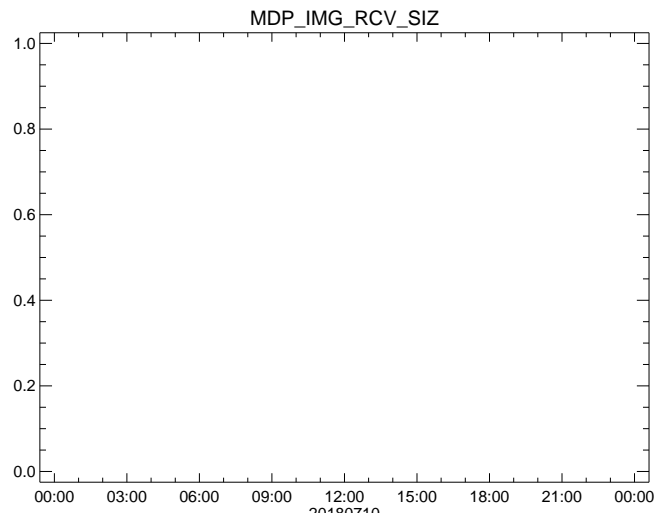
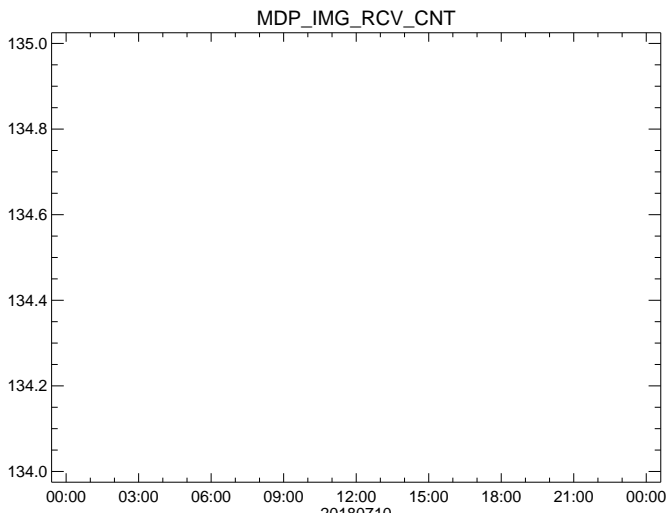
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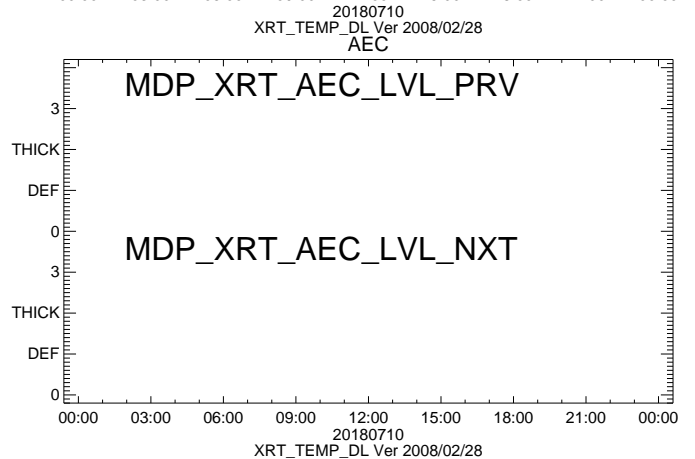
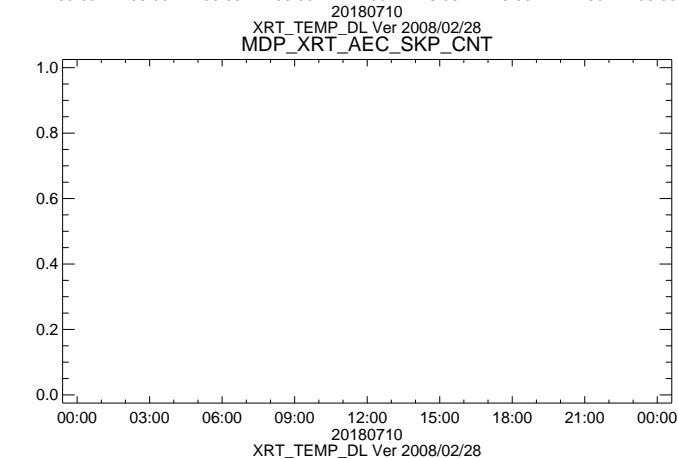
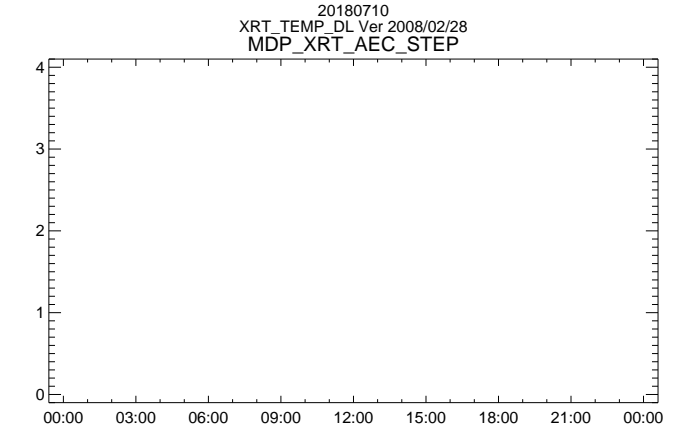
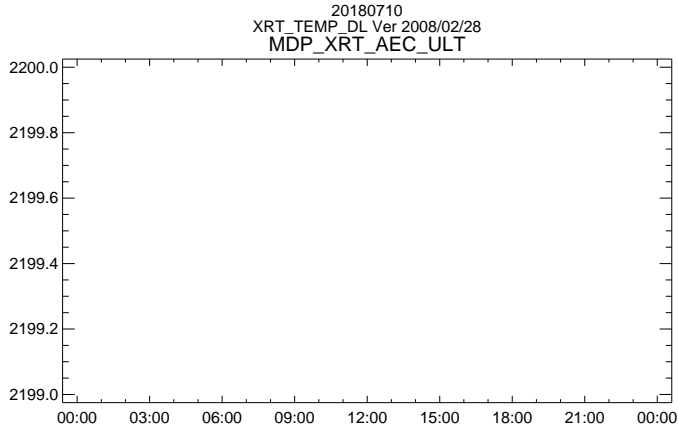
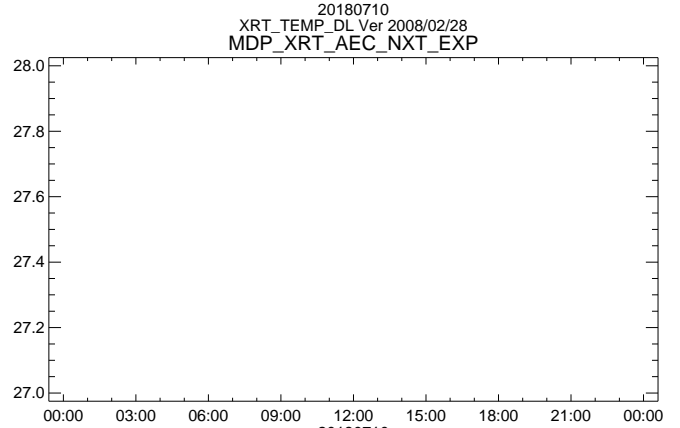
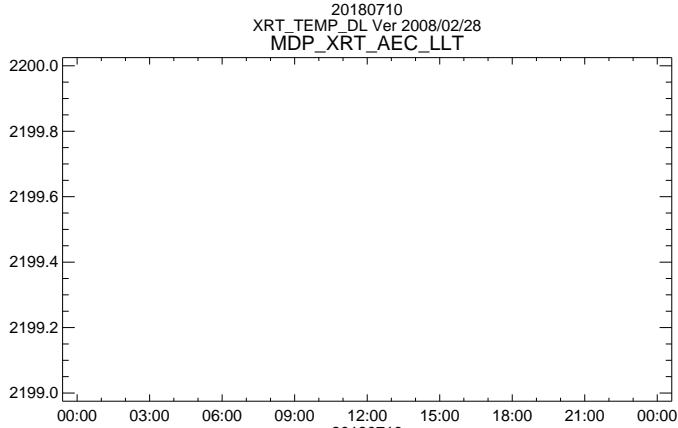
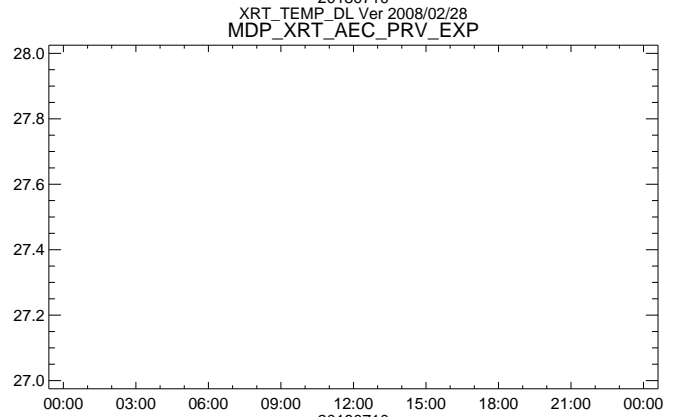
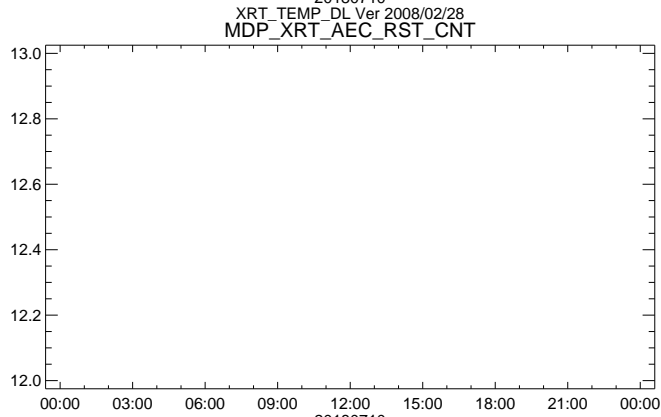
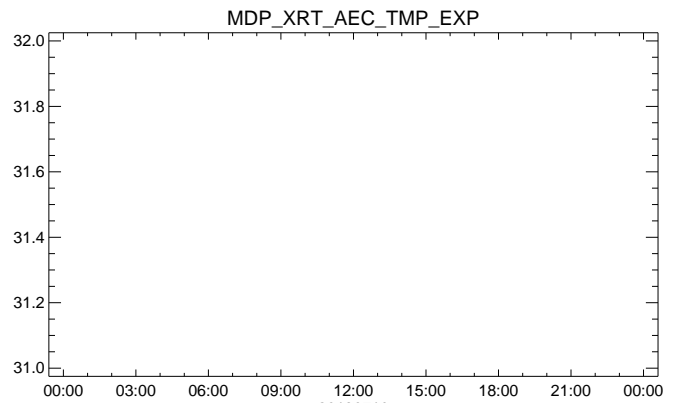
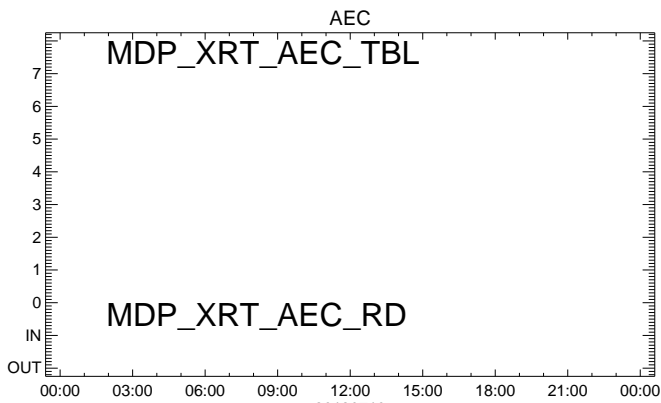


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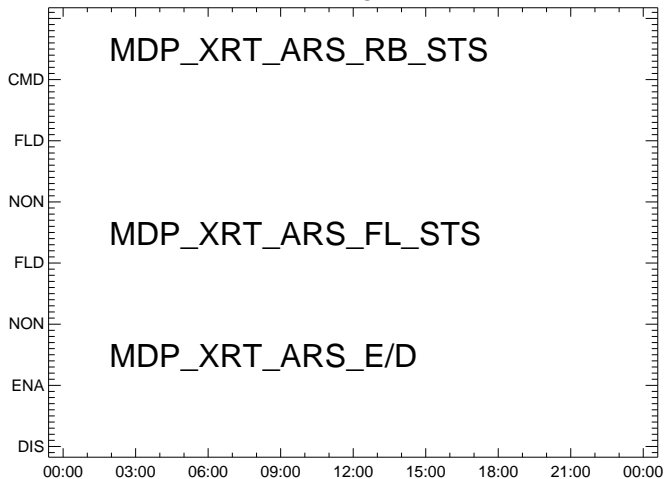
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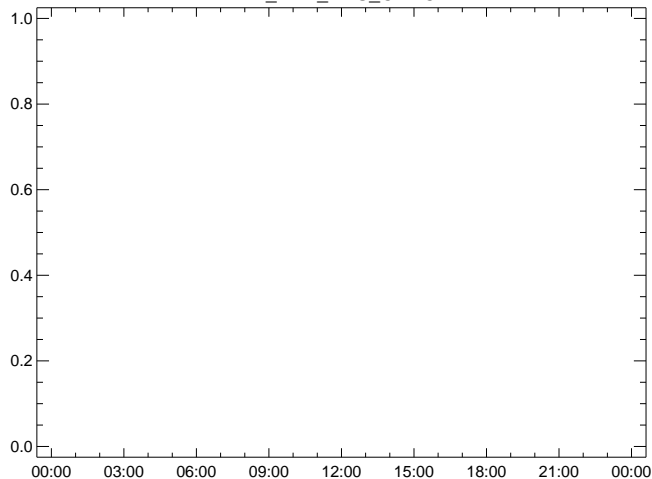




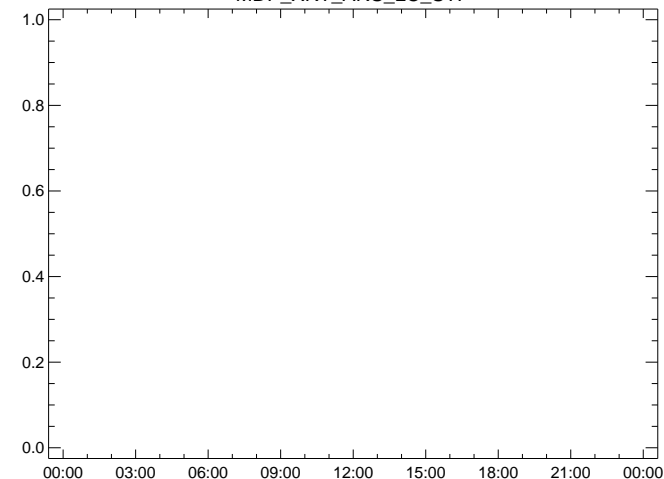
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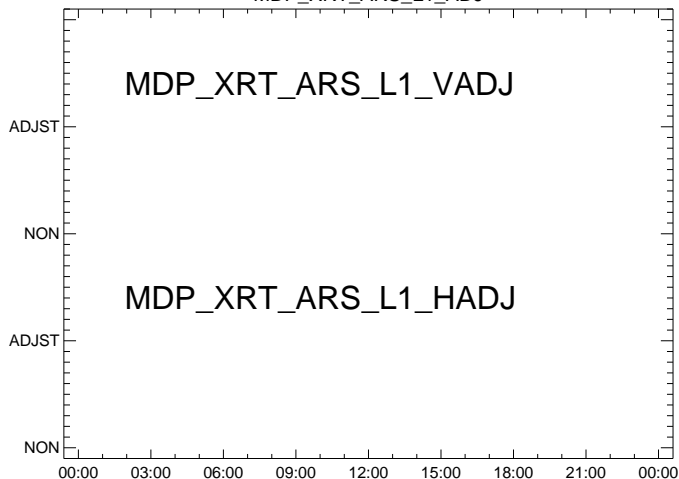
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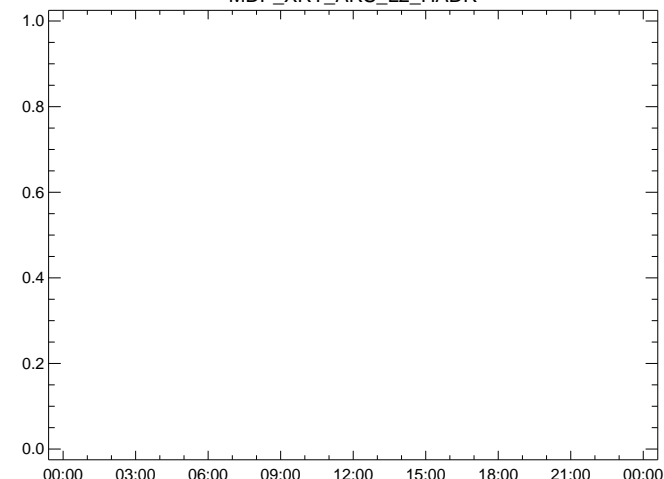
20180710
XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_ARS_LC_STP



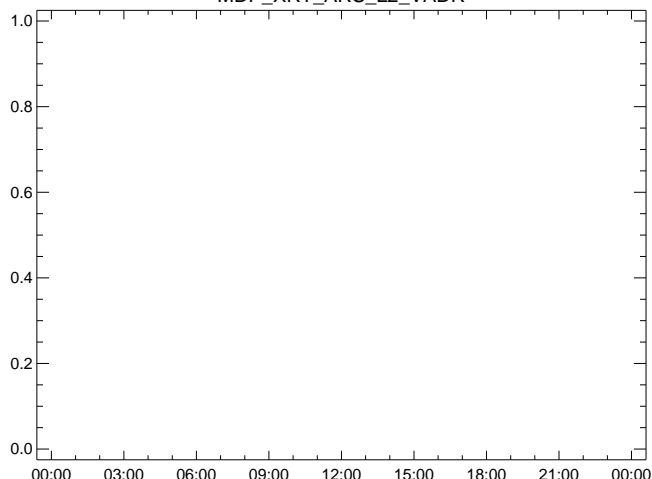
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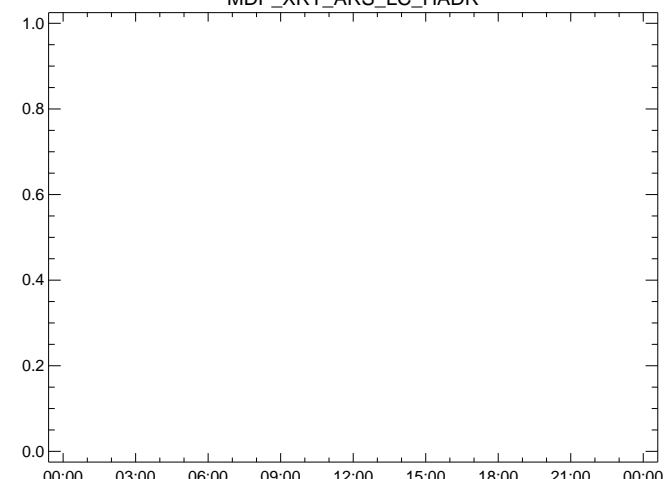
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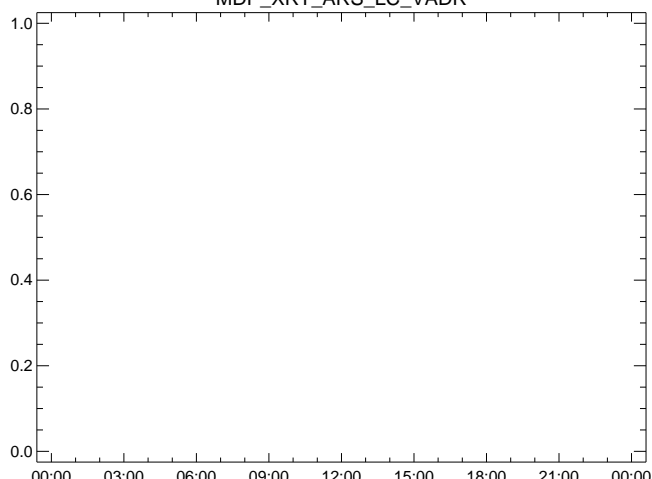
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XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_ARS_L2_VADR



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XRT_TEMP_DL Ver 2008/02/28
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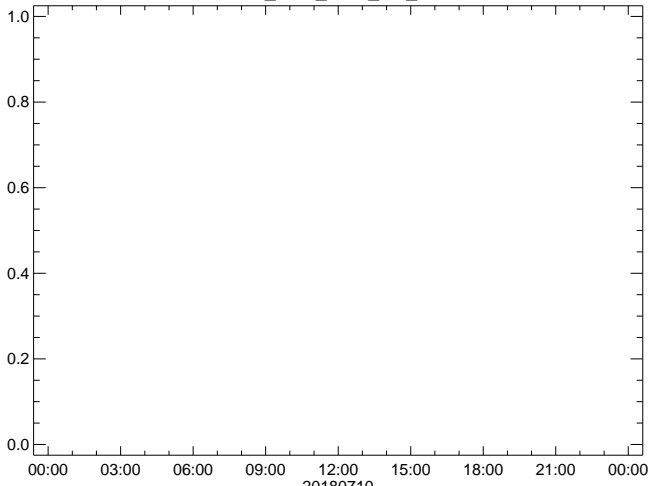
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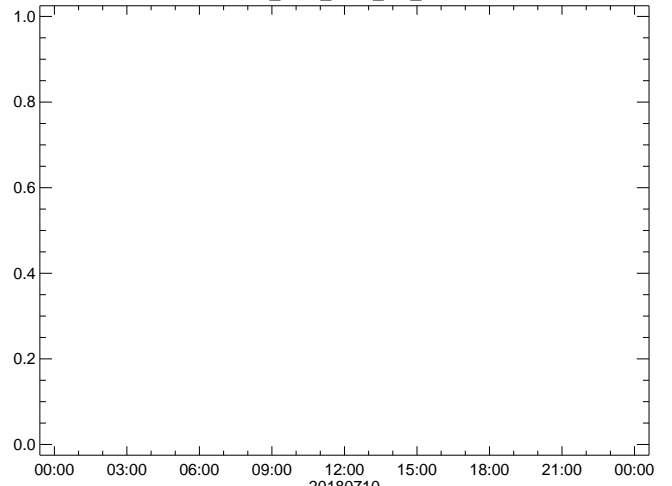
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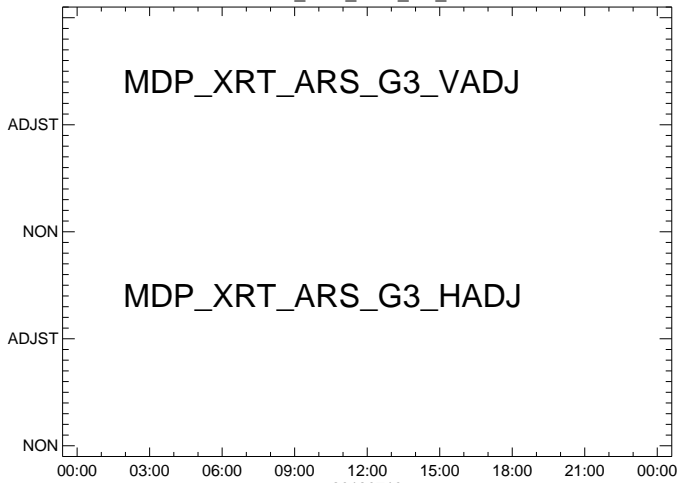
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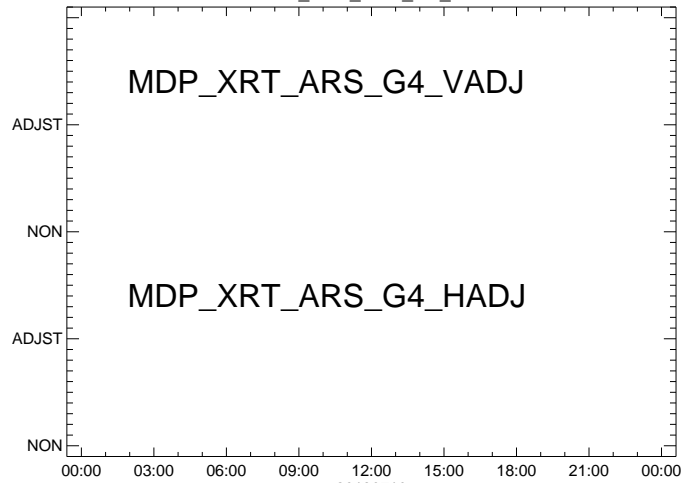
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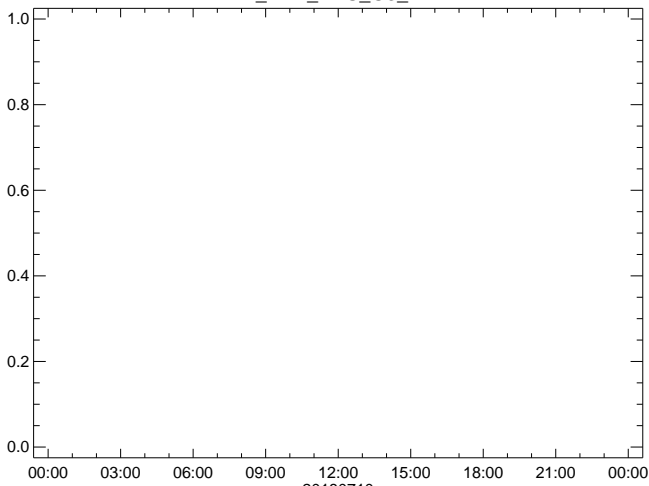
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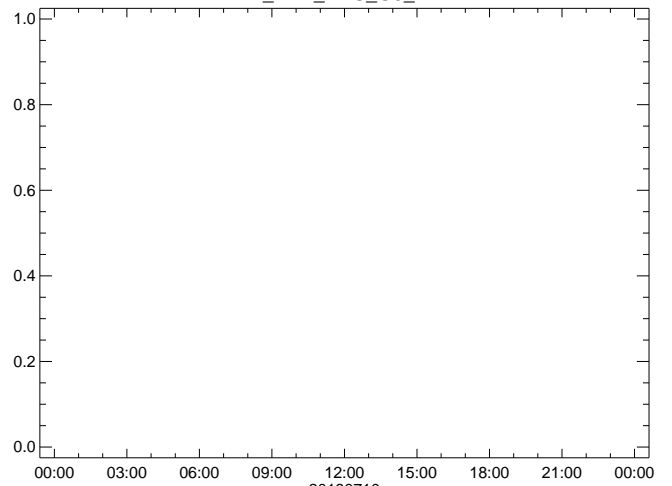
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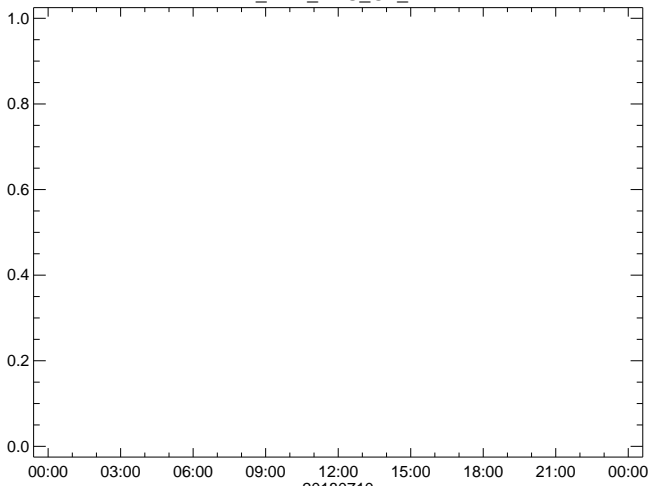
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MDP_XRT_ARS_G3_HADR



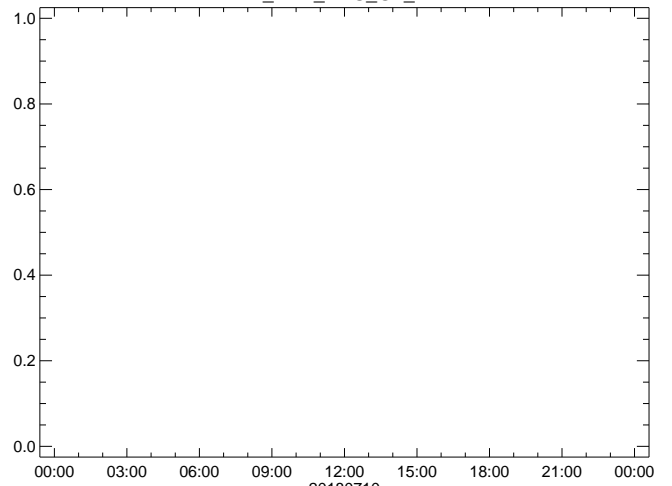
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MDP_XRT_ARS_G3_VADR



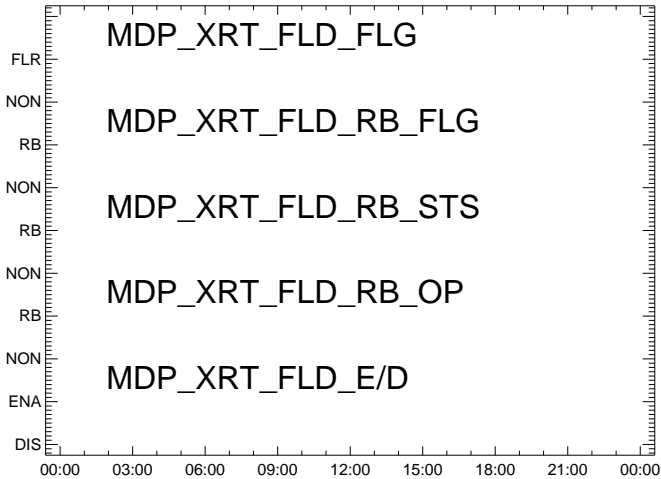
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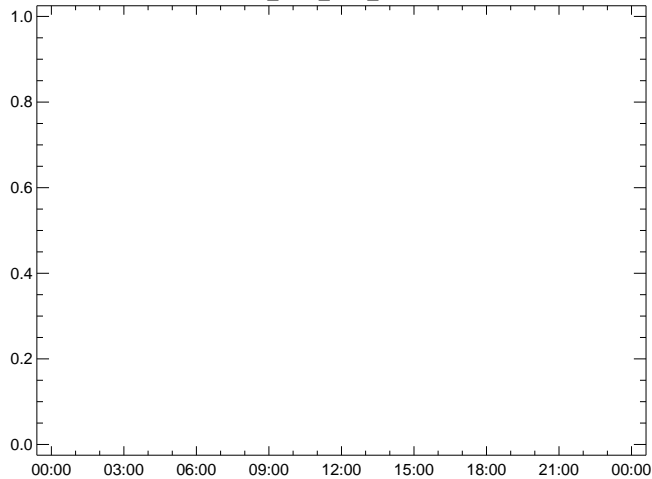
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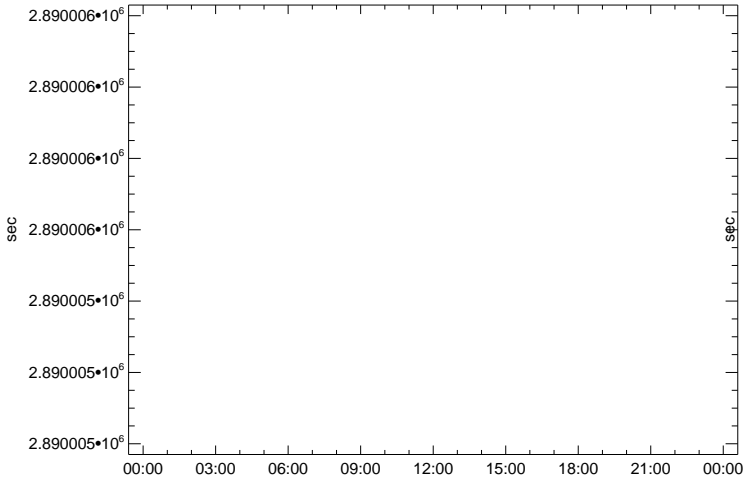
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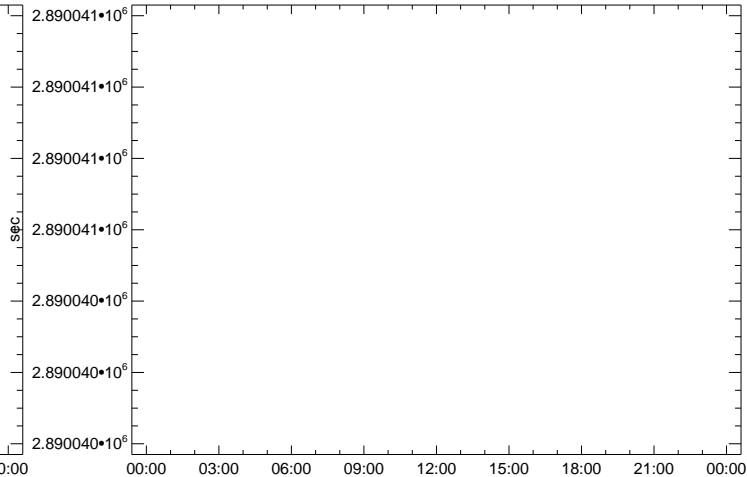
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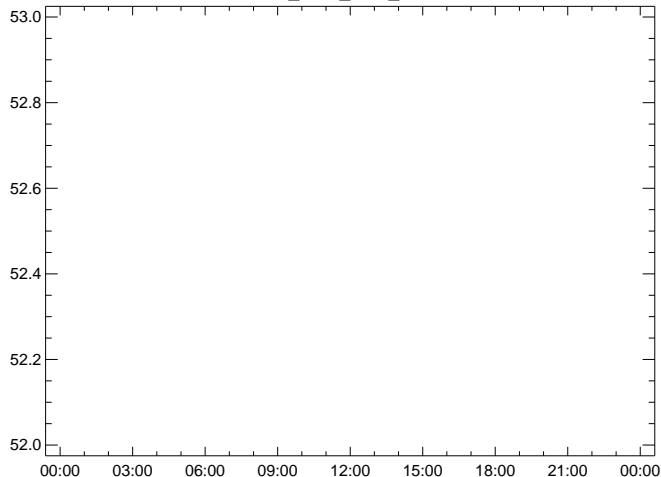
XRT_TEMP_DL Ver 2008/02/28
20180710
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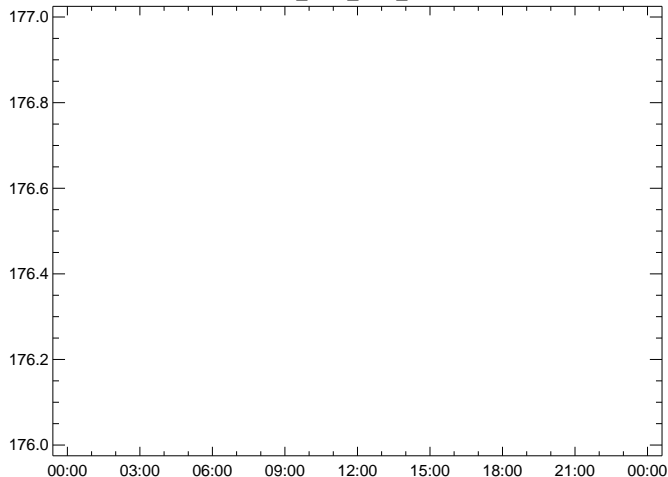
XRT_TEMP_DL Ver 2008/02/28
20180710
MDP_XRT_FLR_TRM_TIM



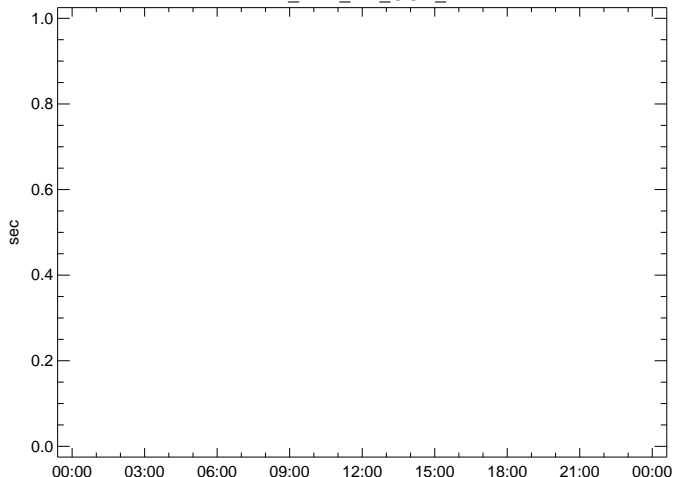
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20180710
MDP_XRT_FLD_HADR



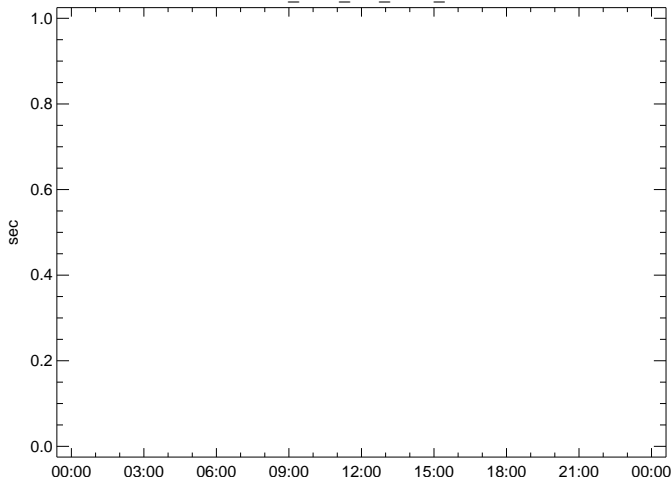
XRT_TEMP_DL Ver 2008/02/28
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MDP_XRT_FLD_VADR



XRT_TEMP_DL Ver 2008/02/28
20180710
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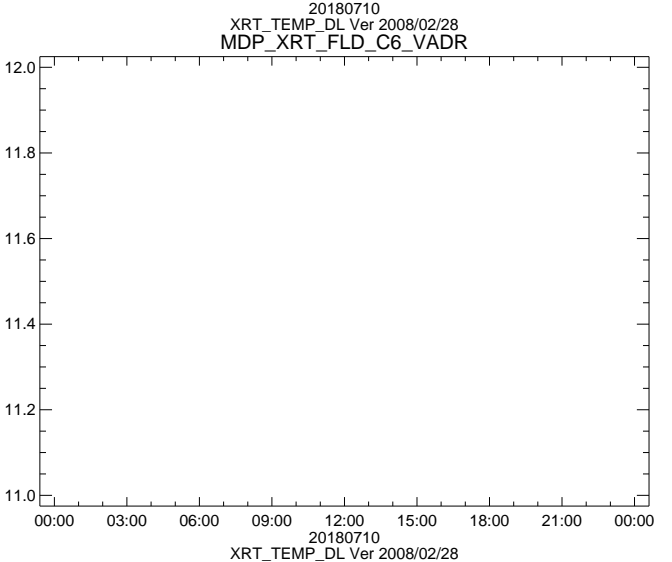
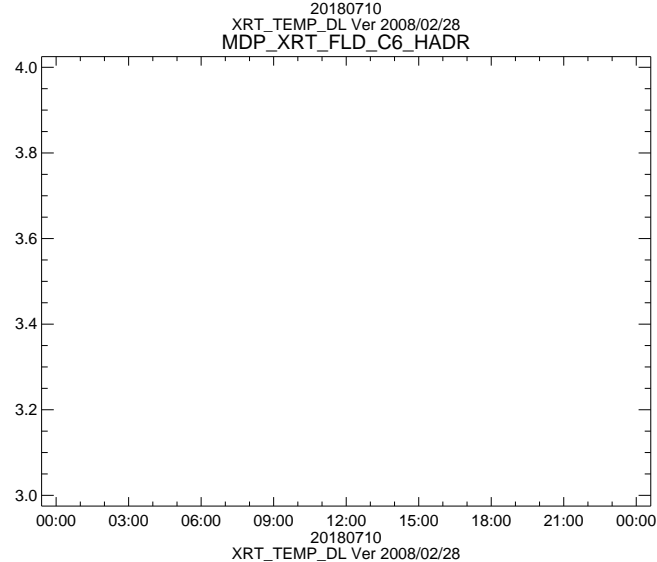
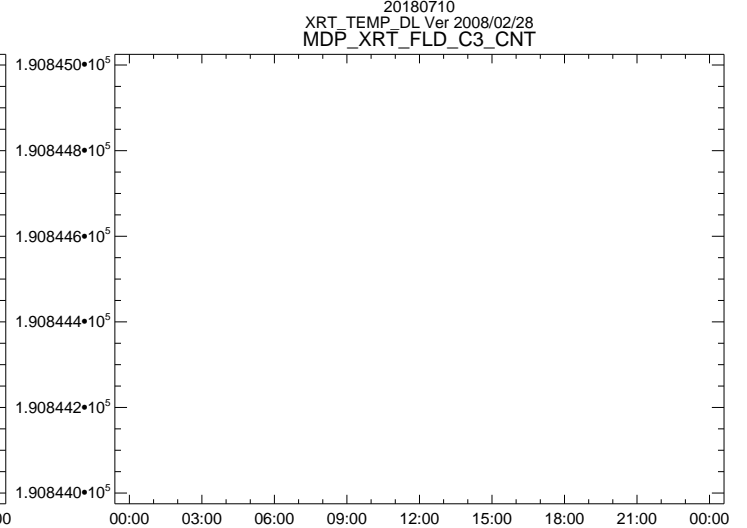
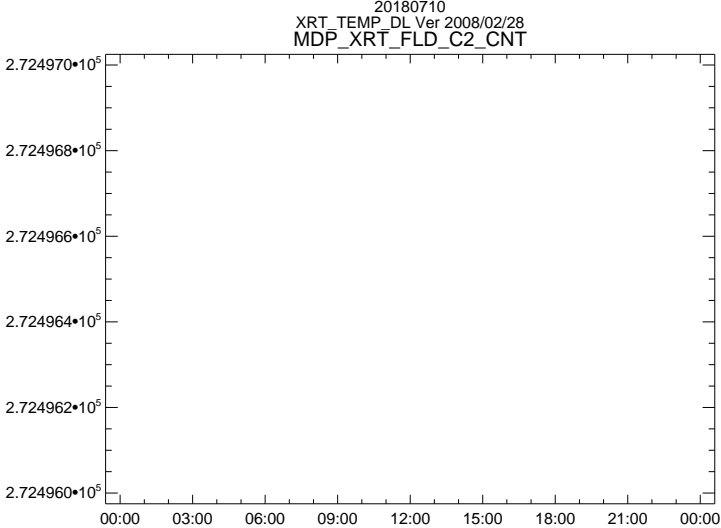
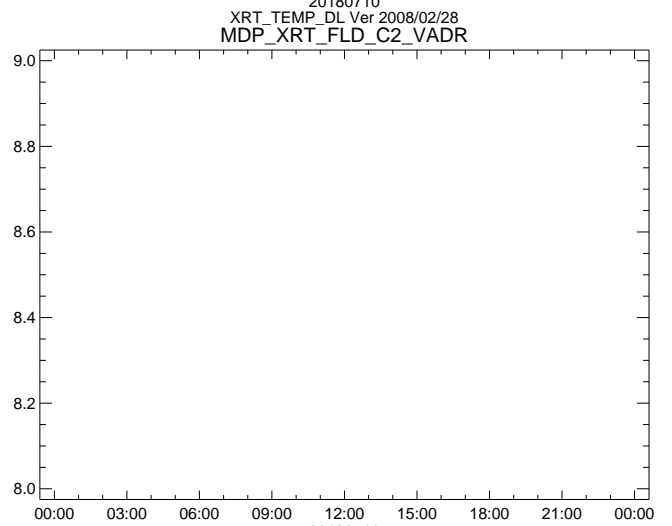
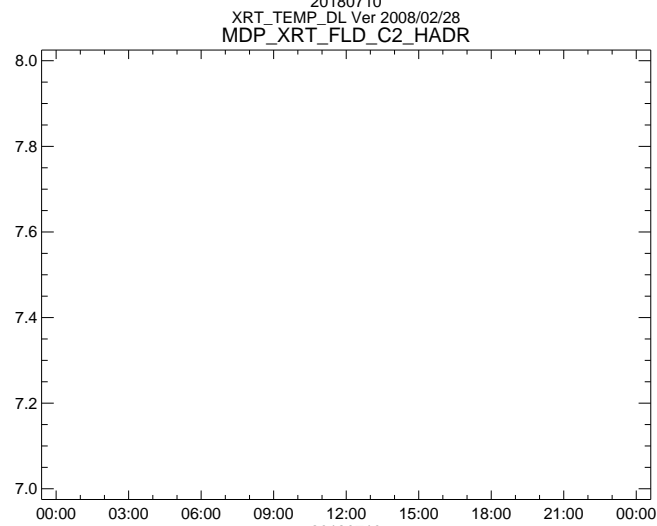
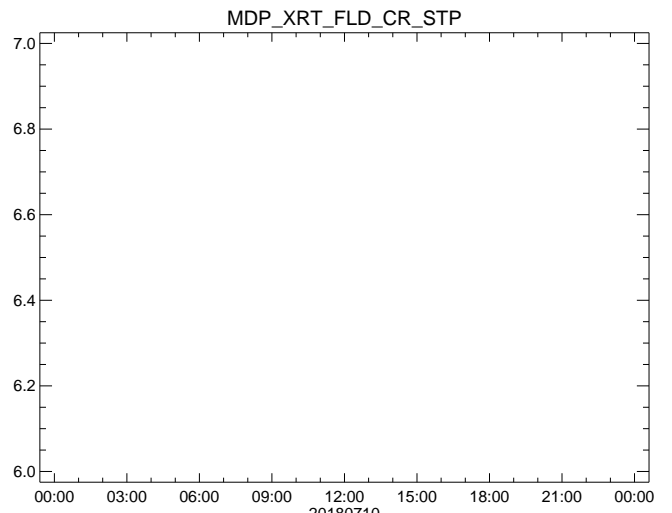
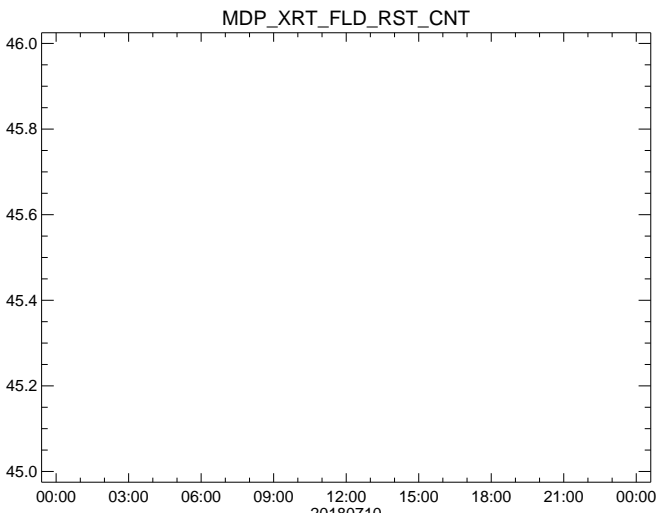


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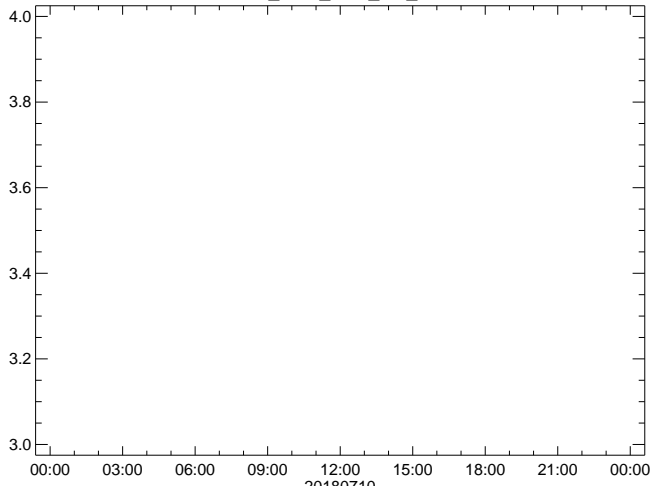


XRT_TEMP_DL Ver 2008/02/28

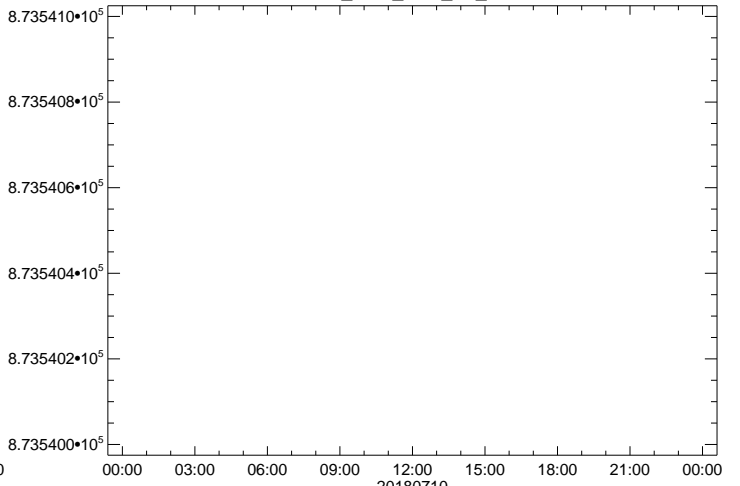
XRT_TEMP_DL Ver 2008/02/28



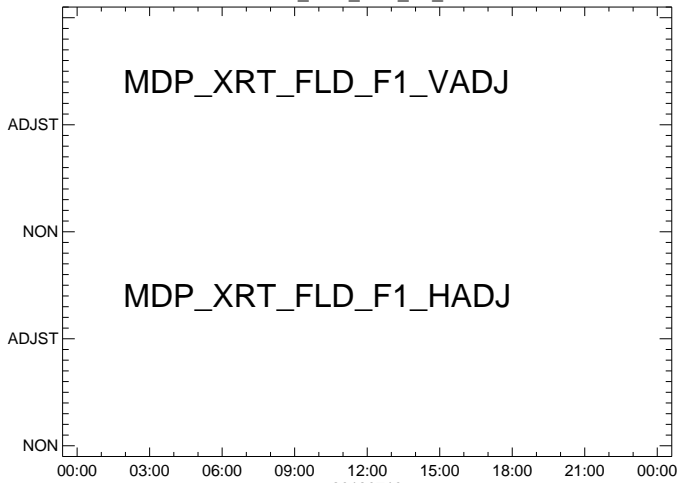
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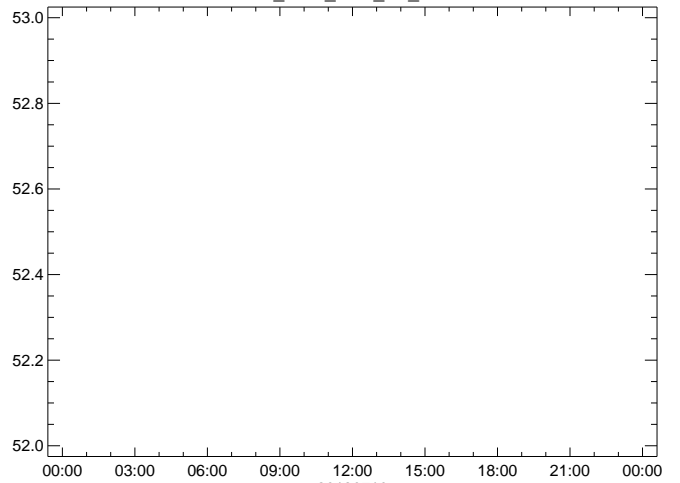
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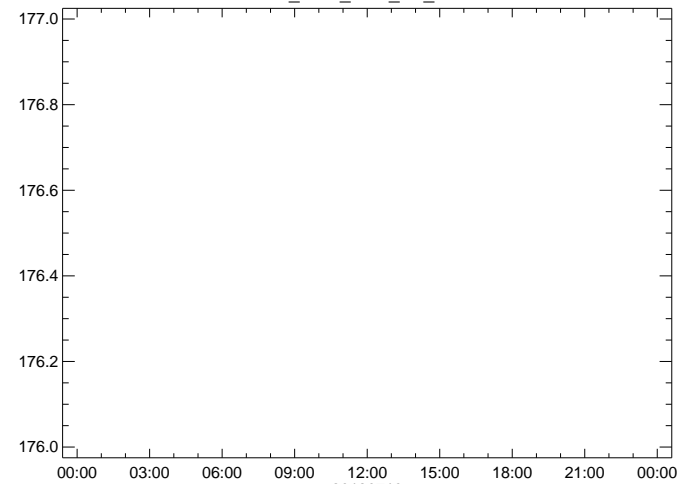
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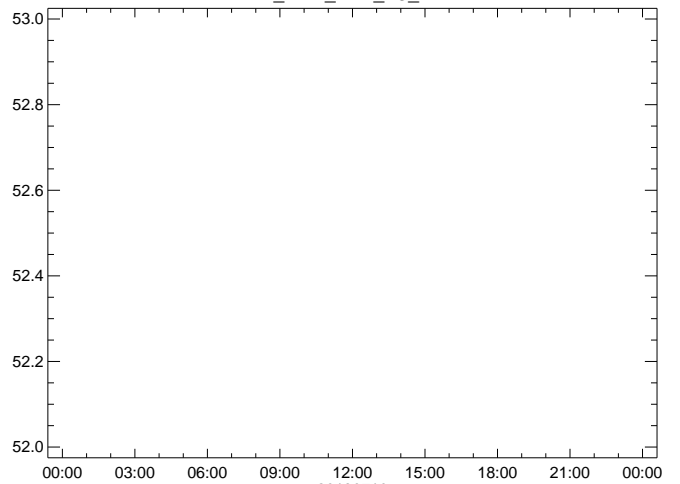
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MDP_XRT_FLD_F2_HADR



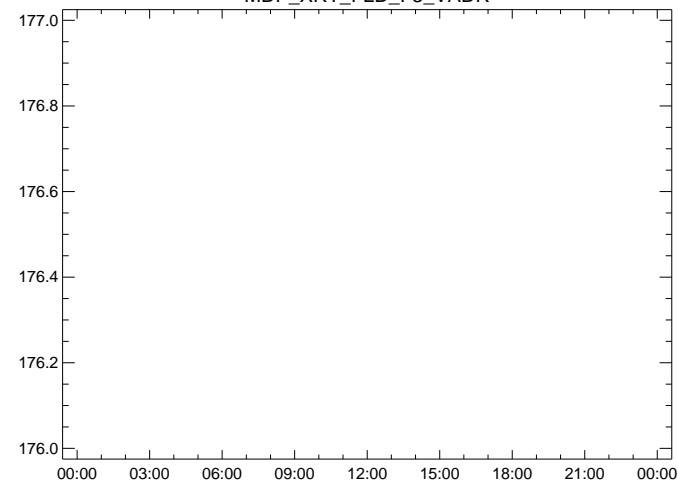
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XRT_TEMP_DL Ver 2008/02/28
MDP_XRT_FLD_F2_VADR



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XRT_TEMP_DL Ver 2008/02/28
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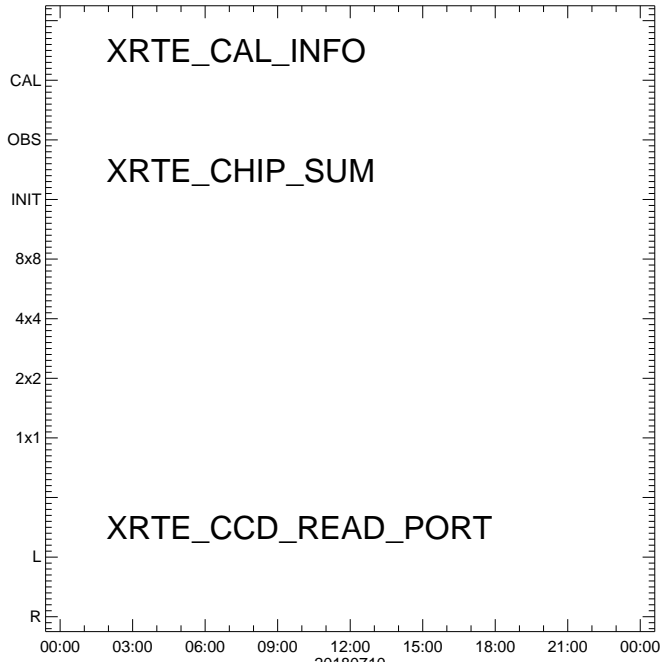


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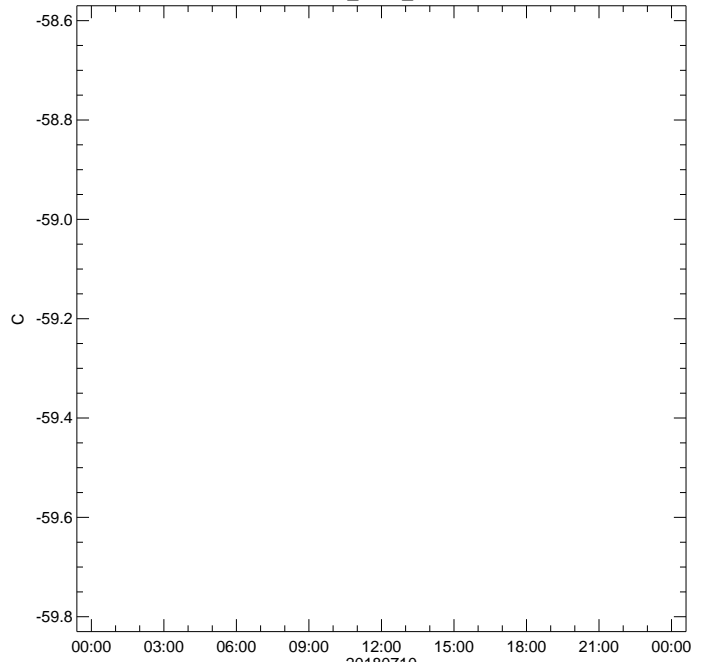


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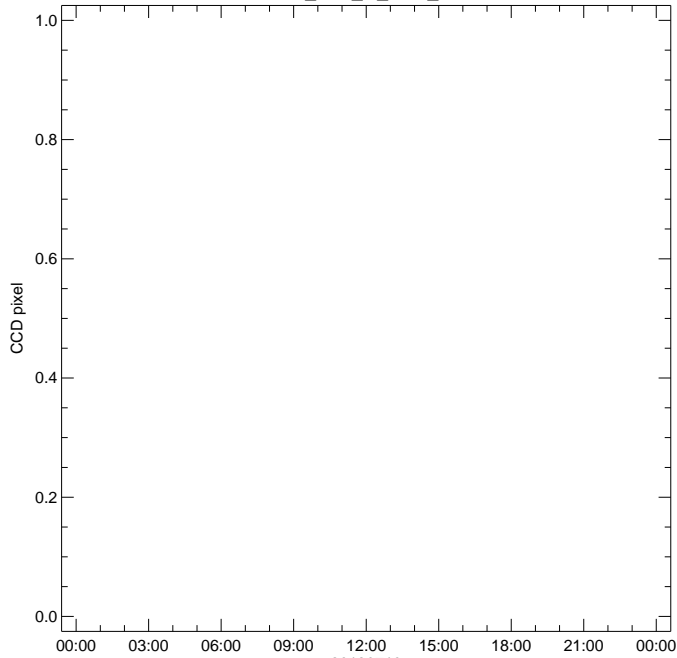
ON/OFF Statuses



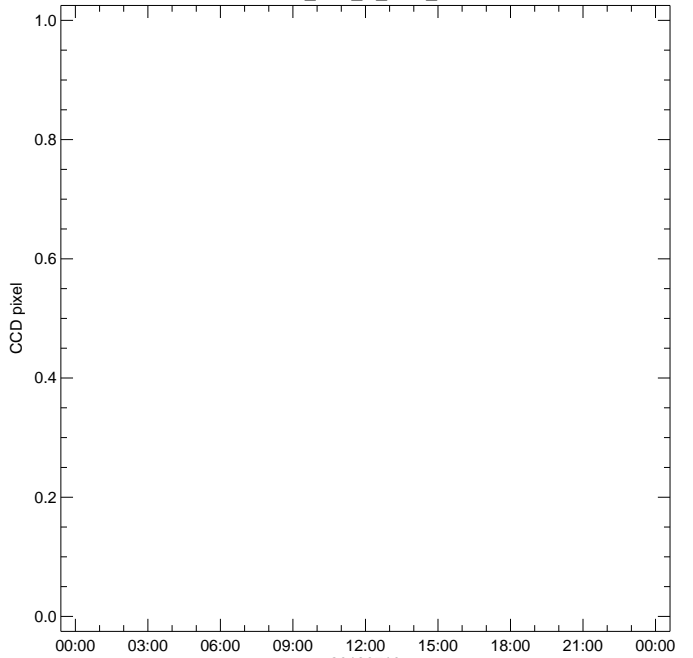
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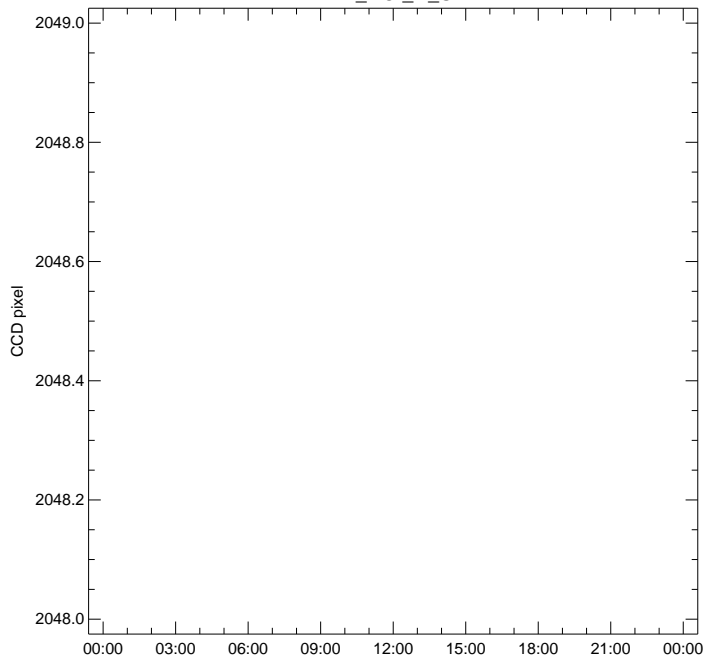
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XRT_TEMP_DL Ver 2008/02/28
XRTE_ROI_H_STA_POS



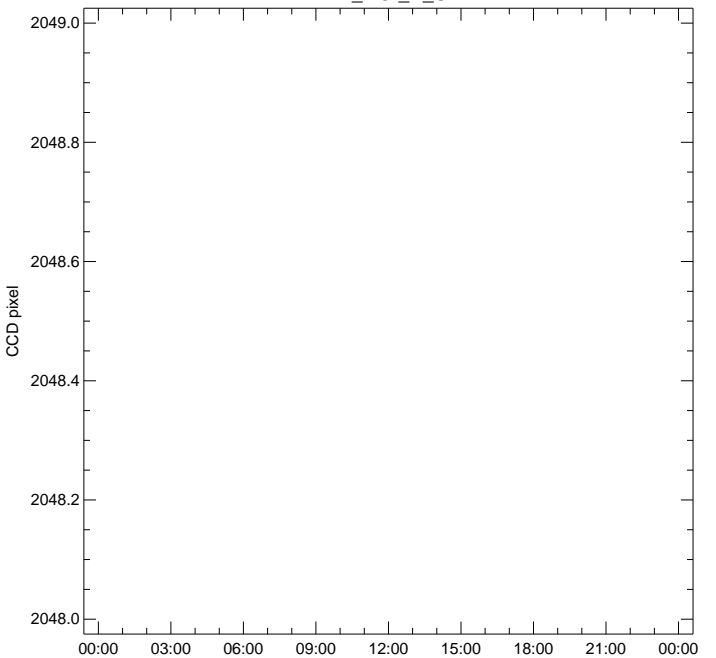
20180710
XRT_TEMP_DL Ver 2008/02/28
XRTE_ROI_V_STA_POS



20180710
XRT_TEMP_DL Ver 2008/02/28
XRTE_ROI_H_SIZE



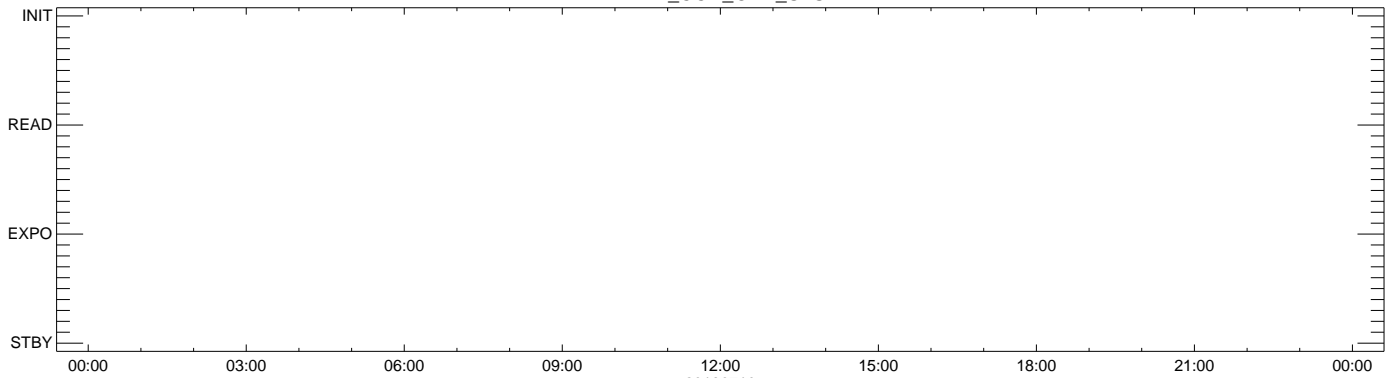
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XRTE_ROI_V_SIZE



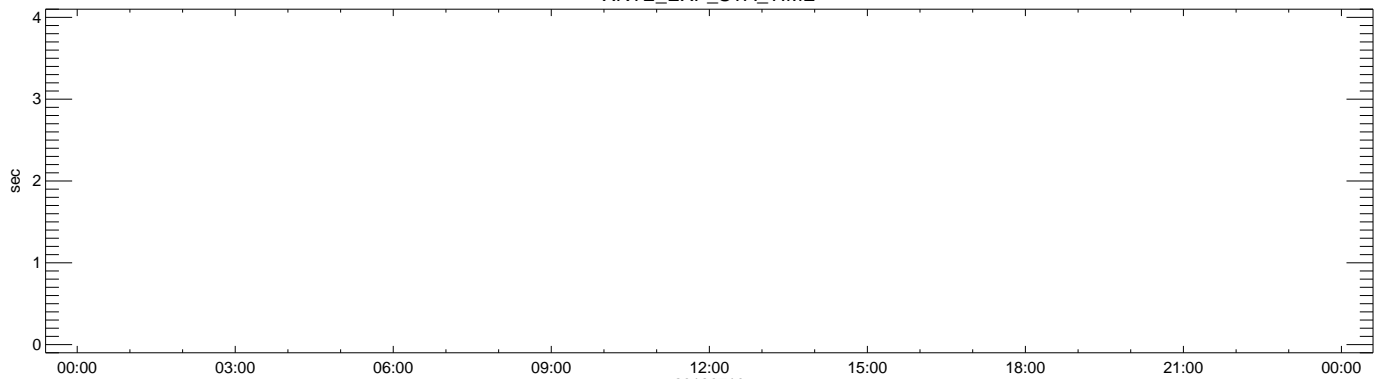
20180710
XRT_TEMP_DL Ver 2008/02/28

20180710
XRT_TEMP_DL Ver 2008/02/28

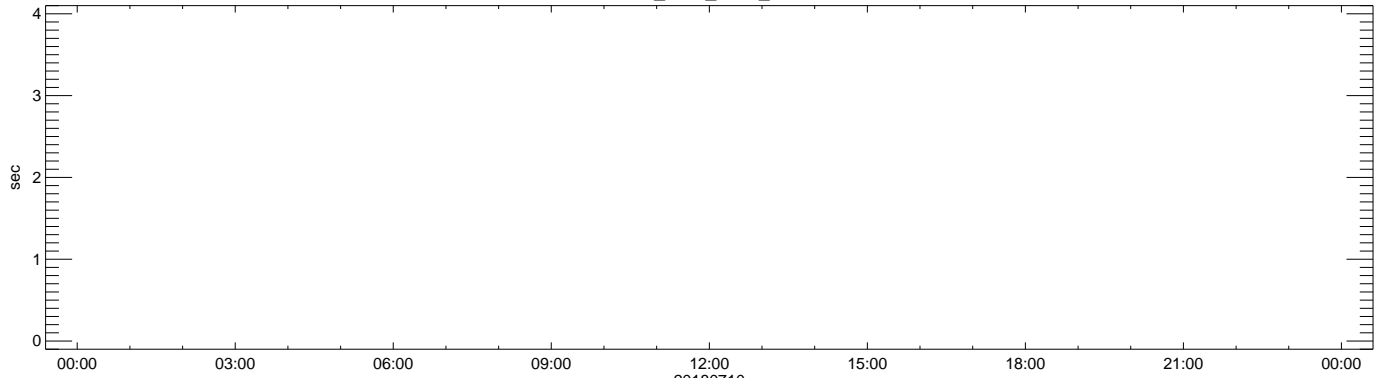
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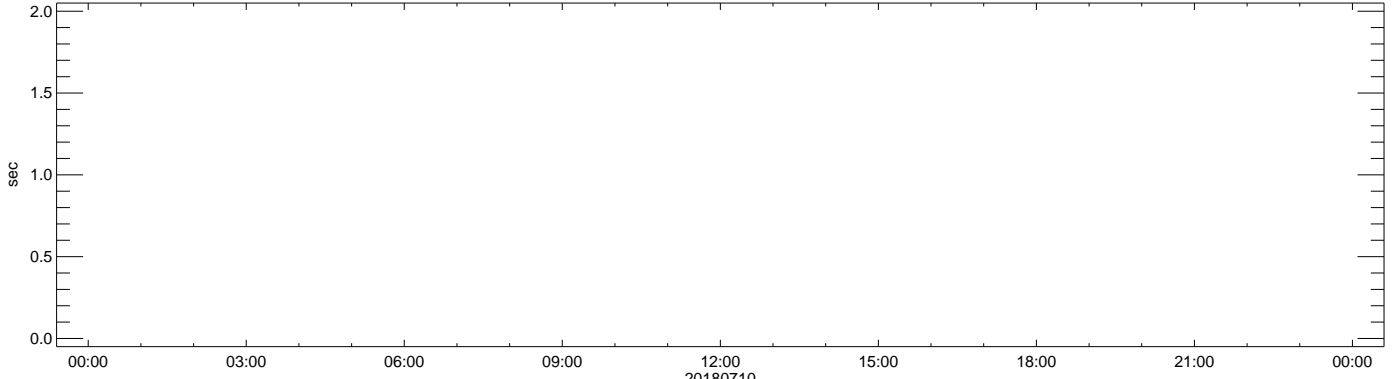
20180710
XRT_TEMP_DL Ver 2008/02/28
XRTE_EXP_STA_TIME



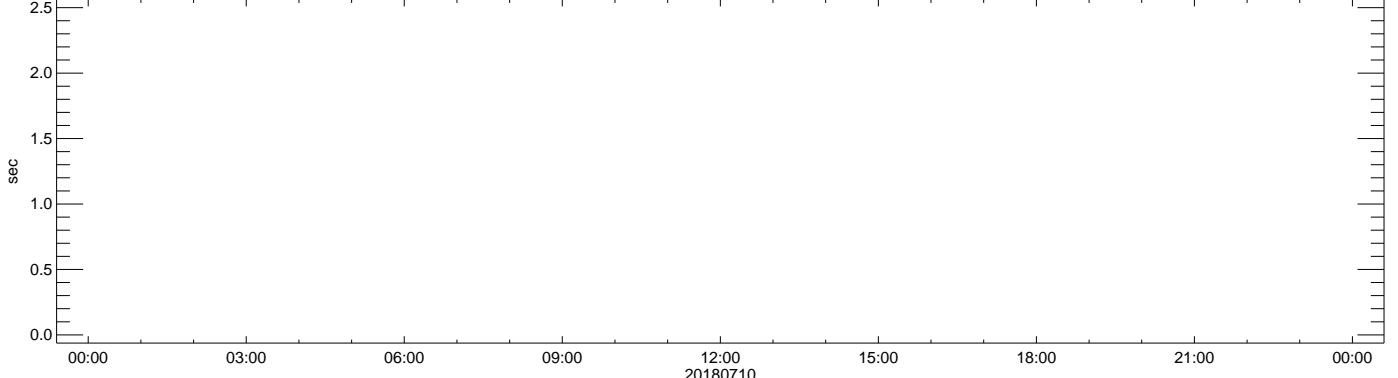
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XRTE_EXP_END_TIME



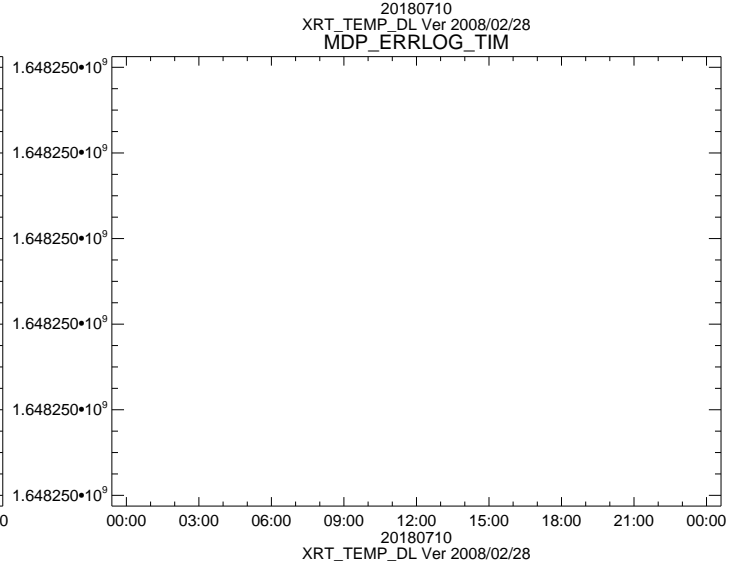
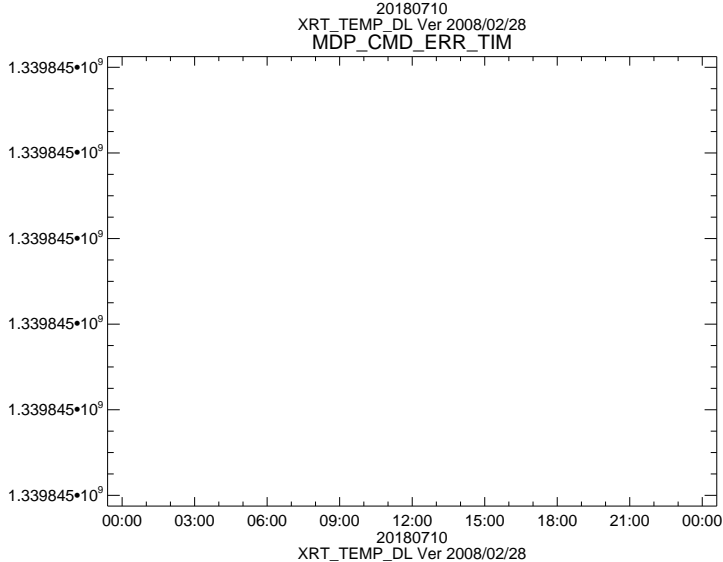
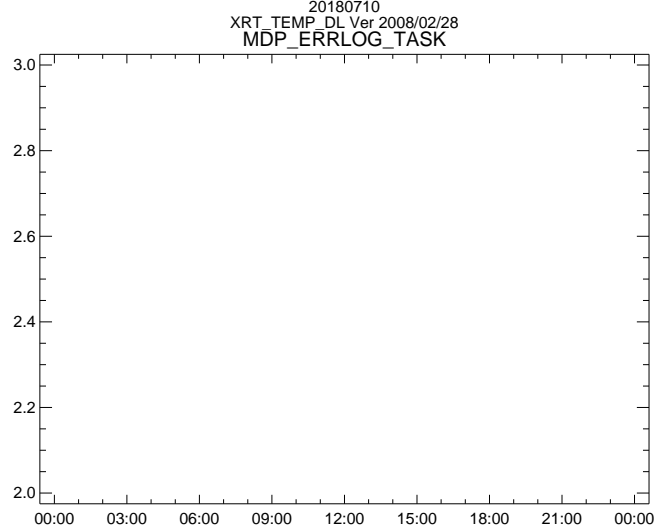
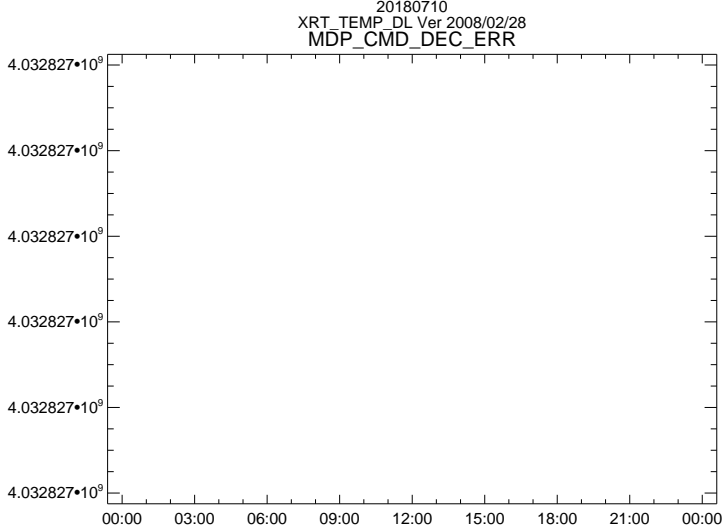
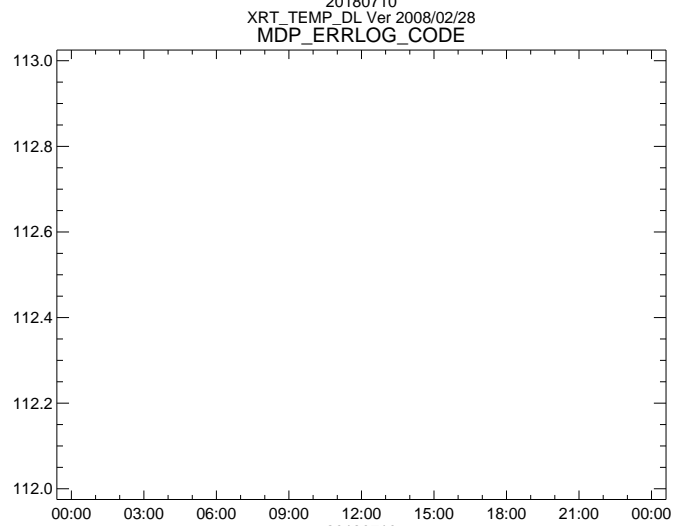
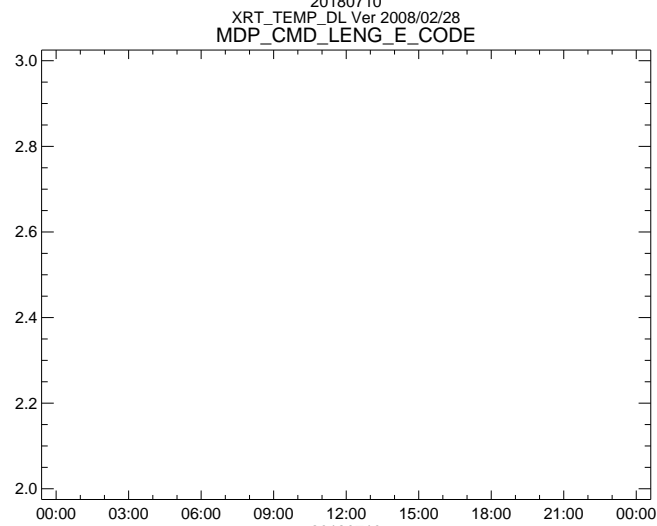
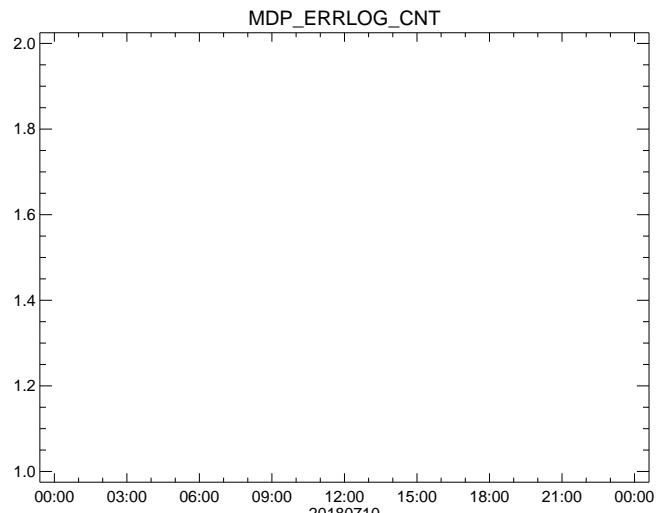
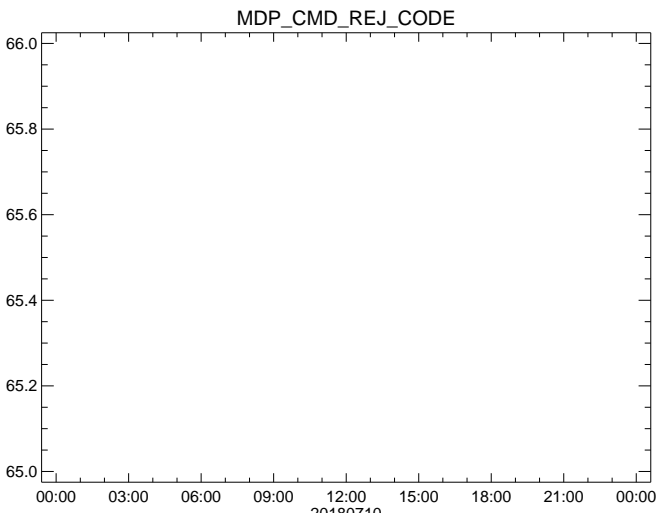
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XRTE_READ_END_TIME



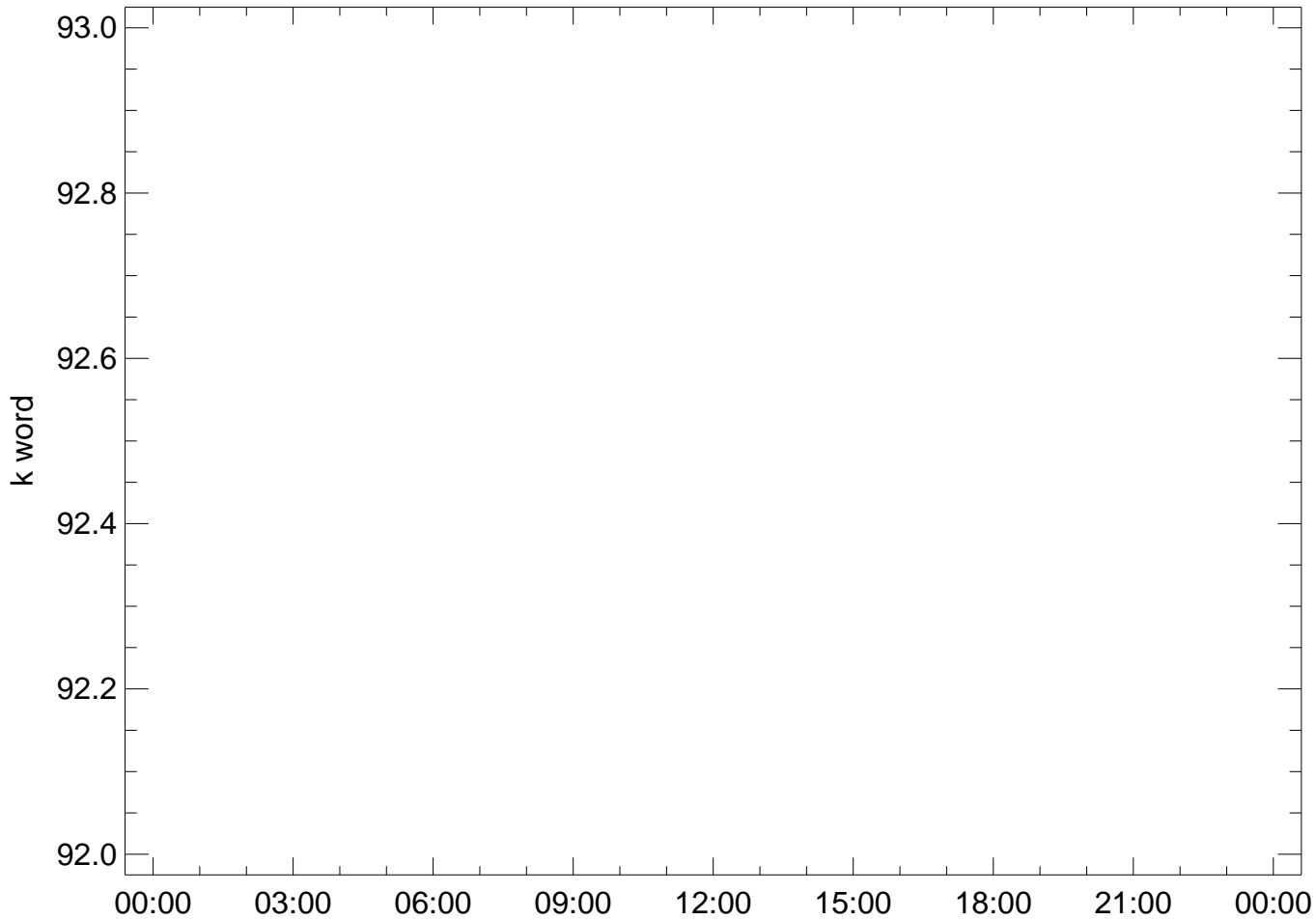
20180710
XRT_TEMP_DL Ver 2008/02/28
XRTE_TIMER_COUNTER



20180710
XRT_TEMP_DL Ver 2008/02/28

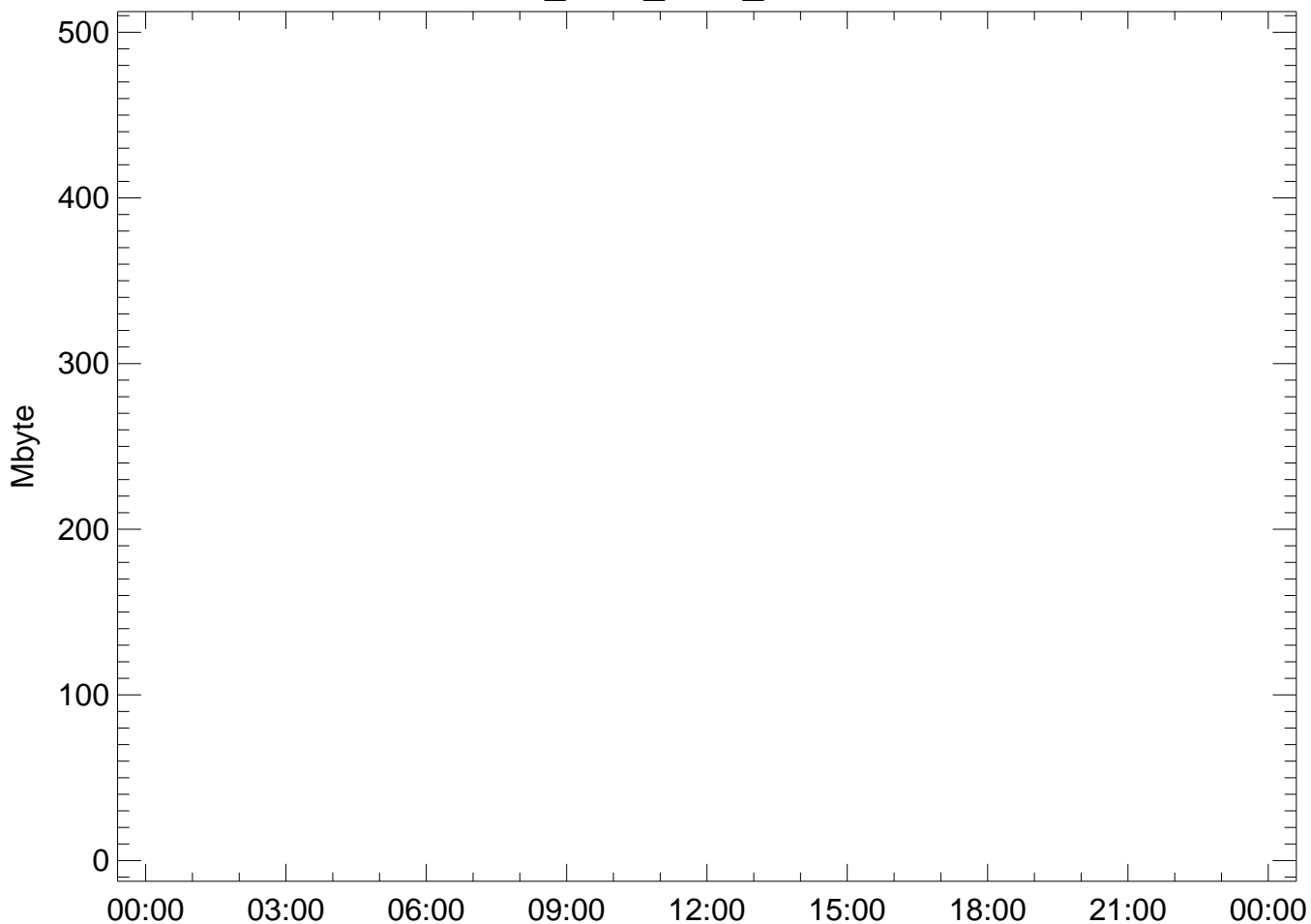


MDP_PST_CMP_XRT_ACM



20180710

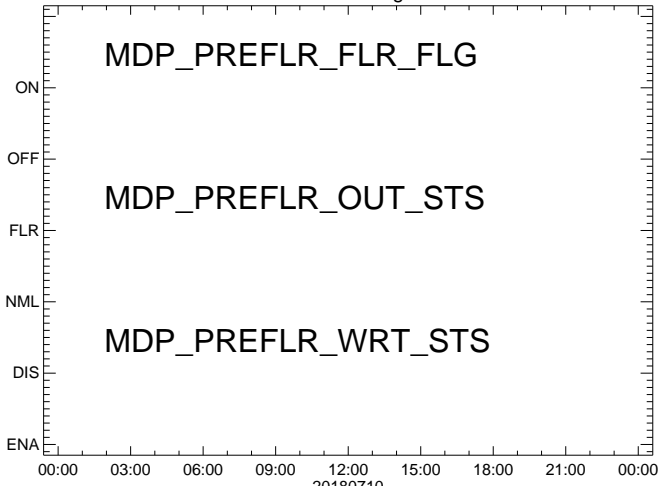
XRT_TEMP_DL Ver 2008/02/28 MDP_XRT_TLM_LIM & CNT



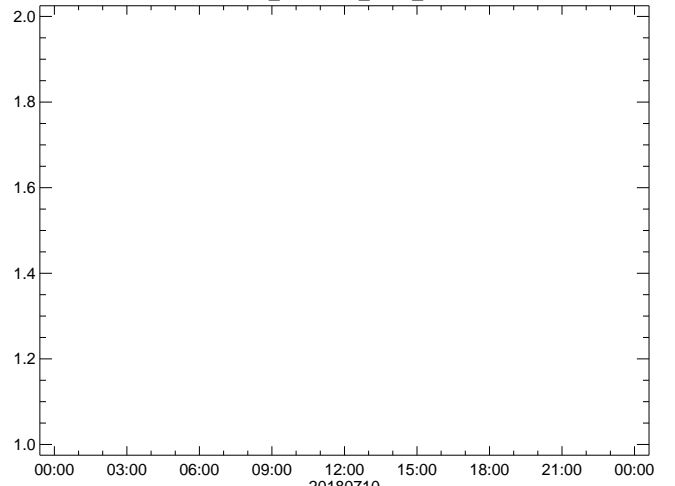
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XRT_TEMP_DL Ver 2008/02/28

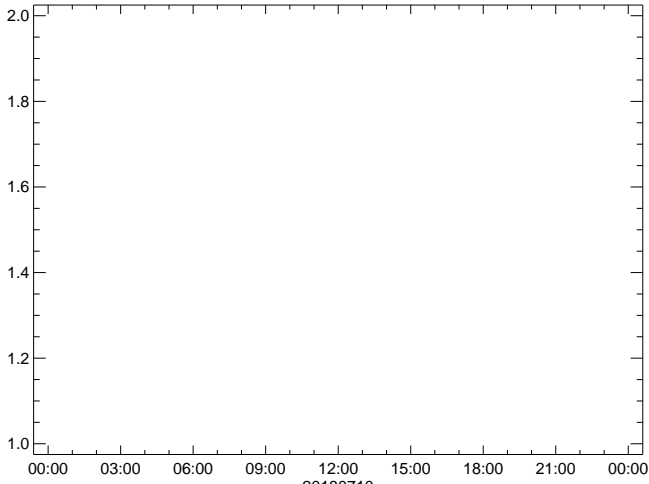
PreFlare Flags



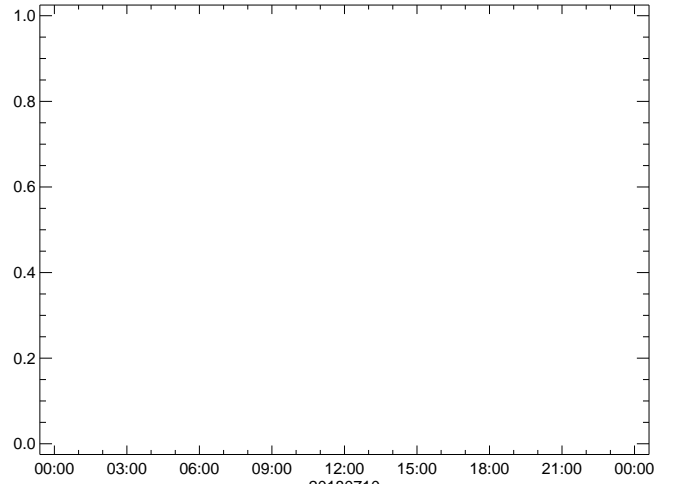
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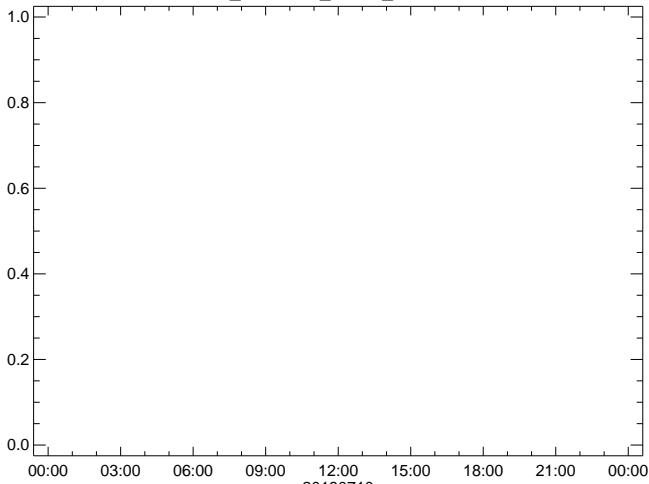
XRT_TEMP_DL Ver 2008/02/28
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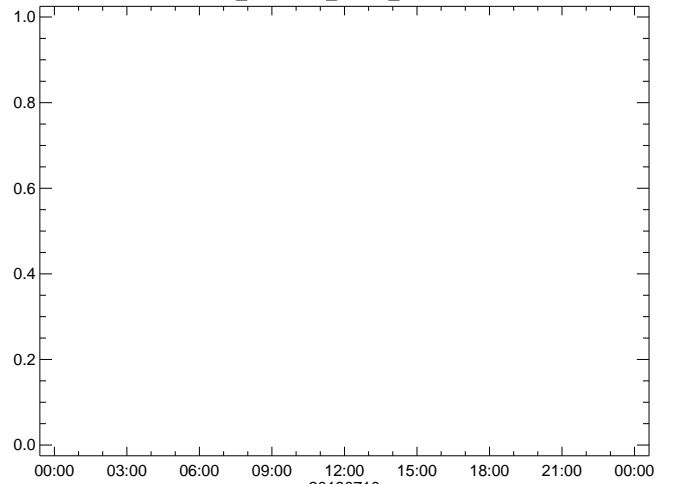
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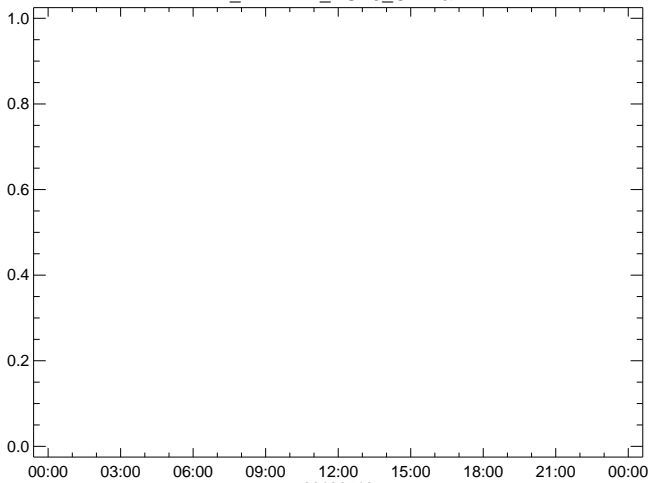
XRT_TEMP_DL Ver 2008/02/28
MDP_PREFLR_BUF1_CNT & WP



XRT_TEMP_DL Ver 2008/02/28
MDP_PREFLR_BUF2_CNT & WP



XRT_TEMP_DL Ver 2008/02/28
MDP_PREFLR_BUF3_CNT & WP



XRT_TEMP_DL Ver 2008/02/28

